

PROFINET

GSDML Description

Package Version 1.43 (20191122)

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1. About this document

Description for GSDML Files:

- GSDML-V2.1-BR-X20BC00E3-20191122
- GSDML-V2.25-BR-X20BC00E3-20191122
- GSDML-V2.1-BR-X20cBC00E3-20191122
- GSDML-V2.25-BR-X20cBC00E3-20191122
- GSDML-V2.1-BR-X67BCE321.L12-20191122
- GSDML-V2.25-BR-X67BCE321.L12-20191122

This document is an assistance for PROFINET engineering systems which are not able to use the datapoint description of the GSDML file. Each IO-module in the GSDML file will be documented here.

Each table for an IO-module contains the cyclic input-/ output-datapoints and acyclic configuration registers handled via the GSDML file. These registers can be configured in the used engineering tool. Furthermore, some IO-modules have additional configuration registers, not covered by the GSDML file. These registers can be manipulated by acyclic PROFINET communication functions (acyclic read/write).

Warning!!! Improper register configuration can cause device problems. It is therefore strongly recommended that these registers only be changed by experienced users.

The order of the datapoints is the same, as in the PROFINET-Frame. The name and type of the cyclic datapoints are mentioned in the tables below. The names of the acyclic registers can be checked in the data sheets (chapter "register description") of the corresponding IO-modules. Use the registeraddress (dec) in this document as reference to the registername in the corresponding IO-module data sheet.

Please bear in mind that due to some limitations in the bus controller, for some IO-modules only a subset of registers documented in the data sheet is implemented in the GSDML file. For some modules, different variants exist, marked with _Cx(e.g. X67DM1321.L08, X67DM1321.L08_C1, X67DM1321.L08_C2). These are hardware wise the same modules, differing only in the used function model or the IO- and configuration registers.

Example how to read this manual and especially how to derive the corresponding configuration registernames with aid of "GSDML registerdescription" and "IO-module data sheet".

4.1. X20AO2437

HwId: 0xB785 ← First four digits of the serial number printed on the IO module in hex

Functionmodel: 254

Description: 2 Current Outputs, electrically isolated

| Input | | |
|----------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogStatus01 | Unsigned8 | |
| AnalogStatus02 | Unsigned8 | |

| Output | | |
|----------------|-------|------------|
| Name | Input | Output |
| AnalogOutput01 | | Unsigned16 |
| AnalogOutput02 | | Unsigned16 |

| Acyclic registers | | |
|------------------------|--|-----------------------|
| Register address (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0182 | 386 | 2 |
| 0x0186 | 390 | 2 |
| 0x018A | 394 | 2 |
| 0x018E | 398 | 2 |

GSDML-registerdescription

Acyclic configuration register table

Cyclic IO register table for Inputs and Outputs

Depending on the configuration of the IO module, there can be different function models for one HW module

11.4 Function model 254 - Bus controller

| Register | Offset ¹⁾ | Name | Data type | Read | | Write | |
|--|----------------------|--|-----------|--------|------------|--------|------------|
| | | | | Cyclic | Non-cyclic | Cyclic | Non-cyclic |
| Analog signal - Configuration | | | | | | | |
| 386 | - | AnalogMode01 | UINT | | | | • |
| 394 | - | AnalogMode02 | UINT | | | | • |
| 390 | - | DACSlebrate01 | UINT | | | | • |
| 398 | - | DACSlebrate02 | UINT | | | | • |
| Analog signal - Communication | | | | | | | |
| 0 | 0 | AnalogOutput01 | (U)INT | | | • | |
| 2 | 2 | AnalogOutput02 | (U)INT | | | • | |
| 30 | - | AnalogStatus01 | USINT | | • | | |
| 31 | - | AnalogStatus02 | USINT | | • | | |
| | | OpenLineAnalogOutput01 or OpenLineAnalogOutput02 | Bit 2 | | | | |
| | | ConversionErrorAnalogOutput01 or ConversionErrorAnalogOutput02 | Bit 3 | | | | |
| | | IoSuppErrorAnalogOutput01 or IoSuppErrorAnalogOutput02 | Bit 7 | | | | |

I/O-module data sheet

X20(c)AO2437

2. ACOPOS

2.1. 8I64xxxxxxx.00X-1

HwId: 0xB0E4

Functionmodel: 0

Description: ACOPOSinverter

| Input | | |
|----------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| ETAD | Unsigned16 | |
| ETI | Unsigned16 | |
| RFRD | Integer16 | |
| OTR | Integer16 | |
| LCR | Unsigned16 | |
| THD | Unsigned16 | |
| THR | Unsigned16 | |
| IOLR_in | Integer16 | |
| ERRD | Unsigned16 | |
| LFT | Integer16 | |
| Output | | |
| Name | Input | Output |
| CMD | | Unsigned16 |
| CMI | | Unsigned16 |
| LFRD | | Integer16 |
| IOLR_out | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x232A | 9002 | 2 |
| 0x2107 | 8455 | 2 |
| 0x23F0 | 9200 | 2 |
| 0x2110 | 8464 | 2 |
| 0x2394 | 9108 | 2 |
| 0x2311 | 8977 | 2 |
| 0x231B | 8987 | 2 |
| 0x231C | 8988 | 2 |
| 0x2324 | 8996 | 2 |
| 0x2421 | 9249 | 2 |
| 0x2422 | 9250 | 2 |
| 0x2327 | 8999 | 2 |
| 0x2375 | 9077 | 2 |
| 0x2376 | 9078 | 2 |
| 0x213C | 8508 | 2 |
| 0x213B | 8507 | 2 |
| 0x23A8 | 9128 | 2 |
| 0x2393 | 9107 | 2 |
| 0x2395 | 9109 | 2 |
| 0x2396 | 9110 | 2 |
| 0x2398 | 9112 | 2 |
| 0x239A | 9114 | 2 |
| 0x2399 | 9113 | 2 |
| 0x2139 | 8505 | 2 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x213A | 8506 | 2 |
| 0x0604 | 1540 | 4 |
| 0x060C | 1548 | 4 |
| 0x0611 | 1553 | 4 |

3. Analog Input Module

3.1. X20AI1744

HwId: 0x1CDE

Functionmodel: 0

Description: 1 x DMS

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| StatusInput01 | Unsigned8 | |
| AnalogInput01 | Integer32 | |
| Output | | |
| Name | Input | Output |
| ConfigOutput01 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 2 |

3.1. X20AI1744_C1

HwId: 0x1CDE

Functionmodel: 1

Description: 1 x DMS

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| StatusInput01 | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| AnalogInput05 | Integer16 | |
| AnalogInput06 | Integer16 | |
| AnalogInput07 | Integer16 | |
| AnalogInput08 | Integer16 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0640 | 1600 | 1 |

3.1. X20AI1744-3

HwId: 0xA4EF

Functionmodel: 0

Description: 1 x DMS

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| StatusInput01 | Unsigned8 | |
| AnalogInput01 | Integer32 | |
| Output | | |
| Name | Input | Output |
| ConfigOutput01 | | Unsigned8 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

3.1. X20AI1744-3_C1

HwId: 0xA4EF

Functionmodel: 1

Description: 1 x DMS

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| StatusInput01 | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| AnalogInput05 | Integer16 | |
| AnalogInput06 | Integer16 | |
| AnalogInput07 | Integer16 | |
| AnalogInput08 | Integer16 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0640 | 1600 | 1 |

3.1. X20AI2222

HwId: 0xCAB0

Functionmodel: 0

Description: 2 Inputs ?10 V

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0014 | 20 | 2 |
| 0x0016 | 22 | 2 |

3.1. X20AI2237

HwId: 0xC9C4

Functionmodel: 254

Description: 2 Voltage Inputs, electrically isolated

| Input | | |
|-------------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01_Measured | Integer16 | |
| AnalogInput01_Evaluated | Integer16 | |
| Sampletime01 | Integer32 | |
| AnalogStatus01 | Unsigned8 | |
| AnalogInput02_Measured | Integer16 | |
| AnalogInput02_Evaluated | Integer16 | |
| Sampletime02 | Integer32 | |
| AnalogStatus02 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0182 | 386 | 2 |
| 0x01AE | 430 | 2 |
| 0x0186 | 390 | 2 |
| 0x01B2 | 434 | 2 |
| 0x0192 | 402 | 2 |
| 0x01BE | 446 | 2 |
| 0x018E | 398 | 2 |
| 0x01BA | 442 | 2 |
| 0x0196 | 406 | 2 |
| 0x01C2 | 450 | 2 |
| 0x01A2 | 418 | 2 |
| 0x01CE | 462 | 2 |
| 0x01A6 | 422 | 2 |
| 0x01D2 | 466 | 2 |
| 0x019E | 414 | 2 |
| 0x01CA | 458 | 2 |
| 0x019A | 410 | 2 |
| 0x01C6 | 454 | 2 |

3.1. X20AI2322

HwId: 0xCAB2

Functionmodel: 0

Description: 2 Inputs 0 to 20 mA

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |
| 0x0014 | 20 | 2 |
| 0x0016 | 22 | 2 |

3.1. X20AI2437

HwId: 0xB784

Functionmodel: 254

Description: 2 Inputs 0 to 25 mA, 16 bit, electr. isolated

| Input | | |
|-----------------------|------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Unsigned16 | |
| AnalogInput02 | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0182 | 386 | 2 |
| 0x0186 | 390 | 2 |
| 0x01A2 | 418 | 2 |
| 0x01A6 | 422 | 2 |
| 0x01AA | 426 | 2 |
| 0x01AE | 430 | 2 |
| 0x01CA | 458 | 2 |
| 0x01CE | 462 | 2 |
| 0x018E | 398 | 2 |
| 0x0192 | 402 | 2 |
| 0x01B6 | 438 | 2 |
| 0x01BA | 442 | 2 |

3.1. X20AI2438

HwId: 0xB3A9

Functionmodel: 254

Description: 2 Current Inputs, HART Master

| Input | | |
|-----------------------|------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Unsigned16 | |
| AnalogInput02 | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0182 | 386 | 2 |
| 0x0186 | 390 | 2 |
| 0x01A2 | 418 | 2 |
| 0x01A6 | 422 | 2 |
| 0x01AA | 426 | 2 |
| 0x01AE | 430 | 2 |
| 0x01CA | 458 | 2 |
| 0x01CE | 462 | 2 |
| 0x018E | 398 | 2 |
| 0x0192 | 402 | 2 |
| 0x01B6 | 438 | 2 |
| 0x01BA | 442 | 2 |

3.1. X20AI2622

HwId: 0x1B9E

Functionmodel: 0

Description: 2 Inputs ?10 V / 0 to 20 mA

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |
| 0x0014 | 20 | 2 |
| 0x0016 | 22 | 2 |

3.1. X20AI2632

HwId: 0x1BA0

Functionmodel: 0

Description: 2 Inputs ± 10 V / 0 to 20 mA

| Input | | |
|-----------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| Inputs_X20AI2632_a | Unsigned8 | |
| | Bit 0 | Channel01OK |
| | Bit 1 | Channel02OK |
| | Bit 6 | SyncStatus |
| | Bit 7 | ConversionCycle |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0186 | 390 | 2 |
| 0x0101 | 257 | 1 |
| 0x0103 | 259 | 1 |
| 0x0106 | 262 | 2 |
| 0x010A | 266 | 2 |
| 0x010E | 270 | 2 |
| 0x0114 | 276 | 4 |
| 0x011C | 284 | 4 |
| 0x0121 | 289 | 1 |
| 0x0123 | 291 | 1 |
| 0x0126 | 294 | 2 |
| 0x012A | 298 | 2 |
| 0x012E | 302 | 2 |
| 0x0134 | 308 | 4 |
| 0x013C | 316 | 4 |

3.1. X20AI2632-1

HwId: 0xA29E

Functionmodel: 0

Description: 2 Inputs ± 11 V / 0 to 22 mA, 16 bit

| Input | | |
|-----------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| Inputs_X20AI2632-1_a | Unsigned8 | |
| | Bit 0 | Channel01OK |
| | Bit 1 | Channel02OK |
| | Bit 6 | SyncStatus |
| | Bit 7 | ConversionCycle |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0186 | 390 | 2 |
| 0x0101 | 257 | 1 |
| 0x0103 | 259 | 1 |
| 0x0106 | 262 | 2 |
| 0x010A | 266 | 2 |
| 0x010E | 270 | 2 |
| 0x0114 | 276 | 4 |
| 0x011C | 284 | 4 |
| 0x0121 | 289 | 1 |
| 0x0123 | 291 | 1 |
| 0x0126 | 294 | 2 |
| 0x012A | 298 | 2 |
| 0x012E | 302 | 2 |
| 0x0134 | 308 | 4 |
| 0x013C | 316 | 4 |

3.1. X20AI4222

HwId: 0xCAB1

Functionmodel: 0

Description: 4 Inputs ?10 V

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0014 | 20 | 2 |
| 0x0016 | 22 | 2 |

3.1. X20AI4322

HwId: 0xCAB3

Functionmodel: 0

Description: 4 Inputs 0 to 20 mA

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |
| 0x0014 | 20 | 2 |
| 0x0016 | 22 | 2 |

3.1. X20AI4622

HwId: 0x1BAA

Functionmodel: 0

Description: 4 Inputs ?10 V / 0 to 20 mA

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |
| 0x0014 | 20 | 2 |
| 0x0016 | 22 | 2 |

3.1. X20AI4632

HwId: 0x1BA1

Functionmodel: 0

Description: 4 Inputs ± 10 V / 0 to 20 mA

| Input | | |
|-----------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| Inputs_X20AI4632_a | Unsigned8 | |
| | Bit 0 | Channel01OK |
| | Bit 1 | Channel02OK |
| | Bit 2 | Channel03OK |
| | Bit 3 | Channel04OK |
| | Bit 6 | SyncStatus |
| | Bit 7 | ConversionCycle |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0186 | 390 | 2 |
| 0x0101 | 257 | 1 |
| 0x0103 | 259 | 1 |
| 0x0106 | 262 | 2 |
| 0x010A | 266 | 2 |
| 0x010E | 270 | 2 |
| 0x0114 | 276 | 4 |
| 0x011C | 284 | 4 |
| 0x0121 | 289 | 1 |
| 0x0123 | 291 | 1 |
| 0x0126 | 294 | 2 |
| 0x012A | 298 | 2 |
| 0x012E | 302 | 2 |
| 0x0134 | 308 | 4 |
| 0x013C | 316 | 4 |
| 0x0141 | 321 | 1 |
| 0x0143 | 323 | 1 |
| 0x0146 | 326 | 2 |
| 0x014A | 330 | 2 |
| 0x014E | 334 | 2 |
| 0x0154 | 340 | 4 |
| 0x015C | 348 | 4 |
| 0x0161 | 353 | 1 |
| 0x0163 | 355 | 1 |
| 0x0166 | 358 | 2 |
| 0x016A | 362 | 2 |
| 0x016E | 366 | 2 |
| 0x0174 | 372 | 4 |
| 0x017C | 380 | 4 |

3.1. X20AI4632-1

HwId: 0xA29D

Functionmodel: 0

Description: 4 Inputs ± 11 V / 0 to 22 mA

| Input | | |
|-----------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| Inputs_X20AI4632-1_a | Unsigned8 | |
| | Bit 0 | Channel01OK |
| | Bit 1 | Channel02OK |
| | Bit 2 | Channel03OK |
| | Bit 3 | Channel04OK |
| | Bit 6 | SyncStatus |
| | Bit 7 | ConversionCycle |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0186 | 390 | 2 |
| 0x0101 | 257 | 1 |
| 0x0103 | 259 | 1 |
| 0x0106 | 262 | 2 |
| 0x010A | 266 | 2 |
| 0x010E | 270 | 2 |
| 0x0114 | 276 | 4 |
| 0x011C | 284 | 4 |
| 0x0121 | 289 | 1 |
| 0x0123 | 291 | 1 |
| 0x0126 | 294 | 2 |
| 0x012A | 298 | 2 |
| 0x012E | 302 | 2 |
| 0x0134 | 308 | 4 |
| 0x013C | 316 | 4 |
| 0x0141 | 321 | 1 |
| 0x0143 | 323 | 1 |
| 0x0146 | 326 | 2 |
| 0x014A | 330 | 2 |
| 0x014E | 334 | 2 |
| 0x0154 | 340 | 4 |
| 0x015C | 348 | 4 |
| 0x0161 | 353 | 1 |
| 0x0163 | 355 | 1 |
| 0x0166 | 358 | 2 |
| 0x016A | 362 | 2 |
| 0x016E | 366 | 2 |
| 0x0174 | 372 | 4 |
| 0x017C | 380 | 4 |

3.1. X20AI8221

HwId: 0xD82F

Functionmodel: 0

Description: 8 Inputs ?10 V

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| AnalogInput05 | Integer16 | |
| AnalogInput06 | Integer16 | |
| AnalogInput07 | Integer16 | |
| AnalogInput08 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| StatusInput02 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 2 |
| 0x0014 | 20 | 2 |
| 0x0016 | 22 | 2 |

3.1. X20AI8321

HwId: 0xD831

Functionmodel: 0

Description: 8 Inputs 0 to 20 mA

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| AnalogInput05 | Integer16 | |
| AnalogInput06 | Integer16 | |
| AnalogInput07 | Integer16 | |
| AnalogInput08 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| StatusInput02 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 2 |
| 0x0012 | 18 | 2 |
| 0x0014 | 20 | 2 |
| 0x0016 | 22 | 2 |

3.1. X20AIA744

HwId: 0xE50C

Functionmodel: 0

Description: 2x DMS

| Input | | |
|--------------------|-----------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer32 | |
| AnalogInput02 | Integer32 | |
| StatusPacked01 | Unsigned8 | |
| StatusPacked02 | Unsigned8 | |
| AdcConvCtr01 | Integer8 | |
| AdcConvTimeStamp01 | Integer32 | |
| Output | | |
| Name | Input | Output |
| ControlPacked01 | | Unsigned16 |
| ControlPacked02 | | Unsigned16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0202 | 514 | 2 |
| 0x0242 | 578 | 2 |

3.1. X20AIB744

HwId: 0xE286

Functionmodel: 0

Description: 4x DMS

| Input | | |
|--------------------|-----------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer32 | |
| AnalogInput02 | Integer32 | |
| AnalogInput03 | Integer32 | |
| AnalogInput04 | Integer32 | |
| StatusPacked01 | Unsigned8 | |
| StatusPacked02 | Unsigned8 | |
| StatusPacked03 | Unsigned8 | |
| StatusPacked04 | Unsigned8 | |
| AdcConvCtr01 | Integer8 | |
| AdcConvTimeStamp01 | Integer32 | |
| Output | | |
| Name | Input | Output |
| ControlPacked01 | | Unsigned16 |
| ControlPacked02 | | Unsigned16 |
| ControlPacked03 | | Unsigned16 |
| ControlPacked04 | | Unsigned16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0202 | 514 | 2 |
| 0x0242 | 578 | 2 |
| 0x0282 | 642 | 2 |
| 0x02C2 | 706 | 2 |

3.1. X20AP3111

HwId: 0xC9DA

Functionmodel: 254

Description: Power measurement 3 Inputs 480 VAC, 20 mA

| Input | | |
|-----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| StatusInput01 | Unsigned16 | |
| SysStatus00 | Unsigned16 | |
| SysStatus01 | Unsigned16 | |
| PmeanT | Integer16 | |
| QmeanT | Integer16 | |
| SAmeanT | Integer16 | |
| AEnergyT | Integer32 | |
| REnergyT | Integer32 | |
| InputSequence | Unsigned8 | |
| RxByte01 | Unsigned8 | |
| RxByte02 | Unsigned8 | |
| RxByte03 | Unsigned8 | |
| RxByte04 | Unsigned8 | |
| RxByte05 | Unsigned8 | |
| RxByte06 | Unsigned8 | |
| RxByte07 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| ControlOutput01 | | Unsigned16 |
| OutputSequence | | Unsigned8 |
| TxByte01 | | Unsigned8 |
| TxByte02 | | Unsigned8 |
| TxByte03 | | Unsigned8 |
| TxByte04 | | Unsigned8 |
| TxByte05 | | Unsigned8 |
| TxByte06 | | Unsigned8 |
| TxByte07 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x041E | 1054 | 2 |
| 0x041A | 1050 | 2 |
| 0x048A | 1162 | 2 |
| 0x0402 | 1026 | 2 |
| 0x0442 | 1090 | 2 |
| 0x040A | 1034 | 2 |
| 0x040E | 1038 | 2 |
| 0x0412 | 1042 | 2 |
| 0x0416 | 1046 | 2 |
| 0x0446 | 1094 | 2 |
| 0x044A | 1098 | 2 |

3.1. X20AP3111_C1

HwId: 0xC9DA

Functionmodel: 254

Description: Power measurement 3 Inputs 480 VAC, 20 mA

| Input | | |
|-----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| UrmsA | Unsigned16 | |
| UrmsB | Unsigned16 | |
| UrmsC | Unsigned16 | |
| IrmsA | Unsigned16 | |
| IrmsB | Unsigned16 | |
| IrmsC | Unsigned16 | |
| PmeanT | Integer16 | |
| QmeanT | Integer16 | |
| SAmeanT | Integer16 | |
| PFmeanT | Integer16 | |
| Freq | Unsigned16 | |
| InputSequence | Unsigned8 | |
| RxByte01 | Unsigned8 | |
| RxByte02 | Unsigned8 | |
| RxByte03 | Unsigned8 | |
| RxByte04 | Unsigned8 | |
| RxByte05 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| ControlOutput01 | | Unsigned16 |
| OutputSequence | | Unsigned8 |
| TxByte01 | | Unsigned8 |
| TxByte02 | | Unsigned8 |
| TxByte03 | | Unsigned8 |
| TxByte04 | | Unsigned8 |
| TxByte05 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x041E | 1054 | 2 |
| 0x041A | 1050 | 2 |
| 0x048A | 1162 | 2 |
| 0x0402 | 1026 | 2 |
| 0x0442 | 1090 | 2 |
| 0x040A | 1034 | 2 |
| 0x040E | 1038 | 2 |
| 0x0412 | 1042 | 2 |
| 0x0416 | 1046 | 2 |
| 0x0446 | 1094 | 2 |
| 0x044A | 1098 | 2 |
| 0x0801 | 2049 | 1 |
| 0x0803 | 2051 | 1 |

3.1. X20AP3121

HwId: 0xC9DB

Functionmodel: 254

Description: Power measurement 3 Inputs 480 VAC, 1 A

| Input | | |
|-----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| StatusInput01 | Unsigned16 | |
| SysStatus00 | Unsigned16 | |
| SysStatus01 | Unsigned16 | |
| PmeanT | Integer16 | |
| QmeanT | Integer16 | |
| SAmeanT | Integer16 | |
| AEnergyT | Integer32 | |
| REnergyT | Integer32 | |
| InputSequence | Unsigned8 | |
| RxByte01 | Unsigned8 | |
| RxByte02 | Unsigned8 | |
| RxByte03 | Unsigned8 | |
| RxByte04 | Unsigned8 | |
| RxByte05 | Unsigned8 | |
| RxByte06 | Unsigned8 | |
| RxByte07 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| ControlOutput01 | | Unsigned16 |
| OutputSequence | | Unsigned8 |
| TxByte01 | | Unsigned8 |
| TxByte02 | | Unsigned8 |
| TxByte03 | | Unsigned8 |
| TxByte04 | | Unsigned8 |
| TxByte05 | | Unsigned8 |
| TxByte06 | | Unsigned8 |
| TxByte07 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x041E | 1054 | 2 |
| 0x041A | 1050 | 2 |
| 0x048A | 1162 | 2 |
| 0x0402 | 1026 | 2 |
| 0x0442 | 1090 | 2 |
| 0x040A | 1034 | 2 |
| 0x040E | 1038 | 2 |
| 0x0412 | 1042 | 2 |
| 0x0416 | 1046 | 2 |
| 0x0446 | 1094 | 2 |
| 0x044A | 1098 | 2 |

3.1. X20AP3121_C1

HwId: 0xC9DB

Functionmodel: 254

Description: Power measurement 3 Inputs 480 VAC, 1 A

| Input | | |
|-----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| UrmsA | Unsigned16 | |
| UrmsB | Unsigned16 | |
| UrmsC | Unsigned16 | |
| IrmsA | Unsigned16 | |
| IrmsB | Unsigned16 | |
| IrmsC | Unsigned16 | |
| PmeanT | Integer16 | |
| QmeanT | Integer16 | |
| SAmeanT | Integer16 | |
| PFmeanT | Integer16 | |
| Freq | Unsigned16 | |
| InputSequence | Unsigned8 | |
| RxByte01 | Unsigned8 | |
| RxByte02 | Unsigned8 | |
| RxByte03 | Unsigned8 | |
| RxByte04 | Unsigned8 | |
| RxByte05 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| ControlOutput01 | | Unsigned16 |
| OutputSequence | | Unsigned8 |
| TxByte01 | | Unsigned8 |
| TxByte02 | | Unsigned8 |
| TxByte03 | | Unsigned8 |
| TxByte04 | | Unsigned8 |
| TxByte05 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x041E | 1054 | 2 |
| 0x041A | 1050 | 2 |
| 0x048A | 1162 | 2 |
| 0x0402 | 1026 | 2 |
| 0x0442 | 1090 | 2 |
| 0x040A | 1034 | 2 |
| 0x040E | 1038 | 2 |
| 0x0412 | 1042 | 2 |
| 0x0416 | 1046 | 2 |
| 0x0446 | 1094 | 2 |
| 0x044A | 1098 | 2 |
| 0x0801 | 2049 | 1 |
| 0x0803 | 2051 | 1 |

3.1. X20AP3131

HwId: 0xC9DC

Functionmodel: 254

Description: Power measurement 3 Inputs 480 VAC, 5 A

| Input | | |
|-----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| StatusInput01 | Unsigned16 | |
| SysStatus00 | Unsigned16 | |
| SysStatus01 | Unsigned16 | |
| PmeanT | Integer16 | |
| QmeanT | Integer16 | |
| SAmeanT | Integer16 | |
| AEnergyT | Integer32 | |
| REnergyT | Integer32 | |
| InputSequence | Unsigned8 | |
| RxByte01 | Unsigned8 | |
| RxByte02 | Unsigned8 | |
| RxByte03 | Unsigned8 | |
| RxByte04 | Unsigned8 | |
| RxByte05 | Unsigned8 | |
| RxByte06 | Unsigned8 | |
| RxByte07 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| ControlOutput01 | | Unsigned16 |
| OutputSequence | | Unsigned8 |
| TxByte01 | | Unsigned8 |
| TxByte02 | | Unsigned8 |
| TxByte03 | | Unsigned8 |
| TxByte04 | | Unsigned8 |
| TxByte05 | | Unsigned8 |
| TxByte06 | | Unsigned8 |
| TxByte07 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x041E | 1054 | 2 |
| 0x041A | 1050 | 2 |
| 0x048A | 1162 | 2 |
| 0x0402 | 1026 | 2 |
| 0x0442 | 1090 | 2 |
| 0x040A | 1034 | 2 |
| 0x040E | 1038 | 2 |
| 0x0412 | 1042 | 2 |
| 0x0416 | 1046 | 2 |
| 0x0446 | 1094 | 2 |
| 0x044A | 1098 | 2 |

3.1. X20AP3131_C1

HwId: 0xC9DC

Functionmodel: 254

Description: Power measurement 3 Inputs 480 VAC, 5 A

| Input | | |
|-----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| UrmsA | Unsigned16 | |
| UrmsB | Unsigned16 | |
| UrmsC | Unsigned16 | |
| IrmsA | Unsigned16 | |
| IrmsB | Unsigned16 | |
| IrmsC | Unsigned16 | |
| PmeanT | Integer16 | |
| QmeanT | Integer16 | |
| SAmeanT | Integer16 | |
| PFmeanT | Integer16 | |
| Freq | Unsigned16 | |
| InputSequence | Unsigned8 | |
| RxByte01 | Unsigned8 | |
| RxByte02 | Unsigned8 | |
| RxByte03 | Unsigned8 | |
| RxByte04 | Unsigned8 | |
| RxByte05 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| ControlOutput01 | | Unsigned16 |
| OutputSequence | | Unsigned8 |
| TxByte01 | | Unsigned8 |
| TxByte02 | | Unsigned8 |
| TxByte03 | | Unsigned8 |
| TxByte04 | | Unsigned8 |
| TxByte05 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x041E | 1054 | 2 |
| 0x041A | 1050 | 2 |
| 0x048A | 1162 | 2 |
| 0x0402 | 1026 | 2 |
| 0x0442 | 1090 | 2 |
| 0x040A | 1034 | 2 |
| 0x040E | 1038 | 2 |
| 0x0412 | 1042 | 2 |
| 0x0416 | 1046 | 2 |
| 0x0446 | 1094 | 2 |
| 0x044A | 1098 | 2 |
| 0x0801 | 2049 | 1 |
| 0x0803 | 2051 | 1 |

3.1. X20AP3161

HwId: 0xE17B

Functionmodel: 254

Description: Power measurement 3 Inputs 480 VAC, 4 Inputs 333 mVAC

| Input | | |
|-----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| StatusInput01 | Unsigned16 | |
| SysStatus00 | Unsigned16 | |
| SysStatus01 | Unsigned16 | |
| PmeanT | Integer16 | |
| QmeanT | Integer16 | |
| SAmeanT | Integer16 | |
| AEnergyT | Integer32 | |
| REnergyT | Integer32 | |
| InputSequence | Unsigned8 | |
| RxByte01 | Unsigned8 | |
| RxByte02 | Unsigned8 | |
| RxByte03 | Unsigned8 | |
| RxByte04 | Unsigned8 | |
| RxByte05 | Unsigned8 | |
| RxByte06 | Unsigned8 | |
| RxByte07 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| ControlOutput01 | | Unsigned16 |
| OutputSequence | | Unsigned8 |
| TxByte01 | | Unsigned8 |
| TxByte02 | | Unsigned8 |
| TxByte03 | | Unsigned8 |
| TxByte04 | | Unsigned8 |
| TxByte05 | | Unsigned8 |
| TxByte06 | | Unsigned8 |
| TxByte07 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x041E | 1054 | 2 |
| 0x041A | 1050 | 2 |
| 0x048A | 1162 | 2 |
| 0x0402 | 1026 | 2 |
| 0x0442 | 1090 | 2 |
| 0x040A | 1034 | 2 |
| 0x040E | 1038 | 2 |
| 0x0412 | 1042 | 2 |
| 0x0416 | 1046 | 2 |
| 0x0446 | 1094 | 2 |
| 0x044A | 1098 | 2 |

3.1. X20AP3161_C1

HwId: 0xE17B

Functionmodel: 254

Description: Power measurement 3 Inputs 480 VAC, 4 Inputs 333 mVAC

| Input | | |
|-----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| UrmsA | Unsigned16 | |
| UrmsB | Unsigned16 | |
| UrmsC | Unsigned16 | |
| IrmsA | Unsigned16 | |
| IrmsB | Unsigned16 | |
| IrmsC | Unsigned16 | |
| PmeanT | Integer16 | |
| QmeanT | Integer16 | |
| SAmeanT | Integer16 | |
| PFmeanT | Integer16 | |
| Freq | Unsigned16 | |
| InputSequence | Unsigned8 | |
| RxByte01 | Unsigned8 | |
| RxByte02 | Unsigned8 | |
| RxByte03 | Unsigned8 | |
| RxByte04 | Unsigned8 | |
| RxByte05 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| ControlOutput01 | | Unsigned16 |
| OutputSequence | | Unsigned8 |
| TxByte01 | | Unsigned8 |
| TxByte02 | | Unsigned8 |
| TxByte03 | | Unsigned8 |
| TxByte04 | | Unsigned8 |
| TxByte05 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x041E | 1054 | 2 |
| 0x041A | 1050 | 2 |
| 0x048A | 1162 | 2 |
| 0x0402 | 1026 | 2 |
| 0x0442 | 1090 | 2 |
| 0x040A | 1034 | 2 |
| 0x040E | 1038 | 2 |
| 0x0412 | 1042 | 2 |
| 0x0416 | 1046 | 2 |
| 0x0446 | 1094 | 2 |
| 0x044A | 1098 | 2 |
| 0x0801 | 2049 | 1 |
| 0x0803 | 2051 | 1 |

3.1. X20AT2222

HwId: 0x1BA6

Functionmodel: 0

Description: 2 Resistor Temperature Inputs

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |

3.1. X20AT2222_C1

HwId: 0x1BA6

Functionmodel: 1

Description: 2 Resistor Temperature Inputs

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |

3.1. X20AT2311

HwId: 0xA4AA

Functionmodel: 0

Description: 2 Resistor Temperature Inputs, PT100, 4-wire

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer32 | |
| Temperature02 | Integer32 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0801 | 2049 | 1 |
| 0x0803 | 2051 | 1 |

3.1. X20AT2402

HwId: 0x1BA8

Functionmodel: 0

Description: 2 Temperature Sensor Inputs

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 1 |
| 0x001B | 27 | 1 |

3.1. X20AT2402_C1

HwId: 0x1BA8

Functionmodel: 1

Description: 2 Temperature Sensor Inputs

| Input | | |
|---------------------------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| ExternalCompensationTemperature | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 1 |
| 0x001B | 27 | 1 |

3.1. X20AT4222

HwId: 0x1BA7

Functionmodel: 0

Description: 4 Resistor Temperature Inputs

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| Temperature03 | Integer16 | |
| Temperature04 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 2 |

3.1. X20AT4222_C1

HwId: 0x1BA7

Functionmodel: 1

Description: 4 Resistor Temperature Inputs

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| Temperature03 | Integer16 | |
| Temperature04 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 2 |

3.1. X20AT6402

HwId: 0x1BA9

Functionmodel: 0

Description: 6 Temperature Sensor Inputs

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| Temperature03 | Integer16 | |
| Temperature04 | Integer16 | |
| Temperature05 | Integer16 | |
| Temperature06 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| StatusInput02 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 1 |
| 0x001B | 27 | 1 |

3.1. X20AT6402_C1

HwId: 0x1BA9

Functionmodel: 1

Description: 6 Temperature Sensor Inputs

| Input | | |
|---------------------------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| Temperature03 | Integer16 | |
| Temperature04 | Integer16 | |
| Temperature05 | Integer16 | |
| Temperature06 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| StatusInput02 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| ExternalCompensationTemperature | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 1 |
| 0x001B | 27 | 1 |

3.1. X20ATA312

HwId: 0xE0E4

Functionmodel: 254

Description: 2 Resistor Temperature Inputs, PT100, 4-wire

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer32 | |
| Temperature02 | Integer32 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0082 | 130 | 2 |
| 0x0202 | 514 | 2 |
| 0x0242 | 578 | 2 |

3.1. X20ATA492

HwId: 0xBB98

Functionmodel: 0

Description: 2 Temperature Sensor Inputs, electr. isolated

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| StatusInput02 | Unsigned8 | |
| CompensationStatus01 | Unsigned8 | |
| CompensationStatus02 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0186 | 390 | 2 |
| 0x0182 | 386 | 2 |
| 0x019E | 414 | 2 |
| 0x01A2 | 418 | 2 |
| 0x01AE | 430 | 2 |
| 0x01AA | 426 | 2 |
| 0x01C6 | 454 | 2 |
| 0x01CA | 458 | 2 |

3.1. X20ATB312

HwId: 0xE0EF

Functionmodel: 254

Description: 4 Resistor Temperature Inputs, PT100, 4-wire

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer32 | |
| Temperature02 | Integer32 | |
| Temperature03 | Integer32 | |
| Temperature04 | Integer32 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0082 | 130 | 2 |
| 0x0202 | 514 | 2 |
| 0x0242 | 578 | 2 |
| 0x0282 | 642 | 2 |
| 0x02C2 | 706 | 2 |

3.1. X20ATC402

HwId: 0xBB99

Functionmodel: 254

Description: 6 Thermoelement Inputs

| Input | | |
|------------------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| CompensationValueA | Integer16 | |
| CompensationValueB | Integer16 | |
| CompensationStatusA | Unsigned8 | |
| CompensationStatusB | Unsigned8 | |
| TemperatureEvaluated01 | Integer16 | |
| TemperatureEvaluated02 | Integer16 | |
| TemperatureEvaluated03 | Integer16 | |
| TemperatureEvaluated04 | Integer16 | |
| TemperatureEvaluated05 | Integer16 | |
| TemperatureEvaluated06 | Integer16 | |
| IOCycleCounter01 | Unsigned8 | |
| IOCycleCounter02 | Unsigned8 | |
| IOCycleCounter03 | Unsigned8 | |
| IOCycleCounter04 | Unsigned8 | |
| IOCycleCounter05 | Unsigned8 | |
| IOCycleCounter06 | Unsigned8 | |
| ModuleStatus01To04 | Unsigned8 | |
| ModuleStatus05To06 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| ExternalCompensation01 | | Integer16 |
| ExternalCompensation02 | | Integer16 |
| ExternalCompensation03 | | Integer16 |
| ExternalCompensation04 | | Integer16 |
| ExternalCompensation05 | | Integer16 |
| ExternalCompensation06 | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0402 | 1026 | 2 |
| 0x0442 | 1090 | 2 |
| 0x0482 | 1154 | 2 |
| 0x04C2 | 1218 | 2 |
| 0x0502 | 1282 | 2 |
| 0x0542 | 1346 | 2 |
| 0x0582 | 1410 | 2 |

3.1. X20cAI2438

HwId: 0xE1EE

Functionmodel: 254

Description: Coated 2 Current Inputs, HART Master

| Input | | |
|-----------------------|------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Unsigned16 | |
| AnalogInput02 | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0182 | 386 | 2 |
| 0x0186 | 390 | 2 |
| 0x01A2 | 418 | 2 |
| 0x01A6 | 422 | 2 |
| 0x01AA | 426 | 2 |
| 0x01AE | 430 | 2 |
| 0x01CA | 458 | 2 |
| 0x01CE | 462 | 2 |
| 0x018E | 398 | 2 |
| 0x0192 | 402 | 2 |
| 0x01B6 | 438 | 2 |
| 0x01BA | 442 | 2 |

3.1. X20cAI4622

HwId: 0xE1EF

Functionmodel: 0

Description: Coated 4 Inputs ?10 V / 0 to 20 mA / 4 to 20 mA, 12 bit

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |
| 0x0014 | 20 | 2 |
| 0x0016 | 22 | 2 |

3.1. X20cAI4632

HwId: 0xE1F0

Functionmodel: 0

Description: Coated 4 Inputs ± 10 V / 0 to 20 mA, 16 bit

| Input | | |
|-----------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| Inputs_X20cAI4632_a | Unsigned8 | |
| | Bit 0 | Channel01OK |
| | Bit 1 | Channel02OK |
| | Bit 2 | Channel03OK |
| | Bit 3 | Channel04OK |
| | Bit 6 | SyncStatus |
| | Bit 7 | ConversionCycle |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0186 | 390 | 2 |
| 0x0101 | 257 | 1 |
| 0x0103 | 259 | 1 |
| 0x0106 | 262 | 2 |
| 0x010A | 266 | 2 |
| 0x010E | 270 | 2 |
| 0x0114 | 276 | 4 |
| 0x011C | 284 | 4 |
| 0x0121 | 289 | 1 |
| 0x0123 | 291 | 1 |
| 0x0126 | 294 | 2 |
| 0x012A | 298 | 2 |
| 0x012E | 302 | 2 |
| 0x0134 | 308 | 4 |
| 0x013C | 316 | 4 |
| 0x0141 | 321 | 1 |
| 0x0143 | 323 | 1 |
| 0x0146 | 326 | 2 |
| 0x014A | 330 | 2 |
| 0x014E | 334 | 2 |
| 0x0154 | 340 | 4 |
| 0x015C | 348 | 4 |
| 0x0161 | 353 | 1 |
| 0x0163 | 355 | 1 |
| 0x0166 | 358 | 2 |
| 0x016A | 362 | 2 |
| 0x016E | 366 | 2 |
| 0x0174 | 372 | 4 |
| 0x017C | 380 | 4 |

3.1. X20cAI4632-1

HwId: 0xD57A

Functionmodel: 0

Description: Coated 4 Inputs ± 11 V / 0 to 22 mA, 16 bit

| Input | | |
|-----------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| Inputs_X20cAI4632-1_a | Unsigned8 | |
| | Bit 0 | Channel01OK |
| | Bit 1 | Channel02OK |
| | Bit 2 | Channel03OK |
| | Bit 3 | Channel04OK |
| | Bit 6 | SyncStatus |
| | Bit 7 | ConversionCycle |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0186 | 390 | 2 |
| 0x0101 | 257 | 1 |
| 0x0103 | 259 | 1 |
| 0x0106 | 262 | 2 |
| 0x010A | 266 | 2 |
| 0x010E | 270 | 2 |
| 0x0114 | 276 | 4 |
| 0x011C | 284 | 4 |
| 0x0121 | 289 | 1 |
| 0x0123 | 291 | 1 |
| 0x0126 | 294 | 2 |
| 0x012A | 298 | 2 |
| 0x012E | 302 | 2 |
| 0x0134 | 308 | 4 |
| 0x013C | 316 | 4 |
| 0x0141 | 321 | 1 |
| 0x0143 | 323 | 1 |
| 0x0146 | 326 | 2 |
| 0x014A | 330 | 2 |
| 0x014E | 334 | 2 |
| 0x0154 | 340 | 4 |
| 0x015C | 348 | 4 |
| 0x0161 | 353 | 1 |
| 0x0163 | 355 | 1 |
| 0x0166 | 358 | 2 |
| 0x016A | 362 | 2 |
| 0x016E | 366 | 2 |
| 0x0174 | 372 | 4 |
| 0x017C | 380 | 4 |

3.1. X20cAT4222

HwId: 0xE215

Functionmodel: 0

Description: Coated 4 Resistor Temperature Inputs

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| Temperature03 | Integer16 | |
| Temperature04 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 2 |

3.1. X20cAT4222_C1

HwId: 0xE215

Functionmodel: 1

Description: Coated 4 Resistor Temperature Inputs

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| Temperature03 | Integer16 | |
| Temperature04 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 2 |

3.1. X20cAT6402

HwId: 0xDD57

Functionmodel: 0

Description: Coated 6 Thermocouple Temperature Inputs

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| Temperature03 | Integer16 | |
| Temperature04 | Integer16 | |
| Temperature05 | Integer16 | |
| Temperature06 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| StatusInput02 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 1 |
| 0x001B | 27 | 1 |

3.1. X20cAT6402_C1

HwId: 0xDD57

Functionmodel: 1

Description: Coated 6 Thermocouple Temperature Inputs

| Input | | |
|---------------------------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| Temperature03 | Integer16 | |
| Temperature04 | Integer16 | |
| Temperature05 | Integer16 | |
| Temperature06 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| StatusInput02 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| ExternalCompensationTemperature | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 1 |
| 0x001B | 27 | 1 |

3.1. X67AI1223

HwId: 0x16F1

Functionmodel: 0

Description: 4 Inputs ?10 V

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |

3.1. X67AI1323

HwId: 0x16F2

Functionmodel: 0

Description: 4 Inputs 0 to 20 mA

| Input | | | |
|-----------------------|-------|---------------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| AnalogInput01 | | Integer16 | |
| AnalogInput02 | | Integer16 | |
| AnalogInput03 | | Integer16 | |
| AnalogInput04 | | Integer16 | |
| Inputs_X67AI1323_a | | Unsigned8 | |
| | Bit 1 | StatusAnalogInput01 | |
| | Bit 3 | StatusAnalogInput02 | |
| | Bit 5 | StatusAnalogInput03 | |
| | Bit 7 | StatusAnalogInput04 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |

3.1. X67AI1333

HwId: 0xAB1C

Functionmodel: 0

Description: 4 Inputs 0 to 20 mA, 16 bit

| Input | | | |
|-----------------------|-------|------------------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| AnalogInput01 | | Integer16 | |
| AnalogInput02 | | Integer16 | |
| AnalogInput03 | | Integer16 | |
| AnalogInput04 | | Integer16 | |
| Inputs_X67AI1333_a | | Unsigned8 | |
| | Bit 1 | OverflowAnalogInput01 | |
| | Bit 0 | UnderflowAnalogInput01 | |
| | Bit 3 | OverflowAnalogInput02 | |
| | Bit 2 | UnderflowAnalogInput02 | |
| | Bit 5 | OverflowAnalogInput03 | |
| | Bit 4 | UnderflowAnalogInput03 | |
| | Bit 7 | OverflowAnalogInput04 | |
| | Bit 6 | UnderflowAnalogInput04 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |
| 0x0014 | 20 | 2 |
| 0x0016 | 22 | 2 |

3.1. X67AI2744

HwId: 0x2274

Functionmodel: 0

Description: 2x DMS

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| StatusInput01 | Unsigned8 | |
| AnalogInput01 | Integer32 | |
| AnalogInput02 | Integer32 | |
| Output | | |
| Name | Input | Output |
| ConfigOutput01 | | Unsigned8 |
| ConfigOutput02 | | Unsigned8 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

3.1. X67AI4850

HwId: 0x26E5

Functionmodel: 0

Description: 4 Inputs for Potentiometer Encoder

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

3.1. X67AT1311

HwId: 0xD21B

Functionmodel: 0

Description: 4 Inputs for PT100 (2/4-wire)

| Input | | |
|-----------------------|------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| Temperature03 | Integer16 | |
| Temperature04 | Integer16 | |
| StatusInput01 | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 2 |

3.1. X67AT1311-C01

HwId: 0xD2A8

Functionmodel: 0

Description: 4 Inputs for PT100 (2/4-wire)

| Input | | |
|-----------------------|------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| Temperature03 | Integer16 | |
| Temperature04 | Integer16 | |
| StatusInput01 | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 2 |

3.1. X67AT1322

HwId: 0x1488

Functionmodel: 0

Description: 4 Inputs for RTD (2/4-wire)

| Input | | |
|-----------------------|------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| Temperature03 | Integer16 | |
| Temperature04 | Integer16 | |
| StatusInput01 | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 2 |

3.1. X67AT1402

HwId: 0x1486

Functionmodel: 0

Description: 4 Inputs for Thermocouples

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Temperature01 | Integer16 | |
| Temperature02 | Integer16 | |
| Temperature03 | Integer16 | |
| Temperature04 | Integer16 | |
| TerminalTemperature01 | Integer16 | |
| TerminalTemperature02 | Integer16 | |
| TerminalTemperature03 | Integer16 | |
| TerminalTemperature04 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |

4. Analog Output Module

4.1. X20AO2437

HwId: 0xB785

Functionmodel: 254

Description: 2 Current Outputs, electrically isolated

| Input | | |
|----------------|-----------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogStatus01 | Unsigned8 | |
| AnalogStatus02 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Unsigned16 |
| AnalogOutput02 | | Unsigned16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0182 | 386 | 2 |
| 0x0186 | 390 | 2 |
| 0x018A | 394 | 2 |
| 0x018E | 398 | 2 |

4.1. X20AO2438

HwId: 0xB3AA

Functionmodel: 254

Description: 2 Current Outputs, HART Master

| Input | | |
|----------------|-----------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogStatus01 | Unsigned8 | |
| AnalogStatus02 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Unsigned16 |
| AnalogOutput02 | | Unsigned16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0182 | 386 | 2 |
| 0x0186 | 390 | 2 |
| 0x018A | 394 | 2 |
| 0x018E | 398 | 2 |

4.1. X20AO2622

HwId: 0x1BA2

Functionmodel: 1

Description: 2 Outputs ?10 V / 0 to 20 mA / 4 to 20 mA

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

4.1. X20AO2632

HwId: 0x1BA4

Functionmodel: 0

Description: 2 Outputs ?10 V / 0 to 20 mA

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0000 | 0 | 2 |

4.1. X20AO2632-1

HwId: 0xC36E

Functionmodel: 0

Description: 2 Outputs ?11 V / 0 to 22 mA

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0000 | 0 | 2 |

4.1. X20AO4622

HwId: 0x1BA3

Functionmodel: 1

Description: 4 Outputs ?10 V / 0 to 20 mA

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |
| AnalogOutput03 | | Integer16 |
| AnalogOutput04 | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

4.1. X20AO4632

HwId: 0x1BA5

Functionmodel: 0

Description: 4 Outputs ?10 V / 0 to 20 mA

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |
| AnalogOutput03 | | Integer16 |
| AnalogOutput04 | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0000 | 0 | 2 |

4.1. X20AO4632-1

HwId: 0xC36F

Functionmodel: 0

Description: 4 Outputs ?11 V / 0 to 22 mA

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |
| AnalogOutput03 | | Integer16 |
| AnalogOutput04 | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0000 | 0 | 2 |

4.1. X20AO4635

HwId: 0xA7FE

Functionmodel: 0

Description: 4 Outputs ?10 V / 0 to 20 mA Low drift

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |
| AnalogOutput03 | | Integer16 |
| AnalogOutput04 | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0000 | 0 | 2 |

4.1. X20cAO2437

HwId: 0xE1F2

Functionmodel: 254

Description: Coated 2 Current Outputs, electrically isolated

| Input | | |
|----------------|-----------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogStatus01 | Unsigned8 | |
| AnalogStatus02 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Unsigned16 |
| AnalogOutput02 | | Unsigned16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0182 | 386 | 2 |
| 0x0186 | 390 | 2 |
| 0x018A | 394 | 2 |
| 0x018E | 398 | 2 |

4.1. X20cAO2438

HwId: 0xE211

Functionmodel: 254

Description: Coated 2 Current Outputs, HART Master

| Input | | |
|----------------|-----------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogStatus01 | Unsigned8 | |
| AnalogStatus02 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Unsigned16 |
| AnalogOutput02 | | Unsigned16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0182 | 386 | 2 |
| 0x0186 | 390 | 2 |
| 0x018A | 394 | 2 |
| 0x018E | 398 | 2 |

4.1. X20cAO4622

HwId: 0xE212

Functionmodel: 1

Description: Coated 4 Outputs ?10 V / 0 to 20 mA / 4 to 20 mA, 12 bit

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |
| AnalogOutput03 | | Integer16 |
| AnalogOutput04 | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

4.1. X20cAO4632

HwId: 0xD575

Functionmodel: 0

Description: Coated 4 Outputs ?10 V / 0 to 20 mA, 16 bit

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |
| AnalogOutput03 | | Integer16 |
| AnalogOutput04 | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0000 | 0 | 2 |

4.1. X20cAO4632-1

HwId: 0xE213

Functionmodel: 0

Description: Coated 4 Outputs ?11 V / 0 to 22 mA, 16 bit

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |
| AnalogOutput03 | | Integer16 |
| AnalogOutput04 | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0000 | 0 | 2 |

4.1. X20MM2436

HwId: 0x26B5

Functionmodel: 0

Description: 2x PWM - motor bridge, 3 A, 4x DI

| Input | | |
|--------------------------|-----------|----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Counter01 | Integer16 | |
| Counter02 | Integer16 | |
| Inputs_X20MM2436_a | Unsigned8 | |
| | Bit 0 | StatusInput01 |
| | Bit 1 | StatusInput02 |
| | Bit 2 | StatusInput03 |
| | Bit 3 | StatusInput04 |
| Inputs_X20MM2436_b | Unsigned8 | |
| | Bit 0 | UnderVoltageError |
| | Bit 1 | OverVoltageError |
| | Bit 2 | OvertemperatureError |
| | Bit 4 | CurrentError01 |
| | Bit 5 | OverCurrentError01 |
| | Bit 6 | CurrentError02 |
| | Bit 7 | OverCurrentError02 |
| Output | | |
| Name | Input | Output |
| PeriodDurationPWM01PWM02 | | Unsigned16 |
| PulseWidthCurrentPWM01 | | Integer16 |
| PulseWidthCurrentPWM02 | | Integer16 |
| Outputs_X20MM2436_a | | Unsigned8 |
| | Bit 0 | ClearError01 |
| | Bit 1 | ClearError02 |
| | Bit 6 | DitherDisable01 |
| | Bit 7 | DitherDisable02 |
| | Bit 4 | CounterReset01 |
| | Bit 5 | CounterReset02 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x001E | 30 | 1 |
| 0x001F | 31 | 1 |
| 0x0012 | 18 | 1 |
| 0x0014 | 20 | 1 |
| 0x0026 | 38 | 1 |
| 0x0027 | 39 | 1 |

4.1. X20MM3332

HwId: 0xA982

Functionmodel: 0

Description: Full bridge, 3 x 3A, 24 VDC

| Input | | |
|---------------------|-----------|----------------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| CurrentInput01 | Unsigned8 | |
| CurrentInput02 | Unsigned8 | |
| CurrentInput03 | Unsigned8 | |
| Inputs_X20MM3332_a | Unsigned8 | |
| | Bit 7 | UndervoltageError |
| | Bit 6 | OvertemperatureError |
| | Bit 0 | OvercurrentError01 |
| | Bit 3 | StatusDigitalOutput01 |
| | Bit 1 | OvercurrentError02 |
| | Bit 4 | StatusDigitalOutput02 |
| | Bit 2 | OvercurrentError03 |
| | Bit 5 | StatusDigitalOutput03 |
| Output | | |
| Name | Input | Output |
| Outputs_X20MM3332_a | | Unsigned8 |
| | Bit 0 | StartChannel01 |
| | Bit 1 | ShortCircuitChannel01 |
| | Bit 2 | DirectionChannel01 |
| | Bit 4 | StartChannel02 |
| | Bit 5 | ShortCircuitChannel02 |
| | Bit 6 | DirectionChannel02 |
| Outputs_X20MM3332_b | | Unsigned8 |
| | Bit 0 | StartChannel03 |
| | Bit 1 | ShortCircuitChannel03 |
| | Bit 2 | DirectionChannel03 |
| Outputs_X20MM3332_c | | Unsigned8 |
| | Bit 7 | UndervoltageAcknowledge |
| | Bit 6 | OvertemperatureAcknowledge |
| | Bit 0 | OvercurrentAcknowledge01 |
| | Bit 1 | OvercurrentAcknowledge02 |
| | Bit 2 | OvercurrentAcknowledge03 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

4.1. X20MM4331

HwId: 0xA976

Functionmodel: 0

Description: Half bridge, 4 x 3A, 24 VDC

| Input | | | |
|---------------------|-------|-----------------------|----------------------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| CurrentInput01 | | Unsigned8 | |
| CurrentInput02 | | Unsigned8 | |
| CurrentInput03 | | Unsigned8 | |
| CurrentInput04 | | Unsigned8 | |
| Inputs_X20MM4331_a | | Unsigned8 | |
| | Bit 0 | OvercurrentError01 | |
| | Bit 4 | StatusDigitalOutput01 | |
| | Bit 1 | OvercurrentError02 | |
| | Bit 5 | StatusDigitalOutput02 | |
| | Bit 2 | OvercurrentError03 | |
| | Bit 6 | StatusDigitalOutput03 | |
| | Bit 3 | OvercurrentError04 | |
| | Bit 7 | StatusDigitalOutput04 | |
| Inputs_X20MM4331_b | | Unsigned8 | |
| | Bit 1 | UndervoltageError | |
| | Bit 0 | OvertemperatureError | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20MM4331_a | | | Unsigned8 |
| | Bit 0 | | StartChannel01 |
| | Bit 1 | | ShortCircuitChannel01 |
| | Bit 2 | | StartChannel02 |
| | Bit 3 | | ShortCircuitChannel02 |
| | Bit 4 | | StartChannel03 |
| | Bit 5 | | ShortCircuitChannel03 |
| | Bit 6 | | StartChannel04 |
| | Bit 7 | | ShortCircuitChannel04 |
| Outputs_X20MM4331_b | | | Unsigned8 |
| | Bit 7 | | UndervoltageAcknowledge |
| | Bit 6 | | OvertemperatureAcknowledge |
| | Bit 0 | | OvercurrentAcknowledge01 |
| | Bit 1 | | OvercurrentAcknowledge02 |
| | Bit 2 | | OvercurrentAcknowledge03 |
| | Bit 3 | | OvercurrentAcknowledge04 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

4.1. X20MM4456

HwId: 0xA177

Functionmodel: 0

Description: 4x PWM - motor bridge, 6 A, 4x4 DI

| Input | | |
|--------------------|------------|----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Counter01 | Integer16 | |
| CounterLatch01 | Integer16 | |
| usSinceTrigger01 | Unsigned16 | |
| Inputs_X20MM4456_a | Unsigned8 | |
| | Bit 0 | StatusInput01 |
| | Bit 1 | StatusInput02 |
| | Bit 2 | StatusInput03 |
| | Bit 3 | StatusInput04 |
| | Bit 4 | nLatchPending01 |
| | Bit 5 | LatchDone01 |
| | Bit 6 | EndswitchReached01 |
| | Bit 7 | PWMError01 |
| Inputs_X20MM4456_b | Unsigned8 | |
| | Bit 5 | UnderVoltageError |
| | Bit 6 | VoltageWarning |
| | Bit 4 | OverVoltageError |
| | Bit 7 | OvertemperatureError |
| Counter02 | Integer16 | |
| CounterLatch02 | Integer16 | |
| usSinceTrigger02 | Unsigned16 | |
| Inputs_X20MM4456_c | Unsigned8 | |
| | Bit 0 | StatusInput05 |
| | Bit 1 | StatusInput06 |
| | Bit 2 | StatusInput07 |
| | Bit 3 | StatusInput08 |
| | Bit 4 | nLatchPending02 |
| | Bit 5 | LatchDone02 |
| | Bit 6 | EndswitchReached02 |
| | Bit 7 | PWMError02 |
| Inputs_X20MM4456_d | Unsigned8 | |
| | Bit 0 | CurrentError01 |
| | Bit 1 | OverCurrentError01 |
| | Bit 2 | CurrentError02 |
| | Bit 3 | OverCurrentError02 |
| | Bit 4 | CurrentError03 |
| | Bit 5 | OverCurrentError03 |
| | Bit 6 | CurrentError04 |
| | Bit 7 | OverCurrentError04 |
| Counter03 | Integer16 | |
| CounterLatch03 | Integer16 | |
| usSinceTrigger03 | Unsigned16 | |
| Inputs_X20MM4456_e | Unsigned8 | |
| | Bit 0 | StatusInput09 |
| | Bit 1 | StatusInput10 |
| | Bit 2 | StatusInput11 |
| | Bit 3 | StatusInput12 |
| | Bit 4 | nLatchPending03 |
| | Bit 5 | LatchDone03 |

| Input | | | |
|------------------------|-------|--------------------|-------------------|
| Name | | Input | Output |
| | Bit 6 | EndswitchReached03 | |
| | Bit 7 | PWMError03 | |
| Counter04 | | Integer16 | |
| CounterLatch04 | | Integer16 | |
| usSinceTrigger04 | | Unsigned16 | |
| Inputs_X20MM4456_f | | Unsigned8 | |
| | Bit 0 | StatusInput13 | |
| | Bit 1 | StatusInput14 | |
| | Bit 2 | StatusInput15 | |
| | Bit 3 | StatusInput16 | |
| | Bit 4 | nLatchPending04 | |
| | Bit 5 | LatchDone04 | |
| | Bit 6 | EndswitchReached04 | |
| | Bit 7 | PWMError04 | |
| Output | | | |
| Name | | Input | Output |
| PulseWidthCurrentPWM01 | | | Integer16 |
| Outputs_X20MM4456_a | | | Unsigned8 |
| | Bit 0 | | TriggerEdge01 |
| | Bit 1 | | StartTrigger01 |
| | Bit 2 | | StartLatch01 |
| | Bit 3 | | DitherDisable01 |
| | Bit 4 | | ClearError01 |
| | Bit 5 | | ShowMeanCurrent01 |
| | Bit 6 | | ResetCounter01 |
| PeriodDurationPWM | | | Unsigned16 |
| PulseWidthCurrentPWM02 | | | Integer16 |
| Outputs_X20MM4456_b | | | Unsigned8 |
| | Bit 0 | | TriggerEdge02 |
| | Bit 1 | | StartTrigger02 |
| | Bit 2 | | StartLatch02 |
| | Bit 3 | | DitherDisable02 |
| | Bit 4 | | ClearError02 |
| | Bit 5 | | ShowMeanCurrent02 |
| | Bit 6 | | ResetCounter02 |
| PulseWidthCurrentPWM03 | | | Integer16 |
| Outputs_X20MM4456_c | | | Unsigned8 |
| | Bit 0 | | TriggerEdge03 |
| | Bit 1 | | StartTrigger03 |
| | Bit 2 | | StartLatch03 |
| | Bit 3 | | DitherDisable03 |
| | Bit 4 | | ClearError03 |
| | Bit 5 | | ShowMeanCurrent03 |
| | Bit 6 | | ResetCounter03 |
| PulseWidthCurrentPWM04 | | | Integer16 |
| Outputs_X20MM4456_d | | | Unsigned8 |
| | Bit 0 | | TriggerEdge04 |
| | Bit 1 | | StartTrigger04 |
| | Bit 2 | | StartLatch04 |
| | Bit 3 | | DitherDisable04 |
| | Bit 4 | | ClearError04 |
| | Bit 5 | | ShowMeanCurrent04 |
| | Bit 6 | | ResetCounter04 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0048 | 72 | 2 |
| 0x0050 | 80 | 2 |
| 0x0058 | 88 | 2 |
| 0x0060 | 96 | 2 |
| 0x0040 | 64 | 1 |
| 0x0041 | 65 | 1 |

4.1. X20MM4456_C1

HwId: 0xA177

Functionmodel: 0

Description: 4x PWM - motor bridge, 6 A, 4x4 DI

| Input | | |
|--------------------|------------|----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Counter01 | Integer16 | |
| CounterLatch01 | Integer16 | |
| usSinceTrigger01 | Unsigned16 | |
| Inputs_X20MM4456_a | Unsigned8 | |
| | Bit 0 | StatusInput01 |
| | Bit 1 | StatusInput02 |
| | Bit 2 | StatusInput03 |
| | Bit 3 | StatusInput04 |
| | Bit 4 | nLatchPending01 |
| | Bit 5 | LatchDone01 |
| | Bit 6 | EndswitchReached01 |
| | Bit 7 | PWMError01 |
| Inputs_X20MM4456_b | Unsigned8 | |
| | Bit 5 | UnderVoltageError |
| | Bit 6 | VoltageWarning |
| | Bit 4 | OverVoltageError |
| | Bit 7 | OvertemperatureError |
| Counter02 | Integer16 | |
| CounterLatch02 | Integer16 | |
| usSinceTrigger02 | Unsigned16 | |
| Inputs_X20MM4456_c | Unsigned8 | |
| | Bit 0 | StatusInput05 |
| | Bit 1 | StatusInput06 |
| | Bit 2 | StatusInput07 |
| | Bit 3 | StatusInput08 |
| | Bit 4 | nLatchPending02 |
| | Bit 5 | LatchDone02 |
| | Bit 6 | EndswitchReached02 |
| | Bit 7 | PWMError02 |
| Inputs_X20MM4456_d | Unsigned8 | |
| | Bit 0 | CurrentError01 |
| | Bit 1 | OverCurrentError01 |
| | Bit 2 | CurrentError02 |
| | Bit 3 | OverCurrentError02 |
| | Bit 4 | CurrentError03 |
| | Bit 5 | OverCurrentError03 |
| | Bit 6 | CurrentError04 |
| | Bit 7 | OverCurrentError04 |
| Counter03 | Integer16 | |
| CounterLatch03 | Integer16 | |
| usSinceTrigger03 | Unsigned16 | |
| Inputs_X20MM4456_e | Unsigned8 | |
| | Bit 0 | StatusInput09 |
| | Bit 1 | StatusInput10 |
| | Bit 2 | StatusInput11 |
| | Bit 3 | StatusInput12 |
| | Bit 4 | nLatchPending03 |
| | Bit 5 | LatchDone03 |

| Input | | | |
|------------------------|-------|--------------------|-------------------|
| Name | | Input | Output |
| | Bit 6 | EndswitchReached03 | |
| | Bit 7 | PWMError03 | |
| Counter04 | | Integer16 | |
| CounterLatch04 | | Integer16 | |
| usSinceTrigger04 | | Unsigned16 | |
| Inputs_X20MM4456_f | | Unsigned8 | |
| | Bit 0 | StatusInput13 | |
| | Bit 1 | StatusInput14 | |
| | Bit 2 | StatusInput15 | |
| | Bit 3 | StatusInput16 | |
| | Bit 4 | nLatchPending04 | |
| | Bit 5 | LatchDone04 | |
| | Bit 6 | EndswitchReached04 | |
| | Bit 7 | PWMError04 | |
| Output | | | |
| Name | | Input | Output |
| PulseWidthCurrentPWM01 | | | Integer16 |
| Outputs_X20MM4456_a | | | Unsigned8 |
| | Bit 0 | | TriggerEdge01 |
| | Bit 1 | | StartTrigger01 |
| | Bit 2 | | StartLatch01 |
| | Bit 3 | | DitherDisable01 |
| | Bit 4 | | ClearError01 |
| | Bit 5 | | ShowMeanCurrent01 |
| | Bit 6 | | ResetCounter01 |
| | Bit 7 | | OutputEnable01 |
| PeriodDurationPWM | | | Unsigned16 |
| PulseWidthCurrentPWM02 | | | Integer16 |
| Outputs_X20MM4456_b | | | Unsigned8 |
| | Bit 0 | | TriggerEdge02 |
| | Bit 1 | | StartTrigger02 |
| | Bit 2 | | StartLatch02 |
| | Bit 3 | | DitherDisable02 |
| | Bit 4 | | ClearError02 |
| | Bit 5 | | ShowMeanCurrent02 |
| | Bit 6 | | ResetCounter02 |
| | Bit 7 | | OutputEnable02 |
| PulseWidthCurrentPWM03 | | | Integer16 |
| Outputs_X20MM4456_c | | | Unsigned8 |
| | Bit 0 | | TriggerEdge03 |
| | Bit 1 | | StartTrigger03 |
| | Bit 2 | | StartLatch03 |
| | Bit 3 | | DitherDisable03 |
| | Bit 4 | | ClearError03 |
| | Bit 5 | | ShowMeanCurrent03 |
| | Bit 6 | | ResetCounter03 |
| | Bit 7 | | OutputEnable03 |
| PulseWidthCurrentPWM04 | | | Integer16 |
| Outputs_X20MM4456_d | | | Unsigned8 |
| | Bit 0 | | TriggerEdge04 |
| | Bit 1 | | StartTrigger04 |
| | Bit 2 | | StartLatch04 |
| | Bit 3 | | DitherDisable04 |
| | Bit 4 | | ClearError04 |

| Output | | | |
|--------|-------|-------|-------------------|
| Name | | Input | Output |
| | Bit 5 | | ShowMeanCurrent04 |
| | Bit 6 | | ResetCounter04 |
| | Bit 7 | | OutputEnable04 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0048 | 72 | 2 |
| 0x0050 | 80 | 2 |
| 0x0058 | 88 | 2 |
| 0x0060 | 96 | 2 |
| 0x0040 | 64 | 1 |
| 0x0041 | 65 | 1 |
| 0x004A | 74 | 2 |
| 0x004C | 76 | 2 |
| 0x004E | 78 | 2 |
| 0x0068 | 104 | 2 |
| 0x0052 | 82 | 2 |
| 0x0054 | 84 | 2 |
| 0x0056 | 86 | 2 |
| 0x006A | 106 | 2 |
| 0x005A | 90 | 2 |
| 0x005C | 92 | 2 |
| 0x005E | 94 | 2 |
| 0x006C | 108 | 2 |
| 0x0062 | 98 | 2 |
| 0x0064 | 100 | 2 |
| 0x0066 | 102 | 2 |
| 0x006E | 110 | 2 |

4.1. X20SM1426

HwId: 0x2681

Functionmodel: 3

Description: 1x Stepper motor module 1A, 4 DI

| Input | | |
|----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AbsPos01ActVal | Integer32 | |
| MpGenStatus01 | Unsigned16 | |
| InputStatus | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AbsPos01 | | Integer32 |
| MpGenControl01 | | Unsigned16 |
| MpGenMode01 | | Integer8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0048 | 72 | 2 |
| 0x0030 | 48 | 1 |
| 0x0031 | 49 | 1 |
| 0x0032 | 50 | 1 |
| 0x0034 | 52 | 2 |
| 0x0036 | 54 | 2 |
| 0x0038 | 56 | 2 |
| 0x003A | 58 | 2 |
| 0x003C | 60 | 4 |
| 0x0040 | 64 | 4 |
| 0x0044 | 68 | 2 |
| 0x004A | 74 | 1 |
| 0x004B | 75 | 1 |
| 0x0046 | 70 | 1 |
| 0x0033 | 51 | 1 |
| 0x0132 | 306 | 1 |
| 0x0134 | 308 | 1 |
| 0x0158 | 344 | 4 |
| 0x015C | 348 | 4 |

4.1. X20SM1426_C1

HwId: 0x2681

Functionmodel: 3

Description: 1x Stepper motor module 1A, 4 DI

Requires at least FW Version: 200

| Input | | |
|----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AbsPos01ActVal | Integer32 | |
| MpGenStatus01 | Unsigned16 | |
| InputStatus | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AbsPos01 | | Integer32 |
| MpGenControl01 | | Unsigned16 |
| MpGenMode01 | | Integer8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0048 | 72 | 2 |
| 0x0030 | 48 | 1 |
| 0x0031 | 49 | 1 |
| 0x0032 | 50 | 1 |
| 0x0034 | 52 | 2 |
| 0x0036 | 54 | 2 |
| 0x0038 | 56 | 2 |
| 0x003A | 58 | 2 |
| 0x003C | 60 | 4 |
| 0x0040 | 64 | 4 |
| 0x0044 | 68 | 2 |
| 0x004A | 74 | 1 |
| 0x004B | 75 | 1 |
| 0x0046 | 70 | 1 |
| 0x0033 | 51 | 1 |
| 0x0132 | 306 | 1 |
| 0x0134 | 308 | 1 |
| 0x0158 | 344 | 4 |
| 0x015C | 348 | 4 |
| 0x004E | 78 | 2 |

4.1. X20SM1436

HwId: 0x2682

Functionmodel: 3

Description: 1x Stepper motor module 3A, 4 DI

| Input | | |
|----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AbsPos01ActVal | Integer32 | |
| MpGenStatus01 | Unsigned16 | |
| InputStatus | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AbsPos01 | | Integer32 |
| MpGenControl01 | | Unsigned16 |
| MpGenMode01 | | Integer8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0048 | 72 | 2 |
| 0x0030 | 48 | 1 |
| 0x0031 | 49 | 1 |
| 0x0032 | 50 | 1 |
| 0x0034 | 52 | 2 |
| 0x0036 | 54 | 2 |
| 0x0038 | 56 | 2 |
| 0x003A | 58 | 2 |
| 0x003C | 60 | 4 |
| 0x0040 | 64 | 4 |
| 0x0044 | 68 | 2 |
| 0x004A | 74 | 1 |
| 0x004B | 75 | 1 |
| 0x0046 | 70 | 1 |
| 0x0033 | 51 | 1 |
| 0x0132 | 306 | 1 |
| 0x0134 | 308 | 1 |
| 0x0158 | 344 | 4 |
| 0x015C | 348 | 4 |

4.1. X20SM1436_C1

HwId: 0x2682

Functionmodel: 3

Description: 1x Stepper motor module 3A, 4 DI

Requires at least FW Version: 200

| Input | | |
|----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AbsPos01ActVal | Integer32 | |
| MpGenStatus01 | Unsigned16 | |
| InputStatus | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AbsPos01 | | Integer32 |
| MpGenControl01 | | Unsigned16 |
| MpGenMode01 | | Integer8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0048 | 72 | 2 |
| 0x0030 | 48 | 1 |
| 0x0031 | 49 | 1 |
| 0x0032 | 50 | 1 |
| 0x0034 | 52 | 2 |
| 0x0036 | 54 | 2 |
| 0x0038 | 56 | 2 |
| 0x003A | 58 | 2 |
| 0x003C | 60 | 4 |
| 0x0040 | 64 | 4 |
| 0x0044 | 68 | 2 |
| 0x004A | 74 | 1 |
| 0x004B | 75 | 1 |
| 0x0046 | 70 | 1 |
| 0x0033 | 51 | 1 |
| 0x0132 | 306 | 1 |
| 0x0134 | 308 | 1 |
| 0x0158 | 344 | 4 |
| 0x015C | 348 | 4 |
| 0x004E | 78 | 2 |

4.1. X67AO1223

HwId: 0x16F3

Functionmodel: 0

Description: 4 Outputs ?10 V

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |
| AnalogOutput03 | | Integer16 |
| AnalogOutput04 | | Integer16 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

4.1. X67AO1223-1

HwId: 0xA345

Functionmodel: 0

Description: 4 Outputs ?10 V

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |
| AnalogOutput03 | | Integer16 |
| AnalogOutput04 | | Integer16 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

4.1. X67AO1323

HwId: 0x16F4

Functionmodel: 0

Description: 4 Outputs 0 to 20 mA

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |
| AnalogOutput03 | | Integer16 |
| AnalogOutput04 | | Integer16 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

4.1. X67MM2436

HwId: 0x2273

Functionmodel: 0

Description: 2x PWM - motor bridge, 3 A, 2x3 DI

| Input | | |
|--------------------------|------------|----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Counter01 | Integer16 | |
| Counter02 | Integer16 | |
| CounterLatch01 | Integer16 | |
| CounterLatch02 | Integer16 | |
| Inputs_X67MM2436_a | Unsigned8 | |
| | Bit 0 | StatusInput01 |
| | Bit 1 | StatusInput02 |
| | Bit 2 | StatusInput03 |
| | Bit 3 | StatusInput04 |
| | Bit 4 | StatusInput05 |
| | Bit 5 | StatusInput06 |
| | Bit 6 | StatusInput09 |
| | Bit 7 | StatusInput10 |
| usSinceTrigger | Unsigned16 | |
| Inputs_X67MM2436_b | Unsigned8 | |
| | Bit 1 | LatchDone01 |
| | Bit 3 | LatchDone02 |
| | Bit 0 | StatusInput07 |
| | Bit 2 | StatusInput08 |
| | Bit 4 | TriggerInput |
| Inputs_X67MM2436_c | Unsigned8 | |
| | Bit 0 | UnderVoltageError |
| | Bit 1 | OverVoltageError |
| | Bit 2 | OvertemperatureError |
| | Bit 4 | OpenloadError01 |
| | Bit 5 | OverCurrentError01 |
| | Bit 6 | OpenloadError02 |
| | Bit 7 | OverCurrentError02 |
| Output | | |
| Name | Input | Output |
| PeriodDurationPWM01PWM02 | | Unsigned16 |
| PulseWidthCurrentPWM01 | | Integer16 |
| PulseWidthCurrentPWM02 | | Integer16 |
| Outputs_X67MM2436_a | | Unsigned8 |
| | Bit 0 | StartLatch01 |
| | Bit 1 | StartLatch02 |
| | Bit 5 | StartTrigger |
| | Bit 4 | TriggerEdge |
| Outputs_X67MM2436_b | | Unsigned8 |
| | Bit 0 | ClearError01 |
| | Bit 1 | ClearError02 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
| 0x001E | 30 | 1 |
| 0x0004 | 4 | 1 |
| 0x0012 | 18 | 1 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |
| 0x001F | 31 | 1 |

4.1. X67SM2436

HwId: 0x1DCB

Functionmodel: 3

Description: 2x Stepper motor module 3A, 2x3 DI

| Input | | |
|----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AbsPos01ActVal | Integer32 | |
| MpGenStatus01 | Unsigned16 | |
| InputStatus | Unsigned8 | |
| AbsPos02ActVal | Integer32 | |
| MpGenStatus02 | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| AbsPos01 | | Integer32 |
| MpGenControl01 | | Unsigned16 |
| MpGenMode01 | | Integer8 |
| AbsPos02 | | Integer32 |
| MpGenControl02 | | Unsigned16 |
| MpGenMode02 | | Integer8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0048 | 72 | 2 |
| 0x0088 | 136 | 2 |
| 0x0030 | 48 | 1 |
| 0x0031 | 49 | 1 |
| 0x0032 | 50 | 1 |
| 0x0070 | 112 | 1 |
| 0x0071 | 113 | 1 |
| 0x0072 | 114 | 1 |
| 0x0034 | 52 | 2 |
| 0x0074 | 116 | 2 |
| 0x0036 | 54 | 2 |
| 0x0038 | 56 | 2 |
| 0x0076 | 118 | 2 |
| 0x0078 | 120 | 2 |
| 0x003A | 58 | 2 |
| 0x007A | 122 | 2 |
| 0x003C | 60 | 4 |
| 0x0040 | 64 | 4 |
| 0x007C | 124 | 4 |
| 0x0080 | 128 | 4 |
| 0x0044 | 68 | 2 |
| 0x0084 | 132 | 2 |
| 0x004A | 74 | 1 |
| 0x008A | 138 | 1 |
| 0x004B | 75 | 1 |
| 0x008B | 139 | 1 |
| 0x0046 | 70 | 1 |
| 0x0086 | 134 | 1 |
| 0x0033 | 51 | 1 |
| 0x0073 | 115 | 1 |
| 0x0130 | 304 | 1 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0132 | 306 | 1 |
| 0x017C | 380 | 4 |
| 0x0180 | 384 | 4 |
| 0x0134 | 308 | 1 |
| 0x0184 | 388 | 4 |
| 0x0188 | 392 | 4 |

4.1. X67SM2436_C1

HwId: 0x1DCB

Functionmodel: 3

Description: 2x Stepper motor module 3A, 2x3 DI

Requires at least FW Version: 200, HW Variant: 4

| Input | | |
|----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AbsPos01ActVal | Integer32 | |
| MpGenStatus01 | Unsigned16 | |
| InputStatus | Unsigned8 | |
| AbsPos02ActVal | Integer32 | |
| MpGenStatus02 | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| AbsPos01 | | Integer32 |
| MpGenControl01 | | Unsigned16 |
| MpGenMode01 | | Integer8 |
| AbsPos02 | | Integer32 |
| MpGenControl02 | | Unsigned16 |
| MpGenMode02 | | Integer8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0048 | 72 | 2 |
| 0x0088 | 136 | 2 |
| 0x0030 | 48 | 1 |
| 0x0031 | 49 | 1 |
| 0x0032 | 50 | 1 |
| 0x0070 | 112 | 1 |
| 0x0071 | 113 | 1 |
| 0x0072 | 114 | 1 |
| 0x0034 | 52 | 2 |
| 0x0074 | 116 | 2 |
| 0x0036 | 54 | 2 |
| 0x0038 | 56 | 2 |
| 0x0076 | 118 | 2 |
| 0x0078 | 120 | 2 |
| 0x003A | 58 | 2 |
| 0x007A | 122 | 2 |
| 0x003C | 60 | 4 |
| 0x0040 | 64 | 4 |
| 0x007C | 124 | 4 |
| 0x0080 | 128 | 4 |
| 0x0044 | 68 | 2 |
| 0x0084 | 132 | 2 |
| 0x004A | 74 | 1 |
| 0x008A | 138 | 1 |
| 0x004B | 75 | 1 |
| 0x008B | 139 | 1 |
| 0x0046 | 70 | 1 |
| 0x0086 | 134 | 1 |
| 0x0033 | 51 | 1 |
| 0x0073 | 115 | 1 |
| 0x0130 | 304 | 1 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0132 | 306 | 1 |
| 0x017C | 380 | 4 |
| 0x0180 | 384 | 4 |
| 0x0134 | 308 | 1 |
| 0x0184 | 388 | 4 |
| 0x0188 | 392 | 4 |
| 0x004E | 78 | 2 |
| 0x008E | 142 | 2 |

4.1. X67SM4320

HwId: 0x1DCC

Functionmodel: 3

Description: 4x Stepper motor module 1A

| Input | | |
|----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AbsPos01ActVal | Integer32 | |
| MpGenStatus01 | Unsigned16 | |
| AbsPos02ActVal | Integer32 | |
| MpGenStatus02 | Unsigned16 | |
| AbsPos03ActVal | Integer32 | |
| MpGenStatus03 | Unsigned16 | |
| AbsPos04ActVal | Integer32 | |
| MpGenStatus04 | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| AbsPos01 | | Integer32 |
| MpGenControl01 | | Unsigned16 |
| MpGenMode01 | | Integer8 |
| AbsPos02 | | Integer32 |
| MpGenControl02 | | Unsigned16 |
| MpGenMode02 | | Integer8 |
| AbsPos03 | | Integer32 |
| MpGenControl03 | | Unsigned16 |
| MpGenMode03 | | Integer8 |
| AbsPos04 | | Integer32 |
| MpGenControl04 | | Unsigned16 |
| MpGenMode04 | | Integer8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0030 | 48 | 1 |
| 0x0031 | 49 | 1 |
| 0x0032 | 50 | 1 |
| 0x0070 | 112 | 1 |
| 0x0071 | 113 | 1 |
| 0x0072 | 114 | 1 |
| 0x00B0 | 176 | 1 |
| 0x00B1 | 177 | 1 |
| 0x00B2 | 178 | 1 |
| 0x00F0 | 240 | 1 |
| 0x00F1 | 241 | 1 |
| 0x00F2 | 242 | 1 |
| 0x0034 | 52 | 2 |
| 0x0074 | 116 | 2 |
| 0x00B4 | 180 | 2 |
| 0x00F4 | 244 | 2 |
| 0x0036 | 54 | 2 |
| 0x0038 | 56 | 2 |
| 0x0076 | 118 | 2 |
| 0x0078 | 120 | 2 |
| 0x00B6 | 182 | 2 |
| 0x00B8 | 184 | 2 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x00F6 | 246 | 2 |
| 0x00F8 | 248 | 2 |
| 0x003A | 58 | 2 |
| 0x007A | 122 | 2 |
| 0x00BA | 186 | 2 |
| 0x00FA | 250 | 2 |
| 0x003C | 60 | 4 |
| 0x0040 | 64 | 4 |
| 0x007C | 124 | 4 |
| 0x0080 | 128 | 4 |
| 0x00BC | 188 | 4 |
| 0x00C0 | 192 | 4 |
| 0x00FC | 252 | 4 |
| 0x0100 | 256 | 4 |
| 0x0044 | 68 | 2 |
| 0x0084 | 132 | 2 |
| 0x00C4 | 196 | 2 |
| 0x0104 | 260 | 2 |
| 0x004A | 74 | 1 |
| 0x008A | 138 | 1 |
| 0x00CA | 202 | 1 |
| 0x010A | 266 | 1 |
| 0x004B | 75 | 1 |
| 0x008B | 139 | 1 |
| 0x00CB | 203 | 1 |
| 0x010B | 267 | 1 |
| 0x0046 | 70 | 1 |
| 0x0086 | 134 | 1 |
| 0x00C6 | 198 | 1 |
| 0x0106 | 262 | 1 |
| 0x0033 | 51 | 1 |
| 0x0073 | 115 | 1 |
| 0x00B3 | 179 | 1 |
| 0x00F3 | 243 | 1 |
| 0x0134 | 308 | 1 |
| 0x01C0 | 448 | 4 |
| 0x01C4 | 452 | 4 |
| 0x01C8 | 456 | 4 |
| 0x01CC | 460 | 4 |
| 0x01D0 | 464 | 4 |
| 0x01D4 | 468 | 4 |
| 0x01D8 | 472 | 4 |
| 0x01DC | 476 | 4 |

4.1. X67SM4320_C1

HwId: 0x1DCC

Functionmodel: 3

Description: 4x Stepper motor module 1A

Requires at least FW Version: 200

| Input | | |
|----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AbsPos01ActVal | Integer32 | |
| MpGenStatus01 | Unsigned16 | |
| AbsPos02ActVal | Integer32 | |
| MpGenStatus02 | Unsigned16 | |
| AbsPos03ActVal | Integer32 | |
| MpGenStatus03 | Unsigned16 | |
| AbsPos04ActVal | Integer32 | |
| MpGenStatus04 | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| AbsPos01 | | Integer32 |
| MpGenControl01 | | Unsigned16 |
| MpGenMode01 | | Integer8 |
| AbsPos02 | | Integer32 |
| MpGenControl02 | | Unsigned16 |
| MpGenMode02 | | Integer8 |
| AbsPos03 | | Integer32 |
| MpGenControl03 | | Unsigned16 |
| MpGenMode03 | | Integer8 |
| AbsPos04 | | Integer32 |
| MpGenControl04 | | Unsigned16 |
| MpGenMode04 | | Integer8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0030 | 48 | 1 |
| 0x0031 | 49 | 1 |
| 0x0032 | 50 | 1 |
| 0x0070 | 112 | 1 |
| 0x0071 | 113 | 1 |
| 0x0072 | 114 | 1 |
| 0x00B0 | 176 | 1 |
| 0x00B1 | 177 | 1 |
| 0x00B2 | 178 | 1 |
| 0x00F0 | 240 | 1 |
| 0x00F1 | 241 | 1 |
| 0x00F2 | 242 | 1 |
| 0x0034 | 52 | 2 |
| 0x0074 | 116 | 2 |
| 0x00B4 | 180 | 2 |
| 0x00F4 | 244 | 2 |
| 0x0036 | 54 | 2 |
| 0x0038 | 56 | 2 |
| 0x0076 | 118 | 2 |
| 0x0078 | 120 | 2 |
| 0x00B6 | 182 | 2 |
| 0x00B8 | 184 | 2 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x00F6 | 246 | 2 |
| 0x00F8 | 248 | 2 |
| 0x003A | 58 | 2 |
| 0x007A | 122 | 2 |
| 0x00BA | 186 | 2 |
| 0x00FA | 250 | 2 |
| 0x003C | 60 | 4 |
| 0x0040 | 64 | 4 |
| 0x007C | 124 | 4 |
| 0x0080 | 128 | 4 |
| 0x00BC | 188 | 4 |
| 0x00C0 | 192 | 4 |
| 0x00FC | 252 | 4 |
| 0x0100 | 256 | 4 |
| 0x0044 | 68 | 2 |
| 0x0084 | 132 | 2 |
| 0x00C4 | 196 | 2 |
| 0x0104 | 260 | 2 |
| 0x004A | 74 | 1 |
| 0x008A | 138 | 1 |
| 0x00CA | 202 | 1 |
| 0x010A | 266 | 1 |
| 0x004B | 75 | 1 |
| 0x008B | 139 | 1 |
| 0x00CB | 203 | 1 |
| 0x010B | 267 | 1 |
| 0x0046 | 70 | 1 |
| 0x0086 | 134 | 1 |
| 0x00C6 | 198 | 1 |
| 0x0106 | 262 | 1 |
| 0x0033 | 51 | 1 |
| 0x0073 | 115 | 1 |
| 0x00B3 | 179 | 1 |
| 0x00F3 | 243 | 1 |
| 0x0134 | 308 | 1 |
| 0x01C0 | 448 | 4 |
| 0x01C4 | 452 | 4 |
| 0x01C8 | 456 | 4 |
| 0x01CC | 460 | 4 |
| 0x01D0 | 464 | 4 |
| 0x01D4 | 468 | 4 |
| 0x01D8 | 472 | 4 |
| 0x01DC | 476 | 4 |
| 0x004E | 78 | 2 |
| 0x008E | 142 | 2 |
| 0x00CE | 206 | 2 |
| 0x010E | 270 | 2 |

5. Digital Input Module

5.1. X20cDI4371

HwId: 0xE21F

Functionmodel: 0

Description: Coated 4 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | | |
|-----------------------|-------|----------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20cDI4371_a | | Unsigned8 | |
| | Bit 0 | DigitalInput01 | |
| | Bit 1 | DigitalInput02 | |
| | Bit 2 | DigitalInput03 | |
| | Bit 3 | DigitalInput04 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20cDI4375

HwId: 0xE220

Functionmodel: 0

Description: Coated 4 Digital Inputs 24 VDC, Wire monitoring

| Input | | |
|-----------------------|------------|---------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20cDI4375_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | StateDigitalInput01 |
| | Bit 5 | StateDigitalInput02 |
| | Bit 6 | StateDigitalInput03 |
| | Bit 7 | StateDigitalInput04 |
| SampleTimeStamp | Unsigned32 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0802 | 2050 | 2 |
| 0x0805 | 2053 | 1 |

5.1. X20cDI4760

HwId: 0xE221

Functionmodel: 0

Description: Coated 4 Digital Inputs, 8.05 VDC, Namur

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20cDI4760_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| Inputs_X20cDI4760_b | Unsigned8 | |
| | Bit 6 | OpenLine03 |
| | Bit 7 | OpenLine04 |
| | Bit 2 | ShortCircuit03 |
| | Bit 3 | ShortCircuit04 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |

5.1. X20cDI6371

HwId: 0xE222

Functionmodel: 0

Description: Coated 6 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20cDI6371_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20cDI6372

HwId: 0xE223

Functionmodel: 0

Description: Coated 6 Digital Inputs 24 VDC, Source, IEC 61131-2, Type 1

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20cDI6372_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20cDI9371

HwId: 0xD574

Functionmodel: 0

Description: Coated 12 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20cDI9371_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Inputs_X20cDI9371_b | Unsigned8 | |
| | Bit 0 | DigitalInput09 |
| | Bit 1 | DigitalInput10 |
| | Bit 2 | DigitalInput11 |
| | Bit 3 | DigitalInput12 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20cDI9372

HwId: 0xE224

Functionmodel: 0

Description: Coated 12 Digital Inputs 24 VDC, Source

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20cDI9372_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Inputs_X20cDI9372_b | Unsigned8 | |
| | Bit 0 | DigitalInput09 |
| | Bit 1 | DigitalInput10 |
| | Bit 2 | DigitalInput11 |
| | Bit 3 | DigitalInput12 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20cDIF371

HwId: 0xDD44

Functionmodel: 0

Description: Coated 16 Digital Inputs 24 VDC, Sink

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20cDIF371_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Inputs_X20cDIF371_b | Unsigned8 | |
| | Bit 0 | DigitalInput09 |
| | Bit 1 | DigitalInput10 |
| | Bit 2 | DigitalInput11 |
| | Bit 3 | DigitalInput12 |
| | Bit 4 | DigitalInput13 |
| | Bit 5 | DigitalInput14 |
| | Bit 6 | DigitalInput15 |
| | Bit 7 | DigitalInput16 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI0471

HwId: 0xE7CE

Functionmodel: 0

Description: X20 Digital 10xE, 48V, Sink, 1 Leiter

| Input | | |
|-----------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DI0471_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Inputs_X20DI0471_b | Unsigned8 | |
| | Bit 0 | DigitalInput09 |
| | Bit 1 | DigitalInput10 |
| | Bit 7 | ReferenceStatus |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI2371

HwId: 0x1B8D

Functionmodel: 0

Description: 2 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DI2371_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI2372

HwId: 0x22A7

Functionmodel: 0

Description: 2 Digital Inputs 24 VDC, Source

| Input | | | |
|-----------------------|-------|----------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DI2372_a | | Unsigned8 | |
| | Bit 0 | DigitalInput01 | |
| | Bit 1 | DigitalInput02 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI2377

HwId: 0x1B8E

Functionmodel: 0

Description: 2 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1, Counter

| Input | | |
|---------------------|----------------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DI2377_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Counter01 | Unsigned16 | |
| Counter02 | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DI2377_a | | Unsigned8 |
| Bit 5 | | ResetCounter01 |
| Outputs_X20DI2377_b | | Unsigned8 |
| Bit 5 | | ResetCounter02 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI2377_C1

HwId: 0x1B8E

Functionmodel: 1

Description: 2 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1, Counter

| Input | | | |
|------------------------|-------|-----------------------|--------------------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Counter01 | | Unsigned16 | |
| Counter02 | | Unsigned16 | |
| Inputs_X20DI2377_C1_a | | Unsigned8 | |
| | Bit 0 | DigitalInput01Latch | |
| | Bit 1 | DigitalInput02Latch | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DI2377_C1_a | | | Unsigned8 |
| | Bit 5 | | ResetCounter01 |
| Outputs_X20DI2377_C1_b | | | Unsigned8 |
| | Bit 5 | | ResetCounter02 |
| Outputs_X20DI2377_C1_c | | | Unsigned8 |
| | Bit 0 | | DigitalInput01LatchQuitt |
| | Bit 1 | | DigitalInput02LatchQuitt |
| Acyclic registers | | | |
| Registeraddress (hex) | | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | | 18 | 1 |

5.1. X20DI2653

HwId: 0x2544

Functionmodel: 0

Description: 2 Digital Inputs, 100-240VAC

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DI2653_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 7 | PowerSupply |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI4371

HwId: 0x1B92

Functionmodel: 0

Description: 4 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DI4371_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI4372

HwId: 0x22A8

Functionmodel: 0

Description: 4 Digital Inputs 24 VDC, Source

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DI4372_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI4375

HwId: 0xA911

Functionmodel: 0

Description: 4 Digital Inputs 24 VDC, Wire monitoring

| Input | | |
|-----------------------|------------|---------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DI4375_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | StateDigitalInput01 |
| | Bit 5 | StateDigitalInput02 |
| | Bit 6 | StateDigitalInput03 |
| | Bit 7 | StateDigitalInput04 |
| SampleTimeStamp | Unsigned32 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0802 | 2050 | 2 |
| 0x0805 | 2053 | 1 |

5.1. X20DI4653

HwId: 0x2545

Functionmodel: 0

Description: 4 Digital Inputs, 100-240VAC

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DI4653_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 7 | PowerSupply |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI4760

HwId: 0x2105

Functionmodel: 0

Description: 4x Namur IN, 8.05 VDC

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DI4760_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| Inputs_X20DI4760_b | Unsigned8 | |
| | Bit 6 | OpenLine03 |
| | Bit 7 | OpenLine04 |
| | Bit 2 | ShortCircuit03 |
| | Bit 3 | ShortCircuit04 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |

5.1. X20DI6371

HwId: 0x1B93

Functionmodel: 0

Description: 6 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DI6371_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI6372

HwId: 0x1B94
Functionmodel: 0
Description: 6 Digital Inputs 24 VDC, Source, IEC 61131-2, Type 1

| Input | | |
|--------------------|-------|----------------|
| Name | | Output |
| ModuleOK | | Unsigned8 |
| Inputs_X20DI6372_a | | Unsigned8 |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| Output | | |
| Name | | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI6373

HwId: 0xA7A2
Functionmodel: 0
Description: 6 Digital Inputs 24 VDC electrically isolated, Sink/Source

| Input | | |
|--------------------|-------|----------------|
| Name | | Output |
| ModuleOK | | Unsigned8 |
| Inputs_X20DI6373_a | | Unsigned8 |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| Output | | |
| Name | | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI6553

HwId: 0x256F

Functionmodel: 0

Description: 6 Digital Inputs, 100-120VAC

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DI6553_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 7 | PowerSupply |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI8371

HwId: 0xA4AB

Functionmodel: 0

Description: 8 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | |
|-----------------------|-------|----------------|
| Name | | Output |
| ModuleOK | | Unsigned8 |
| Inputs_X20DI8371_a | | Unsigned8 |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Output | | |
| Name | | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI9371

HwId: 0x1B95

Functionmodel: 0

Description: 12 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DI9371_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Inputs_X20DI9371_b | Unsigned8 | |
| | Bit 0 | DigitalInput09 |
| | Bit 1 | DigitalInput10 |
| | Bit 2 | DigitalInput11 |
| | Bit 3 | DigitalInput12 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DI9372

HwId: 0x1D28

Functionmodel: 0

Description: 12 Digital Inputs 24 VDC, Source

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DI9372_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Inputs_X20DI9372_b | Unsigned8 | |
| | Bit 0 | DigitalInput09 |
| | Bit 1 | DigitalInput10 |
| | Bit 2 | DigitalInput11 |
| | Bit 3 | DigitalInput12 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DID371

HwId: 0xC0E7

Functionmodel: 0

Description: 8 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | |
|-----------------------|-------|----------------|
| Name | | Input |
| ModuleOK | | Unsigned8 |
| Inputs_X20DID371_a | | Unsigned8 |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Output | | |
| Name | | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X20DIF371

HwId: 0xC0E8

Functionmodel: 0

Description: 16 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DIF371_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Inputs_X20DIF371_b | Unsigned8 | |
| | Bit 0 | DigitalInput09 |
| | Bit 1 | DigitalInput10 |
| | Bit 2 | DigitalInput11 |
| | Bit 3 | DigitalInput12 |
| | Bit 4 | DigitalInput13 |
| | Bit 5 | DigitalInput14 |
| | Bit 6 | DigitalInput15 |
| | Bit 7 | DigitalInput16 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

5.1. X67DI1371

HwId: 0x1434

Functionmodel: 0

Description: 8 Inputs 24 VDC, 1 ms

| Input | | | |
|--------------------|-------|----------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X67DI1371_a | | Unsigned8 | |
| | Bit 0 | DigitalInput01 | |
| | Bit 1 | DigitalInput02 | |
| | Bit 2 | DigitalInput03 | |
| | Bit 3 | DigitalInput04 | |
| | Bit 4 | DigitalInput05 | |
| | Bit 5 | DigitalInput06 | |
| | Bit 6 | DigitalInput07 | |
| | Bit 7 | DigitalInput08 | |

Output

| Name | | Input | Output |
|-----------------------|--|-------|--------|
| No Output Data | | | |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

5.1. X67DI1371.L08

HwId: 0x1A1A

Functionmodel: 0

Description: 16 Inputs 24 VDC, 1 ms

| Input | | |
|------------------------|-----------------------|-----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DI1371.L08_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Inputs_X67DI1371.L08_b | Unsigned8 | |
| | Bit 0 | DigitalInput09 |
| | Bit 1 | DigitalInput10 |
| | Bit 2 | DigitalInput11 |
| | Bit 3 | DigitalInput12 |
| | Bit 4 | DigitalInput13 |
| | Bit 5 | DigitalInput14 |
| | Bit 6 | DigitalInput15 |
| | Bit 7 | DigitalInput16 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |
| | | |
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

5.1. X67DI1371.L12

HwId: 0x1A1B

Functionmodel: 0

Description: 16 Inputs 24 VDC, 1 ms

| Input | | |
|------------------------|-----------------------|-----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DI1371.L12_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Inputs_X67DI1371.L12_b | Unsigned8 | |
| | Bit 0 | DigitalInput09 |
| | Bit 1 | DigitalInput10 |
| | Bit 2 | DigitalInput11 |
| | Bit 3 | DigitalInput12 |
| | Bit 4 | DigitalInput13 |
| | Bit 5 | DigitalInput14 |
| | Bit 6 | DigitalInput15 |
| | Bit 7 | DigitalInput16 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |
| | | |
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

5.1. X67DI1372

HwId: 0xC5E9

Functionmodel: 0

Description: 8 Inputs 24 VDC, 1 ms

| Input | | | |
|-----------------------|-------|----------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X67DI1372_a | | Unsigned8 | |
| | Bit 0 | DigitalInput01 | |
| | Bit 1 | DigitalInput02 | |
| | Bit 2 | DigitalInput03 | |
| | Bit 3 | DigitalInput04 | |
| | Bit 4 | DigitalInput05 | |
| | Bit 5 | DigitalInput06 | |
| | Bit 6 | DigitalInput07 | |
| | Bit 7 | DigitalInput08 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6. Digital Output Module

6.1. X20cDO2633

HwId: 0xE680

Functionmodel: 0

Description: X20c 2 Outputs 48 - 240 VAC / 2 A

| Input | | | |
|---------------------|-------|---------------------|-----------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20cDO2633_a | | Unsigned8 | |
| | Bit 0 | LowCurrentStatus1 | |
| | Bit 1 | LowCurrentStatus2 | |
| | Bit 4 | ZeroCrossingInput | |
| | Bit 6 | ZeroCrossingWarning | |
| | Bit 7 | ZeroCrossingStatus | |
| Output | | | |
| Name | | Input | Output |
| AnalogOutput01 | | | Unsigned8 |
| AnalogOutput02 | | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |
| 0x0016 | 22 | 1 |
| 0x001D | 29 | 1 |
| 0x001C | 28 | 1 |

6.1. X20cDO2633_C1

HwId: 0xE680

Functionmodel: 2

Description: X20c 2 Outputs 48 - 240 VAC / 2 A

| Input | | |
|------------------------|-----------|--------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20cDO2633_C1_a | Unsigned8 | |
| | Bit 0 | LowCurrentStatus1 |
| | Bit 1 | LowCurrentStatus2 |
| | Bit 4 | ZeroCrossingInput |
| | Bit 7 | ZeroCrossingStatus |

| Output | | |
|----------------|-------|-----------|
| Name | Input | Output |
| AnalogOutput01 | | Unsigned8 |
| AnalogOutput02 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |
| 0x0016 | 22 | 1 |
| 0x001D | 29 | 1 |
| 0x0012 | 18 | 2 |
| 0x001C | 28 | 1 |

6.1. X20cDO4332

HwId: 0xE227

Functionmodel: 0

Description: Coated 4 Outputs 24 VDC / 2 A, Source

| Input | | | |
|----------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20cDO4332_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20cDO4332_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20cDO4633

HwId: 0xE67D

Functionmodel: 0

Description: X20c 4 Outputs 48 - 240 VAC / 1 A

| Input | | |
|---------------------|-----------|---------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20cDO4633_a | Unsigned8 | |
| | Bit 0 | LowCurrentStatus1 |
| | Bit 1 | LowCurrentStatus2 |
| | Bit 2 | LowCurrentStatus3 |
| | Bit 3 | LowCurrentStatus4 |
| | Bit 4 | ZeroCrossingInput |
| | Bit 6 | ZeroCrossingWarning |
| | Bit 7 | ZeroCrossingStatus |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Unsigned8 |
| AnalogOutput02 | | Unsigned8 |
| AnalogOutput03 | | Unsigned8 |
| AnalogOutput04 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |
| 0x0016 | 22 | 1 |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 1 |
| 0x001D | 29 | 1 |
| 0x001C | 28 | 1 |

6.1. X20cDO4633_C1

HwId: 0xE67D

Functionmodel: 2

Description: X20c 4 Outputs 48 - 240 VAC / 1 A

| Input | | |
|------------------------|-----------|--------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20cDO4633_C1_a | Unsigned8 | |
| | Bit 0 | LowCurrentStatus1 |
| | Bit 1 | LowCurrentStatus2 |
| | Bit 2 | LowCurrentStatus3 |
| | Bit 3 | LowCurrentStatus4 |
| | Bit 4 | ZeroCrossingInput |
| | Bit 7 | ZeroCrossingStatus |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Unsigned8 |
| AnalogOutput02 | | Unsigned8 |
| AnalogOutput03 | | Unsigned8 |
| AnalogOutput04 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |
| 0x0016 | 22 | 1 |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 1 |
| 0x001D | 29 | 1 |
| 0x0012 | 18 | 2 |
| 0x001C | 28 | 1 |

6.1. X20cDO6321

HwId: 0xE228

Functionmodel: 0

Description: Coated 6 Outputs 24 VDC / 0.5 A, Sink

| Input | | | |
|----------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20cDO6321_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20cDO6321_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20cDO6639

HwId: 0xE22A
Functionmodel: 0
Description: Coated 6 Outputs, 30 VDC / 250 VAC / 2 A, N.O

| Input | | | |
|----------------------|-------|-----------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20cDO6639_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20cDO8331

HwId: 0xE22B

Functionmodel: 0

Description: Coated 8 Outputs 24 VDC / 2 A, Sink

| Input | | | |
|----------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20cDO8331_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20cDO8331_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20cDO9321

HwId: 0xE22D

Functionmodel: 0

Description: Coated 12 Outputs 24 VDC / 0.5 A, Sink

| Input | | | |
|----------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20cDO9321_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Inputs_X20cDO9321_b | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput09 | |
| | Bit 1 | StatusDigitalOutput10 | |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20cDO9321_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X20cDO9321_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

6.1. X20cDO9322

HwId: 0xD578

Functionmodel: 0

Description: Coated 12 Outputs 24 VDC / 0.5 A

| Input | | | |
|----------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20cDO9322_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Inputs_X20cDO9322_b | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput09 | |
| | Bit 1 | StatusDigitalOutput10 | |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20cDO9322_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X20cDO9322_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

6.1. X20cDOF322

HwId: 0xDD4C

Functionmodel: 0

Description: X20c 16x dig. Output 24 VDC / 0.5 A

| Input | | | |
|----------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20cDOF322_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Inputs_X20cDOF322_b | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput09 | |
| | Bit 1 | StatusDigitalOutput10 | |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| | Bit 4 | StatusDigitalOutput13 | |
| | Bit 5 | StatusDigitalOutput14 | |
| | Bit 6 | StatusDigitalOutput15 | |
| | Bit 7 | StatusDigitalOutput16 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20cDOF322_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X20cDOF322_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

6.1. X20DO2321

HwId: 0x22B3

Functionmodel: 0

Description: 2 Outputs 24 VDC / 0.5 A, Sink

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO2321_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO2321_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO2322

HwId: 0x1B96

Functionmodel: 0

Description: 2 Outputs 24 VDC / 0.5 A

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO2322_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO2322_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO2623

HwId: 0x267B

Functionmodel: 0

Description: 2 Outputs 100 - 240 VAC / 1 A

| Input | | | |
|---------------------|-------|--------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO2623_a | | Unsigned8 | |
| | Bit 0 | ZeroCrossingInput | |
| | Bit 4 | ZeroCrossingStatus | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO2623_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x001C | 28 | 1 |

6.1. X20DO2633

HwId: 0xAC39

Functionmodel: 0

Description: X20 2 Outputs 48 - 240 VAC / 2 A

| Input | | |
|--------------------|-----------|---------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DO2633_a | Unsigned8 | |
| | Bit 0 | LowCurrentStatus1 |
| | Bit 1 | LowCurrentStatus2 |
| | Bit 4 | ZeroCrossingInput |
| | Bit 6 | ZeroCrossingWarning |
| | Bit 7 | ZeroCrossingStatus |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Unsigned8 |
| AnalogOutput02 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |
| 0x0016 | 22 | 1 |
| 0x001D | 29 | 1 |
| 0x001C | 28 | 1 |

6.1. X20DO2633_C1

HwId: 0xAC39

Functionmodel: 2

Description: X20 2 Outputs 48 - 240 VAC / 2 A

| Input | | |
|-----------------------|-----------|--------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DO2633_C1_a | Unsigned8 | |
| | Bit 0 | LowCurrentStatus1 |
| | Bit 1 | LowCurrentStatus2 |
| | Bit 4 | ZeroCrossingInput |
| | Bit 7 | ZeroCrossingStatus |

| Output | | |
|----------------|-------|-----------|
| Name | Input | Output |
| AnalogOutput01 | | Unsigned8 |
| AnalogOutput02 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |
| 0x0016 | 22 | 1 |
| 0x001D | 29 | 1 |
| 0x0012 | 18 | 2 |
| 0x001C | 28 | 1 |

6.1. X20DO2649

HwId: 0x20DA

Functionmodel: 0

Description: 2 Outputs, 30 VDC / 250 VAC / 5 A, C.O.

| Input | | |
|---------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DO2649_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO4321

HwId: 0x22B4

Functionmodel: 0

Description: 4 Outputs 24 VDC / 0.5 A, Sink

| Input | | | |
|--------------------|-------|-----------------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO4321_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |

| Output | | | |
|---------------------|-------|-------|-----------------|
| Name | | Input | Output |
| Outputs_X20DO4321_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO4322

HwId: 0x1B97

Functionmodel: 0

Description: 4 Outputs 24 VDC / 0.5 A

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO4322_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO4322_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO4331

HwId: 0x22B5

Functionmodel: 0

Description: 4 Outputs 24 VDC / 2 A, Sink

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO4331_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO4331_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO4332

HwId: 0x1B9C

Functionmodel: 0

Description: 4 Outputs 24 VDC / 2 A

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO4332_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO4332_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO4529

HwId: 0x20D9

Functionmodel: 0

Description: 4 Outputs, 30 VDC / 1 A, 115 VAC / 0.5 A, C.O.

| Input | | | |
|---------------------|-------|-----------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO4529_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO4613

HwId: 0xAD05

Functionmodel: 0

Description: X20 4 Outputs for ext. Triac, 48 - 240 VAC / 0.05 A

| Input | | |
|--------------------|-----------|---------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DO4613_a | Unsigned8 | |
| | Bit 4 | ZeroCrossingInput |
| | Bit 6 | ZeroCrossingWarning |
| | Bit 7 | ZeroCrossingStatus |

| Output | | |
|----------------|-------|-----------|
| Name | Input | Output |
| AnalogOutput01 | | Unsigned8 |
| AnalogOutput02 | | Unsigned8 |
| AnalogOutput03 | | Unsigned8 |
| AnalogOutput04 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |
| 0x0016 | 22 | 1 |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 1 |
| 0x001D | 29 | 1 |
| 0x001C | 28 | 1 |

6.1. X20DO4613_C1

HwId: 0xAD05

Functionmodel: 2

Description: X20 4 Outputs for ext. Triac, 48 - 240 VAC / 0.05 A

| Input | | |
|-----------------------|-----------|--------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DO4613_C1_a | Unsigned8 | |
| | Bit 4 | ZeroCrossingInput |
| | Bit 7 | ZeroCrossingStatus |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Unsigned8 |
| AnalogOutput02 | | Unsigned8 |
| AnalogOutput03 | | Unsigned8 |
| AnalogOutput04 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |
| 0x0016 | 22 | 1 |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 1 |
| 0x001D | 29 | 1 |
| 0x0012 | 18 | 2 |
| 0x001C | 28 | 1 |

6.1. X20DO4623

HwId: 0x267C

Functionmodel: 0

Description: 4 Outputs 100 - 240 VAC / 0,5 A

| Input | | |
|-----------------------|-----------------------|-----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DO4623_a | Unsigned8 | |
| | Bit 0 | ZeroCrossingInput |
| | Bit 4 | ZeroCrossingStatus |
| Output | | |
| Name | Input | Output |
| Outputs_X20DO4623_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x001C | 28 | 1 |

6.1. X20DO4633

HwId: 0xAC3A

Functionmodel: 0

Description: X20 4 Outputs 48 - 240 VAC / 1 A

| Input | | |
|--------------------|-----------|---------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DO4633_a | Unsigned8 | |
| | Bit 0 | LowCurrentStatus1 |
| | Bit 1 | LowCurrentStatus2 |
| | Bit 2 | LowCurrentStatus3 |
| | Bit 3 | LowCurrentStatus4 |
| | Bit 4 | ZeroCrossingInput |
| | Bit 6 | ZeroCrossingWarning |
| | Bit 7 | ZeroCrossingStatus |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Unsigned8 |
| AnalogOutput02 | | Unsigned8 |
| AnalogOutput03 | | Unsigned8 |
| AnalogOutput04 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |
| 0x0016 | 22 | 1 |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 1 |
| 0x001D | 29 | 1 |
| 0x001C | 28 | 1 |

6.1. X20DO4633_C1

HwId: 0xAC3A

Functionmodel: 2

Description: X20 4 Outputs 48 - 240 VAC / 1 A

| Input | | |
|-----------------------|-----------|--------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DO4633_C1_a | Unsigned8 | |
| | Bit 0 | LowCurrentStatus1 |
| | Bit 1 | LowCurrentStatus2 |
| | Bit 2 | LowCurrentStatus3 |
| | Bit 3 | LowCurrentStatus4 |
| | Bit 4 | ZeroCrossingInput |
| | Bit 7 | ZeroCrossingStatus |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Unsigned8 |
| AnalogOutput02 | | Unsigned8 |
| AnalogOutput03 | | Unsigned8 |
| AnalogOutput04 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |
| 0x0016 | 22 | 1 |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 1 |
| 0x001D | 29 | 1 |
| 0x0012 | 18 | 2 |
| 0x001C | 28 | 1 |

6.1. X20DO4649

HwId: 0xA704

Functionmodel: 0

Description: 4 Outputs, 30 VDC / 250 VAC / 5 A, N.O.

| Input | | | |
|---------------------|-------|-----------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO4649_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO6321

HwId: 0x1B99

Functionmodel: 0

Description: 6 Outputs 24 VDC / 0.5 A, Sink

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO6321_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO6321_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO6322

HwId: 0x1B98

Functionmodel: 0

Description: 6 Outputs 24 VDC / 0.5 A

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO6322_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO6322_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO6325

HwId: 0xE284

Functionmodel: 0

Description: 6 Outputs 24 VDC / 0.5 A, source, Monitoring

| Input | | |
|---------------------|--------------------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DO6325_a | Unsigned8 | |
| Bit 0 | DigitalStatusGnd01 | |
| Bit 1 | DigitalStatusGnd02 | |
| Bit 2 | DigitalStatusGnd03 | |
| Bit 3 | DigitalStatusGnd04 | |
| Bit 4 | DigitalStatusGnd05 | |
| Bit 5 | DigitalStatusGnd06 | |
| Inputs_X20DO6325_b | Unsigned8 | |
| Bit 0 | DigitalStatusVcc01 | |
| Bit 1 | DigitalStatusVcc02 | |
| Bit 2 | DigitalStatusVcc03 | |
| Bit 3 | DigitalStatusVcc04 | |
| Bit 4 | DigitalStatusVcc05 | |
| Bit 5 | DigitalStatusVcc06 | |
| Inputs_X20DO6325_c | Unsigned8 | |
| Bit 0 | DigitalStatusBw01 | |
| Bit 1 | DigitalStatusBw02 | |
| Bit 2 | DigitalStatusBw03 | |
| Bit 3 | DigitalStatusBw04 | |
| Bit 4 | DigitalStatusBw05 | |
| Bit 5 | DigitalStatusBw06 | |
| Inputs_X20DO6325_d | Unsigned8 | |
| Bit 0 | DigitalStatusSum01 | |
| Bit 1 | DigitalStatusSum02 | |
| Bit 2 | DigitalStatusSum03 | |
| Bit 3 | DigitalStatusSum04 | |
| Bit 4 | DigitalStatusSum05 | |
| Bit 5 | DigitalStatusSum06 | |
| Bit 7 | PowerSupply01 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DO6325_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0004 | 4 | 1 |

6.1. X20DO6529

HwId: 0x2019

Functionmodel: 0

Description: 6 Outputs, 30 VDC / 1 A, 115 VAC / 0.5 A, N.O

| Input | | | |
|---------------------|-------|-----------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO6529_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO6639

HwId: 0xDF50
Functionmodel: 0
Description: 6 Outputs, 30 VDC / 250 VAC / 2 A, N.O

| Input | | | |
|---------------------|-------|-----------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO6639_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO8232

HwId: 0xA4AD

Functionmodel: 0

Description: 8 Outputs 12 VDC / 2 A

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO8232_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO8232_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO8232_C1

HwId: 0xA4AD

Functionmodel: 1

Description: 8 Outputs 12 VDC / 2 A

| Input | | |
|------------------------|-----------------------|----------------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DO8232_C1_a | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DO8232_C1_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |
| Outputs_X20DO8232_C1_b | | Unsigned8 |
| Bit 0 | | DigitalOutput01Delayed |
| Bit 1 | | DigitalOutput02Delayed |
| Bit 2 | | DigitalOutput03Delayed |
| Bit 3 | | DigitalOutput04Delayed |
| Bit 4 | | DigitalOutput05Delayed |
| Bit 5 | | DigitalOutput06Delayed |
| Bit 6 | | DigitalOutput07Delayed |
| Bit 7 | | DigitalOutput08Delayed |
| Outputs_X20DO8232_C1_c | | Unsigned8 |
| Bit 0 | | DigitalOutput01DelayEnable |
| Bit 1 | | DigitalOutput02DelayEnable |
| Bit 2 | | DigitalOutput03DelayEnable |
| Bit 3 | | DigitalOutput04DelayEnable |
| Bit 4 | | DigitalOutput05DelayEnable |
| Bit 5 | | DigitalOutput06DelayEnable |
| Bit 6 | | DigitalOutput07DelayEnable |
| Bit 7 | | DigitalOutput08DelayEnable |
| OutputDelayTime | | Unsigned8 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

| |
|------------------------|
| No acyclic Data |
|------------------------|

6.1. X20DO8322

HwId: 0xA4AC

Functionmodel: 0

Description: 8 Outputs 24 VDC / 0.5 A

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO8322_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO8322_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO8323

HwId: 0xDF4E

Functionmodel: 0

Description: 8 Outputs 24 VDC / 0.5 A, sink/source

| Input | | |
|---------------------|-----------------------|---------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DO8323_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X20DO8323_b | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Inputs_X20DO8323_c | Unsigned8 | |
| Bit 0 | StatusDigitalOutputs | |
| Bit 4 | StatusSupplyLO | |
| Bit 5 | StatusSupplyHI | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DO8323_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |
| Outputs_X20DO8323_b | | Unsigned8 |
| Bit 0 | | EnabDigitalOutput01 |
| Bit 1 | | EnabDigitalOutput02 |
| Bit 2 | | EnabDigitalOutput03 |
| Bit 3 | | EnabDigitalOutput04 |
| Bit 4 | | EnabDigitalOutput05 |
| Bit 5 | | EnabDigitalOutput06 |
| Bit 6 | | EnabDigitalOutput07 |
| Bit 7 | | EnabDigitalOutput08 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

6.1. X20DO8331

HwId: 0x22EB

Functionmodel: 0

Description: 8 Outputs 24 VDC / 2 A, Sink

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO8331_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO8331_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO8331_C1

HwId: 0x22EB

Functionmodel: 1

Description: 8 Outputs 24 VDC / 2 A, Sink

| Input | | |
|------------------------|-----------|----------------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DO8331_C1_a | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 |
| | Bit 1 | StatusDigitalOutput02 |
| | Bit 2 | StatusDigitalOutput03 |
| | Bit 3 | StatusDigitalOutput04 |
| | Bit 4 | StatusDigitalOutput05 |
| | Bit 5 | StatusDigitalOutput06 |
| | Bit 6 | StatusDigitalOutput07 |
| | Bit 7 | StatusDigitalOutput08 |
| Output | | |
| Name | Input | Output |
| Outputs_X20DO8331_C1_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |
| Outputs_X20DO8331_C1_b | | Unsigned8 |
| | Bit 0 | DigitalOutput01Delayed |
| | Bit 1 | DigitalOutput02Delayed |
| | Bit 2 | DigitalOutput03Delayed |
| | Bit 3 | DigitalOutput04Delayed |
| | Bit 4 | DigitalOutput05Delayed |
| | Bit 5 | DigitalOutput06Delayed |
| | Bit 6 | DigitalOutput07Delayed |
| | Bit 7 | DigitalOutput08Delayed |
| Outputs_X20DO8331_C1_c | | Unsigned8 |
| | Bit 0 | DigitalOutput01DelayEnable |
| | Bit 1 | DigitalOutput02DelayEnable |
| | Bit 2 | DigitalOutput03DelayEnable |
| | Bit 3 | DigitalOutput04DelayEnable |
| | Bit 4 | DigitalOutput05DelayEnable |
| | Bit 5 | DigitalOutput06DelayEnable |
| | Bit 6 | DigitalOutput07DelayEnable |
| | Bit 7 | DigitalOutput08DelayEnable |
| OutputDelayTime | | Unsigned8 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO8332

HwId: 0x1B9D

Functionmodel: 0

Description: 8 Outputs 24 VDC / 2 A

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO8332_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO8332_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DO8332_C1

HwId: 0x1B9D

Functionmodel: 1

Description: 8 Outputs 24 VDC / 2 A

| Input | | |
|------------------------|-----------|----------------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DO8332_C1_a | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 |
| | Bit 1 | StatusDigitalOutput02 |
| | Bit 2 | StatusDigitalOutput03 |
| | Bit 3 | StatusDigitalOutput04 |
| | Bit 4 | StatusDigitalOutput05 |
| | Bit 5 | StatusDigitalOutput06 |
| | Bit 6 | StatusDigitalOutput07 |
| | Bit 7 | StatusDigitalOutput08 |
| Output | | |
| Name | Input | Output |
| Outputs_X20DO8332_C1_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |
| Outputs_X20DO8332_C1_b | | Unsigned8 |
| | Bit 0 | DigitalOutput01Delayed |
| | Bit 1 | DigitalOutput02Delayed |
| | Bit 2 | DigitalOutput03Delayed |
| | Bit 3 | DigitalOutput04Delayed |
| | Bit 4 | DigitalOutput05Delayed |
| | Bit 5 | DigitalOutput06Delayed |
| | Bit 6 | DigitalOutput07Delayed |
| | Bit 7 | DigitalOutput08Delayed |
| Outputs_X20DO8332_C1_c | | Unsigned8 |
| | Bit 0 | DigitalOutput01DelayEnable |
| | Bit 1 | DigitalOutput02DelayEnable |
| | Bit 2 | DigitalOutput03DelayEnable |
| | Bit 3 | DigitalOutput04DelayEnable |
| | Bit 4 | DigitalOutput05DelayEnable |
| | Bit 5 | DigitalOutput06DelayEnable |
| | Bit 6 | DigitalOutput07DelayEnable |
| | Bit 7 | DigitalOutput08DelayEnable |
| OutputDelayTime | | Unsigned8 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

6.1. X20DO9321

HwId: 0x1B9B

Functionmodel: 0

Description: 12 Outputs 24 VDC / 0.5 A, Sink

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO9321_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Inputs_X20DO9321_b | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput09 | |
| | Bit 1 | StatusDigitalOutput10 | |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO9321_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X20DO9321_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

6.1. X20DO9322

HwId: 0x1B9A

Functionmodel: 0

Description: 12 Outputs 24 VDC / 0.5 A

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DO9322_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Inputs_X20DO9322_b | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput09 | |
| | Bit 1 | StatusDigitalOutput10 | |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DO9322_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X20DO9322_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

6.1. X20DOD322

HwId: 0xC0E9

Functionmodel: 0

Description: 8 Outputs 24 VDC / 0.5 A

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DOD322_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DOD322_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X20DOF322

HwId: 0xC0EA

Functionmodel: 0

Description: 16 Outputs 24 VDC / 0.5 A

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20DOF322_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Inputs_X20DOF322_b | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput09 | |
| | Bit 1 | StatusDigitalOutput10 | |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| | Bit 4 | StatusDigitalOutput13 | |
| | Bit 5 | StatusDigitalOutput14 | |
| | Bit 6 | StatusDigitalOutput15 | |
| | Bit 7 | StatusDigitalOutput16 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20DOF322_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X20DOF322_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

6.1. X67DO1332

HwId: 0x1467

Functionmodel: 0

Description: 8 Outputs 24 VDC / 2 A

| Input | | | |
|---------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X67DO1332_a | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X67DO1332_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

6.1. X67DO9332.L12

HwId: 0x2658

Functionmodel: 0

Description: 8 Outputs 24 VDC / 2 A

| Input | | | |
|-------------------------|-------|-----------------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X67DO9332.L12_a | | Unsigned8 | |
| | Bit 0 | StatusSupplyOutput01 | |
| | Bit 1 | StatusSupplyOutput02 | |
| | Bit 2 | StatusSupplyOutput03 | |
| | Bit 3 | StatusSupplyOutput04 | |
| | Bit 4 | StatusSupplyOutput05 | |
| | Bit 5 | StatusSupplyOutput06 | |
| | Bit 6 | StatusSupplyOutput07 | |
| | Bit 7 | StatusSupplyOutput08 | |
| Inputs_X67DO9332.L12_b | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X67DO9332.L12_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

7. Mixed Module

7.1. X20CM0985

HwId: 0x2433

Functionmodel: 0

Description: Energiemessmodul , 13 AI, 6 DO

| Input | | |
|--------------------|------------|---------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| AnalogInput05 | Integer16 | |
| AnalogInput06 | Unsigned16 | |
| AnalogInput07 | Integer16 | |
| AnalogInput08 | Integer16 | |
| AnalogInput09 | Integer16 | |
| AnalogInput10 | Integer16 | |
| AnalogInput11 | Integer16 | |
| AnalogInput12 | Integer16 | |
| AnalogInput13 | Integer16 | |
| AnalogInput14 | Integer16 | |
| AnalogInput15 | Integer16 | |
| AnalogInput16 | Integer16 | |
| AnalogInput17 | Integer16 | |
| AnalogInput18 | Integer16 | |
| AnalogInput19 | Integer16 | |
| AnalogInput20 | Integer16 | |
| AnalogInput21 | Integer16 | |
| AnalogInput22 | Integer16 | |
| AnalogInput23 | Integer16 | |
| AnalogInput24 | Unsigned16 | |
| AnalogInput25 | Integer16 | |
| AnalogInput26 | Integer16 | |
| AnalogInput27 | Unsigned16 | |
| AnalogInput28 | Unsigned16 | |
| AnalogInput29 | Integer16 | |
| AnalogInput30 | Integer16 | |
| Inputs_X20CM0985_a | | Unsigned16 |
| | Bit 8 | StatusInput01 |
| | Bit 9 | StatusInput02 |
| | Bit 10 | StatusInput03 |
| | Bit 11 | StatusInput04 |
| | Bit 12 | StatusInput05 |
| | Bit 13 | StatusInput06 |
| | Bit 14 | StatusInput07 |
| | Bit 15 | StatusInput08 |
| | Bit 0 | StatusInput09 |
| | Bit 1 | StatusInput10 |
| | Bit 2 | StatusInput11 |
| | Bit 3 | StatusInput12 |

| Input | | | |
|---------------------|--------|-----------------------|-----------------|
| Name | | Input | Output |
| | Bit 4 | StatusInput13 | |
| | Bit 5 | StatusInput14 | |
| | Bit 6 | StatusInput15 | |
| Inputs_X20CM0985_b | | Unsigned16 | |
| | Bit 8 | StatusDigitalOutput01 | |
| | Bit 0 | StatusDigitalOutput02 | |
| | Bit 1 | StatusDigitalOutput03 | |
| | Bit 2 | StatusDigitalOutput04 | |
| | Bit 3 | StatusDigitalOutput05 | |
| | Bit 4 | StatusDigitalOutput06 | |
| | Bit 7 | StatusInput16 | |
| | Bit 6 | StatusInput17 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X20CM0985_a | | | Unsigned16 |
| | Bit 8 | | DigitalOutput05 |
| | Bit 9 | | DigitalOutput06 |
| Outputs_X20CM0985_b | | | Unsigned16 |
| | Bit 8 | | ConfigOutput17 |
| | Bit 9 | | ConfigOutput18 |
| | Bit 10 | | ConfigOutput19 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0A5E | 2654 | 2 |
| 0x0A01 | 2561 | 1 |
| 0x0A03 | 2563 | 1 |
| 0x0A05 | 2565 | 1 |
| 0x0A07 | 2567 | 1 |
| 0x0A09 | 2569 | 1 |
| 0x0A0B | 2571 | 1 |
| 0x0A8A | 2698 | 2 |
| 0x0A12 | 2578 | 2 |
| 0x0A16 | 2582 | 2 |
| 0x0A1A | 2586 | 2 |
| 0x0A1E | 2590 | 2 |
| 0x0A22 | 2594 | 2 |
| 0x0A26 | 2598 | 2 |
| 0x0A2A | 2602 | 2 |
| 0x0A2E | 2606 | 2 |
| 0x0A32 | 2610 | 2 |
| 0x0A36 | 2614 | 2 |
| 0x0ADE | 2782 | 2 |
| 0x0AE6 | 2790 | 2 |
| 0x0A62 | 2658 | 2 |
| 0x0A96 | 2710 | 2 |
| 0x0A66 | 2662 | 2 |
| 0x0A9E | 2718 | 2 |
| 0x0A6A | 2666 | 2 |
| 0x0AA6 | 2726 | 2 |
| 0x0A6E | 2670 | 2 |
| 0x0AAE | 2734 | 2 |
| 0x0A72 | 2674 | 2 |
| 0x0AB6 | 2742 | 2 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0AD6 | 2774 | 2 |
| 0x0A76 | 2678 | 2 |
| 0x0ABE | 2750 | 2 |
| 0x0A7A | 2682 | 2 |
| 0x0AC6 | 2758 | 2 |
| 0x0A7E | 2686 | 2 |
| 0x0A82 | 2690 | 2 |
| 0x0A86 | 2694 | 2 |
| 0x0ACE | 2766 | 2 |
| 0x0A42 | 2626 | 2 |
| 0x0A46 | 2630 | 2 |
| 0x0A4A | 2634 | 2 |
| 0x0A4E | 2638 | 2 |
| 0x0A3A | 2618 | 2 |
| 0x0A5A | 2650 | 2 |
| 0x0AEA | 2794 | 2 |
| 0x0AEE | 2798 | 2 |
| 0x0A3E | 2622 | 2 |

7.1. X20CM1201

HwId: 0x21EF

Functionmodel: 0

Description: Counter, 1x AB, 3x Input, 3x Output, 24 VDC

| Input | | |
|------------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| ReadStatus | Unsigned8 | |
| ReadIndex | Unsigned8 | |
| ReadData | Integer32 | |
| Output | | |
| Name | Input | Output |
| SendCommand | | Unsigned8 |
| SendCommandParam | | Unsigned8 |
| SendData | | Integer32 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0082 | 130 | 2 |

7.1. X20CM1941

HwId: 0x1E85
Functionmodel: 0
Description: Resolver

| Input | | |
|----------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Position | Integer32 | |
| StatusInput | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0016 | 22 | 1 |
| 0x0014 | 20 | 2 |

7.1. X20CM8281

HwId: 0x24C3

Functionmodel: 0

Description: 4xDI, 2xDA, 1xAI, 1xAO

| Input | | |
|---------------------|------------|-----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20CM8281_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | StatusDigitalOutput01 |
| | Bit 5 | StatusDigitalOutput02 |
| Counter01 | Unsigned16 | |
| Counter02 | Unsigned16 | |
| AnalogInput01 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20CM8281_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| AnalogOutput01 | | Integer16 |
| Outputs_X20CM8281_b | | Unsigned8 |
| | Bit 4 | ResetCounter01 |
| | Bit 5 | ResetCounter02 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x000C | 12 | 1 |
| 0x0016 | 22 | 1 |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 2 |
| 0x001C | 28 | 2 |

7.1. X20CM8323

HwId: 0x1D43

Functionmodel: 0

Description: 8 channel PWM-Valve switch

| Input | | |
|---------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Unsigned8 | |
| AnalogInput02 | Unsigned8 | |
| AnalogInput03 | Unsigned8 | |
| AnalogInput04 | Unsigned8 | |
| AnalogInput05 | Unsigned8 | |
| AnalogInput06 | Unsigned8 | |
| AnalogInput07 | Unsigned8 | |
| AnalogInput08 | Unsigned8 | |
| AnalogInput09 | Unsigned8 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20CM8323_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |
| StatusOutput01 | | Unsigned16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x000C | 12 | 1 |
| 0x000D | 13 | 1 |
| 0x000E | 14 | 1 |
| 0x000F | 15 | 1 |
| 0x0010 | 16 | 1 |
| 0x0011 | 17 | 1 |
| 0x0012 | 18 | 1 |
| 0x0013 | 19 | 1 |
| 0x0014 | 20 | 1 |
| 0x0015 | 21 | 1 |
| 0x0016 | 22 | 1 |
| 0x0017 | 23 | 1 |
| 0x0018 | 24 | 1 |
| 0x0019 | 25 | 1 |
| 0x001A | 26 | 1 |
| 0x001B | 27 | 1 |
| 0x001C | 28 | 1 |
| 0x001D | 29 | 1 |
| 0x001E | 30 | 1 |
| 0x0026 | 38 | 1 |

7.1. X20CM8323_C1

HwId: 0x1D43

Functionmodel: 0

Description: 8 channel PWM-Valve switch

| Input | | |
|----------------|-----------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Unsigned8 | |
| AnalogInput02 | Unsigned8 | |
| AnalogInput03 | Unsigned8 | |
| AnalogInput04 | Unsigned8 | |
| AnalogInput05 | Unsigned8 | |
| AnalogInput06 | Unsigned8 | |
| AnalogInput07 | Unsigned8 | |
| AnalogInput08 | Unsigned8 | |
| AnalogInput09 | Unsigned8 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| DigitalOutput | | Unsigned8 |
| StatusOutput01 | | Unsigned16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x000C | 12 | 1 |
| 0x000D | 13 | 1 |
| 0x000E | 14 | 1 |
| 0x000F | 15 | 1 |
| 0x0010 | 16 | 1 |
| 0x0011 | 17 | 1 |
| 0x0012 | 18 | 1 |
| 0x0013 | 19 | 1 |
| 0x0014 | 20 | 1 |
| 0x0015 | 21 | 1 |
| 0x0016 | 22 | 1 |
| 0x0017 | 23 | 1 |
| 0x0018 | 24 | 1 |
| 0x0019 | 25 | 1 |
| 0x001A | 26 | 1 |
| 0x001B | 27 | 1 |
| 0x001C | 28 | 1 |
| 0x001D | 29 | 1 |
| 0x001E | 30 | 1 |
| 0x0026 | 38 | 1 |

7.1. X20DC1176

HwId: 0xA706

Functionmodel: 0

Description: Counter, 1x ABR, 5 V, 600 kHz, Wire monitoring

| Input | | |
|---------------------|----------------|----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DC1176_a | Unsigned8 | |
| Bit 0 | PowerSupply01 | |
| Bit 1 | PowerSupply02 | |
| Inputs_X20DC1176_b | Unsigned8 | |
| Bit 0 | BW_Channel_A | |
| Bit 1 | BW_Channel_B | |
| Bit 2 | BW_Channel_R | |
| Inputs_X20DC1176_c | Unsigned8 | |
| Bit 0 | Encoder01_A | |
| Bit 1 | Encoder01_B | |
| Bit 2 | Encoder01_R | |
| Bit 4 | DigitalInput01 | |
| Bit 5 | DigitalInput02 | |
| Encoder01LatchCount | Integer8 | |
| Encoder01 | Integer16 | |
| Encoder01Latch | Integer16 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DC1176_a | | Unsigned8 |
| Bit 0 | | BW_QuitChannel_A |
| Bit 1 | | BW_QuitChannel_B |
| Bit 2 | | BW_QuitChannel_R |
| Outputs_X20DC1176_b | | Unsigned8 |
| Bit 0 | | Encoder01Reset |
| Bit 1 | | Encoder01LatchEnable |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0201 | 513 | 1 |
| 0x0282 | 642 | 2 |
| 0x0301 | 769 | 1 |
| 0x0303 | 771 | 1 |
| 0x0305 | 773 | 1 |
| 0x0309 | 777 | 1 |
| 0x030B | 779 | 1 |
| 0x1801 | 6145 | 1 |
| 0x1803 | 6147 | 1 |
| 0x1805 | 6149 | 1 |
| 0x1807 | 6151 | 1 |
| 0x0334 | 820 | 4 |
| 0x032F | 815 | 1 |
| 0x180F | 6159 | 1 |

7.1. X20DC1198

HwId: 0x1BB0

Functionmodel: 0

Description: Counter, 1x SSI, 5 V, 1 Mbit/s

| Input | | |
|-----------------------|----------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DC1198_a | Unsigned8 | |
| Bit 0 | PowerSupply01 | |
| Bit 1 | PowerSupply02 | |
| Inputs_X20DC1198_b | Unsigned8 | |
| Bit 4 | DigitalInput01 | |
| Bit 5 | DigitalInput02 | |
| Encoder01 | Unsigned32 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0100 | 256 | 1 |
| 0x0102 | 258 | 1 |
| 0x0040 | 64 | 1 |
| 0x0042 | 66 | 1 |
| 0x004A | 74 | 1 |
| 0x0000 | 0 | 1 |
| 0x0002 | 2 | 1 |
| 0x0008 | 8 | 1 |
| 0x000A | 10 | 1 |
| 0x1C00 | 7168 | 2 |
| 0x1C04 | 7172 | 4 |

7.1. X20DC11A6

HwId: 0xB76B

Functionmodel: 0

Description: Counter, 1x ABR, 5 V, 5 MHz, Wire monitoring

| Input | | |
|---------------------|----------------|----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DC11A6_a | Unsigned8 | |
| Bit 0 | PowerSupply01 | |
| Bit 1 | PowerSupply02 | |
| Inputs_X20DC11A6_b | Unsigned8 | |
| Bit 0 | BW_Channel_A | |
| Bit 1 | BW_Channel_B | |
| Bit 2 | BW_Channel_R | |
| Inputs_X20DC11A6_c | Unsigned8 | |
| Bit 0 | Encoder01_A | |
| Bit 1 | Encoder01_B | |
| Bit 2 | Encoder01_R | |
| Bit 4 | DigitalInput01 | |
| Bit 5 | DigitalInput02 | |
| Encoder01LatchCount | Integer8 | |
| Encoder01 | Integer16 | |
| Encoder01Latch | Integer16 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DC11A6_a | | Unsigned8 |
| Bit 0 | | BW_QuitChannel_A |
| Bit 1 | | BW_QuitChannel_B |
| Bit 2 | | BW_QuitChannel_R |
| Outputs_X20DC11A6_b | | Unsigned8 |
| Bit 0 | | Encoder01Reset |
| Bit 1 | | Encoder01LatchEnable |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0201 | 513 | 1 |
| 0x0282 | 642 | 2 |
| 0x0301 | 769 | 1 |
| 0x0303 | 771 | 1 |
| 0x0305 | 773 | 1 |
| 0x0309 | 777 | 1 |
| 0x030B | 779 | 1 |
| 0x1801 | 6145 | 1 |
| 0x1803 | 6147 | 1 |
| 0x1805 | 6149 | 1 |
| 0x1807 | 6151 | 1 |
| 0x0334 | 820 | 4 |
| 0x032F | 815 | 1 |
| 0x180F | 6159 | 1 |

7.1. X20DC11A6_C1

HwId: 0xB76B

Functionmodel: 0

Description: Counter, 1x ABR, 5 V, 5 MHz, Wire monitoring

| Input | | |
|------------------------|----------------|----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DC11A6_C1_a | Unsigned8 | |
| Bit 0 | PowerSupply01 | |
| Bit 1 | PowerSupply02 | |
| Inputs_X20DC11A6_C1_b | Unsigned8 | |
| Bit 0 | BW_Channel_A | |
| Bit 1 | BW_Channel_B | |
| Bit 2 | BW_Channel_R | |
| Inputs_X20DC11A6_C1_c | Unsigned8 | |
| Bit 0 | Encoder01_A | |
| Bit 1 | Encoder01_B | |
| Bit 2 | Encoder01_R | |
| Bit 4 | DigitalInput01 | |
| Bit 5 | DigitalInput02 | |
| Encoder01LatchCount | Integer8 | |
| Encoder01 | Integer32 | |
| Encoder01Latch | Integer32 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DC11A6_C1_a | | Unsigned8 |
| Bit 0 | | BW_QuitChannel_A |
| Bit 1 | | BW_QuitChannel_B |
| Bit 2 | | BW_QuitChannel_R |
| Outputs_X20DC11A6_C1_b | | Unsigned8 |
| Bit 0 | | Encoder01Reset |
| Bit 1 | | Encoder01LatchEnable |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0201 | 513 | 1 |
| 0x0282 | 642 | 2 |
| 0x0301 | 769 | 1 |
| 0x0303 | 771 | 1 |
| 0x0305 | 773 | 1 |
| 0x0309 | 777 | 1 |
| 0x030B | 779 | 1 |
| 0x1801 | 6145 | 1 |
| 0x1803 | 6147 | 1 |
| 0x1805 | 6149 | 1 |
| 0x1807 | 6151 | 1 |
| 0x0334 | 820 | 4 |
| 0x032F | 815 | 1 |
| 0x180F | 6159 | 1 |

7.1. X20DC1376

HwId: 0xA705

Functionmodel: 0

Description: Counter, 1x ABR, 24 V, 100 kHz, Wire monitoring

| Input | | |
|---------------------|----------------|----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DC1376_a | Unsigned8 | |
| Bit 0 | PowerSupply01 | |
| Inputs_X20DC1376_b | Unsigned8 | |
| Bit 0 | BW_Channel_A | |
| Bit 1 | BW_Channel_B | |
| Bit 2 | BW_Channel_R | |
| Inputs_X20DC1376_c | Unsigned8 | |
| Bit 0 | Encoder01_A | |
| Bit 1 | Encoder01_B | |
| Bit 2 | Encoder01_R | |
| Bit 4 | DigitalInput01 | |
| Bit 5 | DigitalInput02 | |
| Encoder01LatchCount | Integer8 | |
| Encoder01 | Integer16 | |
| Encoder01Latch | Integer16 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DC1376_a | | Unsigned8 |
| Bit 0 | | BW_QuitChannel_A |
| Bit 1 | | BW_QuitChannel_B |
| Bit 2 | | BW_QuitChannel_R |
| Outputs_X20DC1376_b | | Unsigned8 |
| Bit 0 | | Encoder01Reset |
| Bit 1 | | Encoder01LatchEnable |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0201 | 513 | 1 |
| 0x0282 | 642 | 2 |
| 0x0301 | 769 | 1 |
| 0x0303 | 771 | 1 |
| 0x0305 | 773 | 1 |
| 0x0309 | 777 | 1 |
| 0x030B | 779 | 1 |
| 0x1801 | 6145 | 1 |
| 0x1803 | 6147 | 1 |
| 0x1805 | 6149 | 1 |
| 0x1807 | 6151 | 1 |
| 0x0334 | 820 | 4 |
| 0x032F | 815 | 1 |
| 0x180F | 6159 | 1 |

7.1. X20DC137A

HwId: 0xDD28

Functionmodel: 0

Description: Counter, 1x ABR, 24 V diff., 300 kHz, Wire monitoring

| Input | | |
|---------------------|----------------|----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DC137A_a | Unsigned8 | |
| Bit 0 | PowerSupply01 | |
| Inputs_X20DC137A_b | Unsigned8 | |
| Bit 0 | BW_Channel_A | |
| Bit 1 | BW_Channel_B | |
| Bit 2 | BW_Channel_R | |
| Inputs_X20DC137A_c | Unsigned8 | |
| Bit 0 | Encoder01_A | |
| Bit 1 | Encoder01_B | |
| Bit 2 | Encoder01_R | |
| Bit 4 | DigitalInput01 | |
| Bit 5 | DigitalInput02 | |
| Encoder01LatchCount | Integer8 | |
| Encoder01 | Integer16 | |
| Encoder01Latch | Integer16 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DC137A_a | | Unsigned8 |
| Bit 0 | | BW_QuitChannel_A |
| Bit 1 | | BW_QuitChannel_B |
| Bit 2 | | BW_QuitChannel_R |
| Outputs_X20DC137A_b | | Unsigned8 |
| Bit 0 | | Encoder01Reset |
| Bit 1 | | Encoder01LatchEnable |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0201 | 513 | 1 |
| 0x0282 | 642 | 2 |
| 0x0301 | 769 | 1 |
| 0x0303 | 771 | 1 |
| 0x0305 | 773 | 1 |
| 0x0309 | 777 | 1 |
| 0x030B | 779 | 1 |
| 0x1801 | 6145 | 1 |
| 0x1803 | 6147 | 1 |
| 0x1805 | 6149 | 1 |
| 0x1807 | 6151 | 1 |
| 0x0334 | 820 | 4 |
| 0x032F | 815 | 1 |
| 0x180F | 6159 | 1 |

7.1. X20DC1396

HwId: 0x1BAC

Functionmodel: 254

Description: Counter, 1x ABR, 24 V, 100 kHz

| Input | | |
|------------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Encoder01 | Integer16 | |
| Inputs_X20DC1396_a | Unsigned8 | |
| | Bit 0 | EncoderA |
| | Bit 1 | EncoderB |
| | Bit 2 | EncoderRefPulse |
| | Bit 3 | DigitalInput01 |
| StatusInput01 | Unsigned8 | |
| Inputs_X20DC1396_b | Unsigned8 | |
| | Bit 0 | PowerSupply01 |
| Output | | |
| Name | Input | Output |
| ReferenceModeEncoder01 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x100A | 4106 | 1 |
| 0x0200 | 512 | 2 |
| 0x0208 | 520 | 1 |
| 0x020A | 522 | 1 |
| 0x0810 | 2064 | 2 |

7.1. X20DC1398

HwId: 0x1BAE

Functionmodel: 0

Description: Counter, 1x SSI, 24 V, 125 kbit/s

| Input | | |
|-----------------------|----------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DC1398_a | Unsigned8 | |
| Bit 0 | PowerSupply01 | |
| Inputs_X20DC1398_b | Unsigned8 | |
| Bit 3 | DigitalInput01 | |
| Encoder01 | Unsigned32 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0100 | 256 | 1 |
| 0x0102 | 258 | 1 |
| 0x0040 | 64 | 1 |
| 0x0042 | 66 | 1 |
| 0x0000 | 0 | 1 |
| 0x0002 | 2 | 1 |
| 0x0006 | 6 | 1 |
| 0x1C00 | 7168 | 2 |
| 0x1C04 | 7172 | 4 |

7.1. X20DC1976

HwId: 0xA707

Functionmodel: 0

Description: Counter, 1x ABR, 5 V single ended, 250 kHz, Wire monitoring

| Input | | |
|---------------------|----------------|----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DC1976_a | Unsigned8 | |
| Bit 0 | PowerSupply01 | |
| Bit 1 | PowerSupply02 | |
| Inputs_X20DC1976_b | Unsigned8 | |
| Bit 0 | BW_Channel_A | |
| Bit 1 | BW_Channel_B | |
| Bit 2 | BW_Channel_R | |
| Inputs_X20DC1976_c | Unsigned8 | |
| Bit 0 | Encoder01_A | |
| Bit 1 | Encoder01_B | |
| Bit 2 | Encoder01_R | |
| Bit 4 | DigitalInput01 | |
| Bit 5 | DigitalInput02 | |
| Encoder01LatchCount | Integer8 | |
| Encoder01 | Integer16 | |
| Encoder01Latch | Integer16 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DC1976_a | | Unsigned8 |
| Bit 0 | | BW_QuitChannel_A |
| Bit 1 | | BW_QuitChannel_B |
| Bit 2 | | BW_QuitChannel_R |
| Outputs_X20DC1976_b | | Unsigned8 |
| Bit 0 | | Encoder01Reset |
| Bit 1 | | Encoder01LatchEnable |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0201 | 513 | 1 |
| 0x0282 | 642 | 2 |
| 0x0301 | 769 | 1 |
| 0x0303 | 771 | 1 |
| 0x0305 | 773 | 1 |
| 0x0309 | 777 | 1 |
| 0x030B | 779 | 1 |
| 0x1801 | 6145 | 1 |
| 0x1803 | 6147 | 1 |
| 0x1805 | 6149 | 1 |
| 0x1807 | 6151 | 1 |
| 0x0334 | 820 | 4 |
| 0x032F | 815 | 1 |
| 0x180F | 6159 | 1 |

7.1. X20DC2395

HwId: 0x1CD4

Functionmodel: 254

Description: 2 Inputs, 2 In- /Outputs 24 VDC; SSI; ABR; PWM

| Input | | |
|----------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| EventCounter01 | Unsigned16 | |
| EventCounter03 | Unsigned16 | |
| PowerSupply01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| PWMOutput02 | | Unsigned16 |
| PWMOutput04 | | Unsigned16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0000 | 0 | 1 |
| 0x0002 | 2 | 1 |
| 0x0004 | 4 | 1 |
| 0x0006 | 6 | 1 |
| 0x0040 | 64 | 1 |
| 0x0042 | 66 | 1 |
| 0x0044 | 68 | 1 |
| 0x0046 | 70 | 1 |
| 0x0808 | 2056 | 1 |
| 0x0908 | 2312 | 1 |
| 0x1800 | 6144 | 2 |
| 0x1810 | 6160 | 2 |

7.1. X20DC2398

HwId: 0x1BAD

Functionmodel: 0

Description: Counter, 2x SSI, 24 V, 125 kbit/s

| Input | | |
|-----------------------|----------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DC2398_a | Unsigned8 | |
| Bit 0 | PowerSupply01 | |
| Inputs_X20DC2398_b | Unsigned8 | |
| Bit 3 | DigitalInput01 | |
| Bit 7 | DigitalInput02 | |
| Encoder01 | Unsigned32 | |
| Encoder02 | Unsigned32 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0100 | 256 | 1 |
| 0x0102 | 258 | 1 |
| 0x0040 | 64 | 1 |
| 0x0042 | 66 | 1 |
| 0x0048 | 72 | 1 |
| 0x004A | 74 | 1 |
| 0x0000 | 0 | 1 |
| 0x0002 | 2 | 1 |
| 0x0006 | 6 | 1 |
| 0x0008 | 8 | 1 |
| 0x000A | 10 | 1 |
| 0x000E | 14 | 1 |
| 0x1C00 | 7168 | 2 |
| 0x1D00 | 7424 | 2 |
| 0x1C04 | 7172 | 4 |
| 0x1D04 | 7428 | 4 |

7.1. X20DC4395

HwId: 0x1CC5

Functionmodel: 254

Description: 4 Inputs, 4 In- /Outputs 24 VDC; SSI; ABR; PWM

| Input | | |
|--------------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| SSIEncoder01 | Unsigned32 | |
| EventCounter03 | Unsigned16 | |
| ABREncoder02 | Integer16 | |
| PowerSupply01 | Unsigned8 | |
| StatusABR02 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| PWMOutput04 | | Unsigned16 |
| PWMOutput08 | | Unsigned16 |
| ReferenceModeABR02 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0000 | 0 | 1 |
| 0x0002 | 2 | 1 |
| 0x0004 | 4 | 1 |
| 0x0006 | 6 | 1 |
| 0x0008 | 8 | 1 |
| 0x000A | 10 | 1 |
| 0x000C | 12 | 1 |
| 0x000E | 14 | 1 |
| 0x0040 | 64 | 1 |
| 0x0042 | 66 | 1 |
| 0x0044 | 68 | 1 |
| 0x0046 | 70 | 1 |
| 0x0048 | 72 | 1 |
| 0x004A | 74 | 1 |
| 0x004C | 76 | 1 |
| 0x004E | 78 | 1 |
| 0x0A10 | 2576 | 2 |
| 0x0A14 | 2580 | 2 |
| 0x0A00 | 2560 | 1 |
| 0x0A08 | 2568 | 1 |
| 0x0A0A | 2570 | 1 |
| 0x0A40 | 2624 | 2 |
| 0x0A48 | 2632 | 2 |
| 0x0220 | 544 | 2 |
| 0x0224 | 548 | 1 |
| 0x100A | 4106 | 1 |
| 0x1008 | 4104 | 1 |
| 0x0908 | 2312 | 1 |
| 0x1C00 | 7168 | 2 |
| 0x1C0C | 7180 | 1 |
| 0x1C04 | 7172 | 4 |
| 0x1810 | 6160 | 2 |
| 0x1830 | 6192 | 2 |

7.1. X20DM9324

HwId: 0x20B9

Functionmodel: 0

Description: 8 Digital Inputs 24 VDC, Sink; 4 Outputs 24 VDC / 0.5 A, Source

| Input | | |
|-----------------------|-----------------------|-----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20DM9324_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X20DM9324_b | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DM9324_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

7.1. X67AM1223

HwId: 0x1465

Functionmodel: 0

Description: 2 Inputs ?10 V, 2 Outputs ?10 V

| Input | | |
|----------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| StatusInput01 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |

7.1. X67AM1323

HwId: 0x1466

Functionmodel: 0

Description: 2 Inputs 0 to 20 mA, 2 Outputs 0 to 20 mA

| Input | | | |
|-----------------------|-------|-----------------------|-----------------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| AnalogInput01 | | Integer16 | |
| AnalogInput02 | | Integer16 | |
| Inputs_X67AM1323_a | | Unsigned8 | |
| | Bit 1 | StatusAnalogInput01 | |
| | Bit 3 | StatusAnalogInput02 | |
| Output | | | |
| Name | | Input | Output |
| AnalogOutput01 | | | Integer16 |
| AnalogOutput02 | | | Integer16 |
| Acyclic registers | | | |
| Registeraddress (hex) | | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | | 16 | 1 |

7.1. X67DC1198

HwId: 0x18D0

Functionmodel: 254

Description: 8 In- / Outputs 24 VDC; 6 In- / Outputs 5 VDC; SSI; PWM

| Input | | |
|------------------|------------|------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| ABRCounter | Unsigned16 | |
| PowerSupplyState | Unsigned8 | |
| ABRState | Unsigned8 | |
| SSIPosition | Unsigned32 | |
| ABCounter1 | Unsigned16 | |
| ABCounter2 | Unsigned16 | |
| ABCounter3 | Unsigned16 | |
| EventCounter | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| PWM1Output | | Unsigned16 |
| ABRReferenceMode | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0C10 | 3088 | 2 |
| 0x0B0A | 2826 | 1 |
| 0x1D08 | 7432 | 2 |
| 0x1810 | 6160 | 2 |
| 0x0200 | 512 | 2 |

7.1. X67DC1198_C1

HwId: 0x18D0

Functionmodel: 0

Description: 8 In- / Outputs 24 VDC; 6 In- / Outputs 5 VDC; SSI; PWM

| Input | | |
|------------------------|------------|-----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DC1198_C1_a | Unsigned16 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | StatusDigitalOutput05 |
| | Bit 5 | StatusDigitalOutput06 |
| | Bit 6 | StatusDigitalOutput07 |
| | Bit 7 | StatusDigitalOutput08 |
| | Bit 9 | StatusDigitalOutput10 |
| | Bit 13 | StatusDigitalOutput14 |
| SSIContector01 | Unsigned32 | |
| SSIContector03 | Unsigned32 | |
| Output | | |
| Name | Input | Output |
| Outputs_X67DC1198_C1_a | | Unsigned16 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |
| | Bit 9 | DigitalOutput10 |
| | Bit 13 | DigitalOutput14 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x100A | 4106 | 2 |
| 0x1008 | 4104 | 2 |
| 0x0102 | 258 | 2 |
| 0x0100 | 256 | 2 |
| 0x0040 | 64 | 1 |
| 0x0042 | 66 | 1 |
| 0x0044 | 68 | 1 |
| 0x0046 | 70 | 1 |
| 0x0048 | 72 | 1 |
| 0x004A | 74 | 1 |
| 0x004C | 76 | 1 |
| 0x004E | 78 | 1 |
| 0x0000 | 0 | 1 |
| 0x0002 | 2 | 1 |
| 0x0004 | 4 | 1 |
| 0x0006 | 6 | 1 |
| 0x0008 | 8 | 1 |
| 0x000A | 10 | 1 |
| 0x000C | 12 | 1 |
| 0x000E | 14 | 1 |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |
| 0x0016 | 22 | 1 |
| 0x0018 | 24 | 1 |
| 0x001A | 26 | 1 |
| 0x001C | 28 | 1 |
| 0x001E | 30 | 1 |
| 0x1C0C | 7180 | 1 |
| 0x1C00 | 7168 | 2 |
| 0x1C04 | 7172 | 4 |
| 0x1D0C | 7436 | 1 |
| 0x1D00 | 7424 | 2 |
| 0x1D04 | 7428 | 4 |

7.1. X67DM1321

HwId: 0x1311

Functionmodel: 2

Description: 8 In- /Outputs 24 VDC

| Input | | | |
|---------------------|-------|-----------------------|------------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X67DM1321_a | | Unsigned8 | |
| | Bit 0 | DigitalInput01 | |
| | Bit 1 | DigitalInput02 | |
| | Bit 2 | DigitalInput03 | |
| | Bit 3 | DigitalInput04 | |
| | Bit 4 | DigitalInput05 | |
| | Bit 5 | DigitalInput06 | |
| | Bit 6 | DigitalInput07 | |
| | Bit 7 | DigitalInput08 | |
| Inputs_X67DM1321_b | | Unsigned8 | |
| | Bit 0 | InputLatch01 | |
| | Bit 1 | InputLatch02 | |
| | Bit 2 | InputLatch03 | |
| | Bit 3 | InputLatch04 | |
| | Bit 4 | InputLatch05 | |
| | Bit 5 | InputLatch06 | |
| | Bit 6 | InputLatch07 | |
| | Bit 7 | InputLatch08 | |
| Inputs_X67DM1321_c | | Unsigned8 | |
| | Bit 0 | StatusDigitalOutput01 | |
| | Bit 1 | StatusDigitalOutput02 | |
| | Bit 2 | StatusDigitalOutput03 | |
| | Bit 3 | StatusDigitalOutput04 | |
| | Bit 4 | StatusDigitalOutput05 | |
| | Bit 5 | StatusDigitalOutput06 | |
| | Bit 6 | StatusDigitalOutput07 | |
| | Bit 7 | StatusDigitalOutput08 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X67DM1321_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X67DM1321_b | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch01 |
| | Bit 1 | | QuitInputLatch02 |
| | Bit 2 | | QuitInputLatch03 |
| | Bit 3 | | QuitInputLatch04 |
| | Bit 4 | | QuitInputLatch05 |
| | Bit 5 | | QuitInputLatch06 |
| | Bit 6 | | QuitInputLatch07 |
| | Bit 7 | | QuitInputLatch08 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |

7.1. X67DM1321.L08

HwId: 0x1A1C

Functionmodel: 2

Description: 16 In- /Outputs 24 VDC

| Input | | |
|------------------------|-----------------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DM1321.L08_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X67DM1321.L08_b | Unsigned8 | |
| Bit 0 | DigitalInput09 | |
| Bit 1 | DigitalInput10 | |
| Bit 2 | DigitalInput11 | |
| Bit 3 | DigitalInput12 | |
| Bit 4 | DigitalInput13 | |
| Bit 5 | DigitalInput14 | |
| Bit 6 | DigitalInput15 | |
| Bit 7 | DigitalInput16 | |
| Inputs_X67DM1321.L08_c | Unsigned8 | |
| Bit 0 | InputLatch01 | |
| Bit 1 | InputLatch02 | |
| Bit 2 | InputLatch03 | |
| Bit 3 | InputLatch04 | |
| Bit 4 | InputLatch05 | |
| Bit 5 | InputLatch06 | |
| Bit 6 | InputLatch07 | |
| Bit 7 | InputLatch08 | |
| Inputs_X67DM1321.L08_d | Unsigned8 | |
| Bit 0 | InputLatch09 | |
| Bit 1 | InputLatch10 | |
| Bit 2 | InputLatch11 | |
| Bit 3 | InputLatch12 | |
| Bit 4 | InputLatch13 | |
| Bit 5 | InputLatch14 | |
| Bit 6 | InputLatch15 | |
| Bit 7 | InputLatch16 | |
| Inputs_X67DM1321.L08_e | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Inputs_X67DM1321.L08_f | Unsigned8 | |
| Bit 0 | StatusDigitalOutput09 | |
| Bit 1 | StatusDigitalOutput10 | |

| Input | | | |
|-------------------------|-------|-----------------------|------------------|
| Name | | Input | Output |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| | Bit 4 | StatusDigitalOutput13 | |
| | Bit 5 | StatusDigitalOutput14 | |
| | Bit 6 | StatusDigitalOutput15 | |
| | Bit 7 | StatusDigitalOutput16 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X67DM1321.L08_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X67DM1321.L08_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |
| Outputs_X67DM1321.L08_c | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch01 |
| | Bit 1 | | QuitInputLatch02 |
| | Bit 2 | | QuitInputLatch03 |
| | Bit 3 | | QuitInputLatch04 |
| | Bit 4 | | QuitInputLatch05 |
| | Bit 5 | | QuitInputLatch06 |
| | Bit 6 | | QuitInputLatch07 |
| | Bit 7 | | QuitInputLatch08 |
| Outputs_X67DM1321.L08_d | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch09 |
| | Bit 1 | | QuitInputLatch10 |
| | Bit 2 | | QuitInputLatch11 |
| | Bit 3 | | QuitInputLatch12 |
| | Bit 4 | | QuitInputLatch13 |
| | Bit 5 | | QuitInputLatch14 |
| | Bit 6 | | QuitInputLatch15 |
| | Bit 7 | | QuitInputLatch16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0011 | 17 | 1 |
| 0x0012 | 18 | 1 |

7.1. X67DM1321.L08_C1

HwId: 0x1A1C

Functionmodel: 1

Description: 16 In- /Outputs 24 VDC

| Input | | |
|---------------------------|-----------------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DM1321.L08_C1_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X67DM1321.L08_C1_b | Unsigned8 | |
| Bit 0 | DigitalInput09 | |
| Bit 1 | DigitalInput10 | |
| Bit 2 | DigitalInput11 | |
| Bit 3 | DigitalInput12 | |
| Bit 4 | DigitalInput13 | |
| Bit 5 | DigitalInput14 | |
| Bit 6 | DigitalInput15 | |
| Bit 7 | DigitalInput16 | |
| Counter01 | Unsigned16 | |
| Counter02 | Unsigned16 | |
| Inputs_X67DM1321.L08_C1_c | Unsigned8 | |
| Bit 0 | InputLatch01 | |
| Bit 1 | InputLatch02 | |
| Bit 2 | InputLatch03 | |
| Bit 3 | InputLatch04 | |
| Bit 4 | InputLatch05 | |
| Bit 5 | InputLatch06 | |
| Bit 6 | InputLatch07 | |
| Bit 7 | InputLatch08 | |
| Inputs_X67DM1321.L08_C1_d | Unsigned8 | |
| Bit 0 | InputLatch09 | |
| Bit 1 | InputLatch10 | |
| Bit 2 | InputLatch11 | |
| Bit 3 | InputLatch12 | |
| Bit 4 | InputLatch13 | |
| Bit 5 | InputLatch14 | |
| Bit 6 | InputLatch15 | |
| Bit 7 | InputLatch16 | |
| Inputs_X67DM1321.L08_C1_e | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Inputs_X67DM1321.L08_C1_f | Unsigned8 | |

| Input | | | |
|----------------------------|-------|-----------------------|------------------|
| Name | | Input | Output |
| | Bit 0 | StatusDigitalOutput09 | |
| | Bit 1 | StatusDigitalOutput10 | |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| | Bit 4 | StatusDigitalOutput13 | |
| | Bit 5 | StatusDigitalOutput14 | |
| | Bit 6 | StatusDigitalOutput15 | |
| | Bit 7 | StatusDigitalOutput16 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X67DM1321.L08_C1_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X67DM1321.L08_C1_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |
| Outputs_X67DM1321.L08_C1_c | | | Unsigned8 |
| | Bit 5 | | ResetCounter01 |
| Outputs_X67DM1321.L08_C1_d | | | Unsigned8 |
| | Bit 5 | | ResetCounter02 |
| Outputs_X67DM1321.L08_C1_e | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch01 |
| | Bit 1 | | QuitInputLatch02 |
| | Bit 2 | | QuitInputLatch03 |
| | Bit 3 | | QuitInputLatch04 |
| | Bit 4 | | QuitInputLatch05 |
| | Bit 5 | | QuitInputLatch06 |
| | Bit 6 | | QuitInputLatch07 |
| | Bit 7 | | QuitInputLatch08 |
| Outputs_X67DM1321.L08_C1_f | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch09 |
| | Bit 1 | | QuitInputLatch10 |
| | Bit 2 | | QuitInputLatch11 |
| | Bit 3 | | QuitInputLatch12 |
| | Bit 4 | | QuitInputLatch13 |
| | Bit 5 | | QuitInputLatch14 |
| | Bit 6 | | QuitInputLatch15 |
| | Bit 7 | | QuitInputLatch16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0011 | 17 | 1 |
| 0x0012 | 18 | 1 |

7.1. X67DM1321.L08_C2

HwId: 0x1A1C

Functionmodel: 1

Description: 16 In- /Outputs 24 VDC

| Input | | |
|---------------------------|-----------------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DM1321.L08_C2_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X67DM1321.L08_C2_b | Unsigned8 | |
| Bit 0 | DigitalInput09 | |
| Bit 1 | DigitalInput10 | |
| Bit 2 | DigitalInput11 | |
| Bit 3 | DigitalInput12 | |
| Bit 4 | DigitalInput13 | |
| Bit 5 | DigitalInput14 | |
| Bit 6 | DigitalInput15 | |
| Bit 7 | DigitalInput16 | |
| Counter01 | Unsigned16 | |
| Counter02 | Unsigned16 | |
| Inputs_X67DM1321.L08_C2_c | Unsigned8 | |
| Bit 0 | InputLatch01 | |
| Bit 1 | InputLatch02 | |
| Bit 2 | InputLatch03 | |
| Bit 3 | InputLatch04 | |
| Bit 4 | InputLatch05 | |
| Bit 5 | InputLatch06 | |
| Bit 6 | InputLatch07 | |
| Bit 7 | InputLatch08 | |
| Inputs_X67DM1321.L08_C2_d | Unsigned8 | |
| Bit 0 | InputLatch09 | |
| Bit 1 | InputLatch10 | |
| Bit 2 | InputLatch11 | |
| Bit 3 | InputLatch12 | |
| Bit 4 | InputLatch13 | |
| Bit 5 | InputLatch14 | |
| Bit 6 | InputLatch15 | |
| Bit 7 | InputLatch16 | |
| Inputs_X67DM1321.L08_C2_e | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Inputs_X67DM1321.L08_C2_f | Unsigned8 | |

| Input | | | |
|----------------------------|-------|-----------------------|------------------|
| Name | | Input | Output |
| | Bit 0 | StatusDigitalOutput09 | |
| | Bit 1 | StatusDigitalOutput10 | |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| | Bit 4 | StatusDigitalOutput13 | |
| | Bit 5 | StatusDigitalOutput14 | |
| | Bit 6 | StatusDigitalOutput15 | |
| | Bit 7 | StatusDigitalOutput16 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X67DM1321.L08_C2_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X67DM1321.L08_C2_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |
| Outputs_X67DM1321.L08_C2_c | | | Unsigned8 |
| | Bit 5 | | ResetCounter02 |
| Outputs_X67DM1321.L08_C2_d | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch01 |
| | Bit 1 | | QuitInputLatch02 |
| | Bit 2 | | QuitInputLatch03 |
| | Bit 3 | | QuitInputLatch04 |
| | Bit 4 | | QuitInputLatch05 |
| | Bit 5 | | QuitInputLatch06 |
| | Bit 6 | | QuitInputLatch07 |
| | Bit 7 | | QuitInputLatch08 |
| Outputs_X67DM1321.L08_C2_e | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch09 |
| | Bit 1 | | QuitInputLatch10 |
| | Bit 2 | | QuitInputLatch11 |
| | Bit 3 | | QuitInputLatch12 |
| | Bit 4 | | QuitInputLatch13 |
| | Bit 5 | | QuitInputLatch14 |
| | Bit 6 | | QuitInputLatch15 |
| | Bit 7 | | QuitInputLatch16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0011 | 17 | 1 |
| 0x0012 | 18 | 1 |

| | | |
|-----------------------|-----------------------|-----------------------|
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |

7.1. X67DM1321.L12

HwId: 0x1A1D

Functionmodel: 2

Description: 16 In- /Outputs 24 VDC

| Input | | |
|------------------------|-----------------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DM1321.L12_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X67DM1321.L12_b | Unsigned8 | |
| Bit 0 | DigitalInput09 | |
| Bit 1 | DigitalInput10 | |
| Bit 2 | DigitalInput11 | |
| Bit 3 | DigitalInput12 | |
| Bit 4 | DigitalInput13 | |
| Bit 5 | DigitalInput14 | |
| Bit 6 | DigitalInput15 | |
| Bit 7 | DigitalInput16 | |
| Inputs_X67DM1321.L12_c | Unsigned8 | |
| Bit 0 | InputLatch01 | |
| Bit 1 | InputLatch02 | |
| Bit 2 | InputLatch03 | |
| Bit 3 | InputLatch04 | |
| Bit 4 | InputLatch05 | |
| Bit 5 | InputLatch06 | |
| Bit 6 | InputLatch07 | |
| Bit 7 | InputLatch08 | |
| Inputs_X67DM1321.L12_d | Unsigned8 | |
| Bit 0 | InputLatch09 | |
| Bit 1 | InputLatch10 | |
| Bit 2 | InputLatch11 | |
| Bit 3 | InputLatch12 | |
| Bit 4 | InputLatch13 | |
| Bit 5 | InputLatch14 | |
| Bit 6 | InputLatch15 | |
| Bit 7 | InputLatch16 | |
| Inputs_X67DM1321.L12_e | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Inputs_X67DM1321.L12_f | Unsigned8 | |
| Bit 0 | StatusDigitalOutput09 | |
| Bit 1 | StatusDigitalOutput10 | |

| Input | | | |
|-------------------------|-------|-----------------------|------------------|
| Name | | Input | Output |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| | Bit 4 | StatusDigitalOutput13 | |
| | Bit 5 | StatusDigitalOutput14 | |
| | Bit 6 | StatusDigitalOutput15 | |
| | Bit 7 | StatusDigitalOutput16 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X67DM1321.L12_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X67DM1321.L12_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |
| Outputs_X67DM1321.L12_c | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch01 |
| | Bit 1 | | QuitInputLatch02 |
| | Bit 2 | | QuitInputLatch03 |
| | Bit 3 | | QuitInputLatch04 |
| | Bit 4 | | QuitInputLatch05 |
| | Bit 5 | | QuitInputLatch06 |
| | Bit 6 | | QuitInputLatch07 |
| | Bit 7 | | QuitInputLatch08 |
| Outputs_X67DM1321.L12_d | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch09 |
| | Bit 1 | | QuitInputLatch10 |
| | Bit 2 | | QuitInputLatch11 |
| | Bit 3 | | QuitInputLatch12 |
| | Bit 4 | | QuitInputLatch13 |
| | Bit 5 | | QuitInputLatch14 |
| | Bit 6 | | QuitInputLatch15 |
| | Bit 7 | | QuitInputLatch16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0011 | 17 | 1 |
| 0x0012 | 18 | 1 |

7.1. X67DM1321.L12_C1

HwId: 0x1A1D

Functionmodel: 1

Description: 16 In- /Outputs 24 VDC

| Input | | |
|---------------------------|-----------------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DM1321.L12_C1_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X67DM1321.L12_C1_b | Unsigned8 | |
| Bit 0 | DigitalInput09 | |
| Bit 1 | DigitalInput10 | |
| Bit 2 | DigitalInput11 | |
| Bit 3 | DigitalInput12 | |
| Bit 4 | DigitalInput13 | |
| Bit 5 | DigitalInput14 | |
| Bit 6 | DigitalInput15 | |
| Bit 7 | DigitalInput16 | |
| Counter01 | Unsigned16 | |
| Counter02 | Unsigned16 | |
| Inputs_X67DM1321.L12_C1_c | Unsigned8 | |
| Bit 0 | InputLatch01 | |
| Bit 1 | InputLatch02 | |
| Bit 2 | InputLatch03 | |
| Bit 3 | InputLatch04 | |
| Bit 4 | InputLatch05 | |
| Bit 5 | InputLatch06 | |
| Bit 6 | InputLatch07 | |
| Bit 7 | InputLatch08 | |
| Inputs_X67DM1321.L12_C1_d | Unsigned8 | |
| Bit 0 | InputLatch09 | |
| Bit 1 | InputLatch10 | |
| Bit 2 | InputLatch11 | |
| Bit 3 | InputLatch12 | |
| Bit 4 | InputLatch13 | |
| Bit 5 | InputLatch14 | |
| Bit 6 | InputLatch15 | |
| Bit 7 | InputLatch16 | |
| Inputs_X67DM1321.L12_C1_e | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Inputs_X67DM1321.L12_C1_f | Unsigned8 | |

| Input | | | |
|----------------------------|-------|-----------------------|------------------|
| Name | | Input | Output |
| | Bit 0 | StatusDigitalOutput09 | |
| | Bit 1 | StatusDigitalOutput10 | |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| | Bit 4 | StatusDigitalOutput13 | |
| | Bit 5 | StatusDigitalOutput14 | |
| | Bit 6 | StatusDigitalOutput15 | |
| | Bit 7 | StatusDigitalOutput16 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X67DM1321.L12_C1_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X67DM1321.L12_C1_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |
| Outputs_X67DM1321.L12_C1_c | | | Unsigned8 |
| | Bit 5 | | ResetCounter01 |
| Outputs_X67DM1321.L12_C1_d | | | Unsigned8 |
| | Bit 5 | | ResetCounter02 |
| Outputs_X67DM1321.L12_C1_e | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch01 |
| | Bit 1 | | QuitInputLatch02 |
| | Bit 2 | | QuitInputLatch03 |
| | Bit 3 | | QuitInputLatch04 |
| | Bit 4 | | QuitInputLatch05 |
| | Bit 5 | | QuitInputLatch06 |
| | Bit 6 | | QuitInputLatch07 |
| | Bit 7 | | QuitInputLatch08 |
| Outputs_X67DM1321.L12_C1_f | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch09 |
| | Bit 1 | | QuitInputLatch10 |
| | Bit 2 | | QuitInputLatch11 |
| | Bit 3 | | QuitInputLatch12 |
| | Bit 4 | | QuitInputLatch13 |
| | Bit 5 | | QuitInputLatch14 |
| | Bit 6 | | QuitInputLatch15 |
| | Bit 7 | | QuitInputLatch16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0011 | 17 | 1 |
| 0x0012 | 18 | 1 |

7.1. X67DM1321.L12_C2

HwId: 0x1A1D

Functionmodel: 1

Description: 16 In- /Outputs 24 VDC

| Input | | |
|---------------------------|-----------------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DM1321.L12_C2_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X67DM1321.L12_C2_b | Unsigned8 | |
| Bit 0 | DigitalInput09 | |
| Bit 1 | DigitalInput10 | |
| Bit 2 | DigitalInput11 | |
| Bit 3 | DigitalInput12 | |
| Bit 4 | DigitalInput13 | |
| Bit 5 | DigitalInput14 | |
| Bit 6 | DigitalInput15 | |
| Bit 7 | DigitalInput16 | |
| Counter01 | Unsigned16 | |
| Counter02 | Unsigned16 | |
| Inputs_X67DM1321.L12_C2_c | Unsigned8 | |
| Bit 0 | InputLatch01 | |
| Bit 1 | InputLatch02 | |
| Bit 2 | InputLatch03 | |
| Bit 3 | InputLatch04 | |
| Bit 4 | InputLatch05 | |
| Bit 5 | InputLatch06 | |
| Bit 6 | InputLatch07 | |
| Bit 7 | InputLatch08 | |
| Inputs_X67DM1321.L12_C2_d | Unsigned8 | |
| Bit 0 | InputLatch09 | |
| Bit 1 | InputLatch10 | |
| Bit 2 | InputLatch11 | |
| Bit 3 | InputLatch12 | |
| Bit 4 | InputLatch13 | |
| Bit 5 | InputLatch14 | |
| Bit 6 | InputLatch15 | |
| Bit 7 | InputLatch16 | |
| Inputs_X67DM1321.L12_C2_e | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Inputs_X67DM1321.L12_C2_f | Unsigned8 | |

| Input | | | |
|----------------------------|-------|-----------------------|------------------|
| Name | | Input | Output |
| | Bit 0 | StatusDigitalOutput09 | |
| | Bit 1 | StatusDigitalOutput10 | |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| | Bit 4 | StatusDigitalOutput13 | |
| | Bit 5 | StatusDigitalOutput14 | |
| | Bit 6 | StatusDigitalOutput15 | |
| | Bit 7 | StatusDigitalOutput16 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X67DM1321.L12_C2_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X67DM1321.L12_C2_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |
| Outputs_X67DM1321.L12_C2_c | | | Unsigned8 |
| | Bit 5 | | ResetCounter02 |
| Outputs_X67DM1321.L12_C2_d | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch01 |
| | Bit 1 | | QuitInputLatch02 |
| | Bit 2 | | QuitInputLatch03 |
| | Bit 3 | | QuitInputLatch04 |
| | Bit 4 | | QuitInputLatch05 |
| | Bit 5 | | QuitInputLatch06 |
| | Bit 6 | | QuitInputLatch07 |
| | Bit 7 | | QuitInputLatch08 |
| Outputs_X67DM1321.L12_C2_e | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch09 |
| | Bit 1 | | QuitInputLatch10 |
| | Bit 2 | | QuitInputLatch11 |
| | Bit 3 | | QuitInputLatch12 |
| | Bit 4 | | QuitInputLatch13 |
| | Bit 5 | | QuitInputLatch14 |
| | Bit 6 | | QuitInputLatch15 |
| | Bit 7 | | QuitInputLatch16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0011 | 17 | 1 |
| 0x0012 | 18 | 1 |

| | | |
|-----------------------|-----------------------|-----------------------|
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |

7.1. X67DM1321.L12-1

HwId: 0xDAC0

Functionmodel: 2

Description: 16 In- /Outputs 24 VDC P24

| Input | | |
|--------------------------|-----------------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DM1321.L12-1_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X67DM1321.L12-1_b | Unsigned8 | |
| Bit 0 | DigitalInput09 | |
| Bit 1 | DigitalInput10 | |
| Bit 2 | DigitalInput11 | |
| Bit 3 | DigitalInput12 | |
| Bit 4 | DigitalInput13 | |
| Bit 5 | DigitalInput14 | |
| Bit 6 | DigitalInput15 | |
| Bit 7 | DigitalInput16 | |
| Inputs_X67DM1321.L12-1_c | Unsigned8 | |
| Bit 0 | InputLatch01 | |
| Bit 1 | InputLatch02 | |
| Bit 2 | InputLatch03 | |
| Bit 3 | InputLatch04 | |
| Bit 4 | InputLatch05 | |
| Bit 5 | InputLatch06 | |
| Bit 6 | InputLatch07 | |
| Bit 7 | InputLatch08 | |
| Inputs_X67DM1321.L12-1_d | Unsigned8 | |
| Bit 0 | InputLatch09 | |
| Bit 1 | InputLatch10 | |
| Bit 2 | InputLatch11 | |
| Bit 3 | InputLatch12 | |
| Bit 4 | InputLatch13 | |
| Bit 5 | InputLatch14 | |
| Bit 6 | InputLatch15 | |
| Bit 7 | InputLatch16 | |
| Inputs_X67DM1321.L12-1_e | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Inputs_X67DM1321.L12-1_f | Unsigned8 | |
| Bit 0 | StatusDigitalOutput09 | |
| Bit 1 | StatusDigitalOutput10 | |

| Input | | | |
|---------------------------|-------|-----------------------|------------------|
| Name | | Input | Output |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| | Bit 4 | StatusDigitalOutput13 | |
| | Bit 5 | StatusDigitalOutput14 | |
| | Bit 6 | StatusDigitalOutput15 | |
| | Bit 7 | StatusDigitalOutput16 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X67DM1321.L12-1_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X67DM1321.L12-1_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |
| Outputs_X67DM1321.L12-1_c | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch01 |
| | Bit 1 | | QuitInputLatch02 |
| | Bit 2 | | QuitInputLatch03 |
| | Bit 3 | | QuitInputLatch04 |
| | Bit 4 | | QuitInputLatch05 |
| | Bit 5 | | QuitInputLatch06 |
| | Bit 6 | | QuitInputLatch07 |
| | Bit 7 | | QuitInputLatch08 |
| Outputs_X67DM1321.L12-1_d | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch09 |
| | Bit 1 | | QuitInputLatch10 |
| | Bit 2 | | QuitInputLatch11 |
| | Bit 3 | | QuitInputLatch12 |
| | Bit 4 | | QuitInputLatch13 |
| | Bit 5 | | QuitInputLatch14 |
| | Bit 6 | | QuitInputLatch15 |
| | Bit 7 | | QuitInputLatch16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0011 | 17 | 1 |
| 0x0012 | 18 | 1 |

7.1. X67DM1321.L12-1_C1

HwId: 0xDAC0

Functionmodel: 1

Description: 16 In- /Outputs 24 VDC P24

| Input | | |
|-----------------------------|-----------------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DM1321.L12-1_C1_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X67DM1321.L12-1_C1_b | Unsigned8 | |
| Bit 0 | DigitalInput09 | |
| Bit 1 | DigitalInput10 | |
| Bit 2 | DigitalInput11 | |
| Bit 3 | DigitalInput12 | |
| Bit 4 | DigitalInput13 | |
| Bit 5 | DigitalInput14 | |
| Bit 6 | DigitalInput15 | |
| Bit 7 | DigitalInput16 | |
| Counter01 | Unsigned16 | |
| Counter02 | Unsigned16 | |
| Inputs_X67DM1321.L12-1_C1_c | Unsigned8 | |
| Bit 0 | InputLatch01 | |
| Bit 1 | InputLatch02 | |
| Bit 2 | InputLatch03 | |
| Bit 3 | InputLatch04 | |
| Bit 4 | InputLatch05 | |
| Bit 5 | InputLatch06 | |
| Bit 6 | InputLatch07 | |
| Bit 7 | InputLatch08 | |
| Inputs_X67DM1321.L12-1_C1_d | Unsigned8 | |
| Bit 0 | InputLatch09 | |
| Bit 1 | InputLatch10 | |
| Bit 2 | InputLatch11 | |
| Bit 3 | InputLatch12 | |
| Bit 4 | InputLatch13 | |
| Bit 5 | InputLatch14 | |
| Bit 6 | InputLatch15 | |
| Bit 7 | InputLatch16 | |
| Inputs_X67DM1321.L12-1_C1_e | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Inputs_X67DM1321.L12-1_C1_f | Unsigned8 | |

| Input | | | |
|------------------------------|-------|-----------------------|------------------|
| Name | | Input | Output |
| | Bit 0 | StatusDigitalOutput09 | |
| | Bit 1 | StatusDigitalOutput10 | |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| | Bit 4 | StatusDigitalOutput13 | |
| | Bit 5 | StatusDigitalOutput14 | |
| | Bit 6 | StatusDigitalOutput15 | |
| | Bit 7 | StatusDigitalOutput16 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X67DM1321.L12-1_C1_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X67DM1321.L12-1_C1_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |
| Outputs_X67DM1321.L12-1_C1_c | | | Unsigned8 |
| | Bit 5 | | ResetCounter01 |
| Outputs_X67DM1321.L12-1_C1_d | | | Unsigned8 |
| | Bit 5 | | ResetCounter02 |
| Outputs_X67DM1321.L12-1_C1_e | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch01 |
| | Bit 1 | | QuitInputLatch02 |
| | Bit 2 | | QuitInputLatch03 |
| | Bit 3 | | QuitInputLatch04 |
| | Bit 4 | | QuitInputLatch05 |
| | Bit 5 | | QuitInputLatch06 |
| | Bit 6 | | QuitInputLatch07 |
| | Bit 7 | | QuitInputLatch08 |
| Outputs_X67DM1321.L12-1_C1_f | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch09 |
| | Bit 1 | | QuitInputLatch10 |
| | Bit 2 | | QuitInputLatch11 |
| | Bit 3 | | QuitInputLatch12 |
| | Bit 4 | | QuitInputLatch13 |
| | Bit 5 | | QuitInputLatch14 |
| | Bit 6 | | QuitInputLatch15 |
| | Bit 7 | | QuitInputLatch16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0011 | 17 | 1 |
| 0x0012 | 18 | 1 |

7.1. X67DM1321.L12-1_C2

HwId: 0xDAC0

Functionmodel: 1

Description: 16 In- /Outputs 24 VDC P24

| Input | | |
|-----------------------------|-----------------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DM1321.L12-1_C2_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X67DM1321.L12-1_C2_b | Unsigned8 | |
| Bit 0 | DigitalInput09 | |
| Bit 1 | DigitalInput10 | |
| Bit 2 | DigitalInput11 | |
| Bit 3 | DigitalInput12 | |
| Bit 4 | DigitalInput13 | |
| Bit 5 | DigitalInput14 | |
| Bit 6 | DigitalInput15 | |
| Bit 7 | DigitalInput16 | |
| Counter01 | Unsigned16 | |
| Counter02 | Unsigned16 | |
| Inputs_X67DM1321.L12-1_C2_c | Unsigned8 | |
| Bit 0 | InputLatch01 | |
| Bit 1 | InputLatch02 | |
| Bit 2 | InputLatch03 | |
| Bit 3 | InputLatch04 | |
| Bit 4 | InputLatch05 | |
| Bit 5 | InputLatch06 | |
| Bit 6 | InputLatch07 | |
| Bit 7 | InputLatch08 | |
| Inputs_X67DM1321.L12-1_C2_d | Unsigned8 | |
| Bit 0 | InputLatch09 | |
| Bit 1 | InputLatch10 | |
| Bit 2 | InputLatch11 | |
| Bit 3 | InputLatch12 | |
| Bit 4 | InputLatch13 | |
| Bit 5 | InputLatch14 | |
| Bit 6 | InputLatch15 | |
| Bit 7 | InputLatch16 | |
| Inputs_X67DM1321.L12-1_C2_e | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Inputs_X67DM1321.L12-1_C2_f | Unsigned8 | |

| Input | | | |
|------------------------------|-------|-----------------------|------------------|
| Name | | Input | Output |
| | Bit 0 | StatusDigitalOutput09 | |
| | Bit 1 | StatusDigitalOutput10 | |
| | Bit 2 | StatusDigitalOutput11 | |
| | Bit 3 | StatusDigitalOutput12 | |
| | Bit 4 | StatusDigitalOutput13 | |
| | Bit 5 | StatusDigitalOutput14 | |
| | Bit 6 | StatusDigitalOutput15 | |
| | Bit 7 | StatusDigitalOutput16 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_X67DM1321.L12-1_C2_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_X67DM1321.L12-1_C2_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |
| Outputs_X67DM1321.L12-1_C2_c | | | Unsigned8 |
| | Bit 5 | | ResetCounter02 |
| Outputs_X67DM1321.L12-1_C2_d | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch01 |
| | Bit 1 | | QuitInputLatch02 |
| | Bit 2 | | QuitInputLatch03 |
| | Bit 3 | | QuitInputLatch04 |
| | Bit 4 | | QuitInputLatch05 |
| | Bit 5 | | QuitInputLatch06 |
| | Bit 6 | | QuitInputLatch07 |
| | Bit 7 | | QuitInputLatch08 |
| Outputs_X67DM1321.L12-1_C2_e | | | Unsigned8 |
| | Bit 0 | | QuitInputLatch09 |
| | Bit 1 | | QuitInputLatch10 |
| | Bit 2 | | QuitInputLatch11 |
| | Bit 3 | | QuitInputLatch12 |
| | Bit 4 | | QuitInputLatch13 |
| | Bit 5 | | QuitInputLatch14 |
| | Bit 6 | | QuitInputLatch15 |
| | Bit 7 | | QuitInputLatch16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0011 | 17 | 1 |
| 0x0012 | 18 | 1 |

| | | |
|-----------------------|-----------------------|-----------------------|
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0014 | 20 | 1 |

7.1. X67DM1321_C1

HwId: 0x1311

Functionmodel: 1

Description: 8 In- /Outputs 24 VDC

| Input | | |
|------------------------|-----------------------|------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DM1321_C1_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Counter01 | Unsigned16 | |
| Counter02 | Unsigned16 | |
| Inputs_X67DM1321_C1_b | Unsigned8 | |
| Bit 0 | InputLatch01 | |
| Bit 1 | InputLatch02 | |
| Bit 2 | InputLatch03 | |
| Bit 3 | InputLatch04 | |
| Bit 4 | InputLatch05 | |
| Bit 5 | InputLatch06 | |
| Bit 6 | InputLatch07 | |
| Bit 7 | InputLatch08 | |
| Inputs_X67DM1321_C1_c | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Output | | |
| Name | Input | Output |
| Outputs_X67DM1321_C1_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |
| Outputs_X67DM1321_C1_b | | Unsigned8 |
| Bit 5 | | ResetCounter01 |
| Outputs_X67DM1321_C1_c | | Unsigned8 |
| Bit 5 | | ResetCounter02 |
| Outputs_X67DM1321_C1_d | | Unsigned8 |
| Bit 0 | | QuitInputLatch01 |
| Bit 1 | | QuitInputLatch02 |

| Output | | | |
|--------|-------|-------|------------------|
| Name | | Input | Output |
| | Bit 2 | | QuitInputLatch03 |
| | Bit 3 | | QuitInputLatch04 |
| | Bit 4 | | QuitInputLatch05 |
| | Bit 5 | | QuitInputLatch06 |
| | Bit 6 | | QuitInputLatch07 |
| | Bit 7 | | QuitInputLatch08 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |

7.1. X67DM1321_C2

HwId: 0x1311

Functionmodel: 1

Description: 8 In- /Outputs 24 VDC

| Input | | |
|------------------------|-----------------------|------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DM1321_C2_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Counter01 | Unsigned16 | |
| Counter02 | Unsigned16 | |
| Inputs_X67DM1321_C2_b | Unsigned8 | |
| Bit 0 | InputLatch01 | |
| Bit 1 | InputLatch02 | |
| Bit 2 | InputLatch03 | |
| Bit 3 | InputLatch04 | |
| Bit 4 | InputLatch05 | |
| Bit 5 | InputLatch06 | |
| Bit 6 | InputLatch07 | |
| Bit 7 | InputLatch08 | |
| Inputs_X67DM1321_C2_c | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Output | | |
| Name | Input | Output |
| Outputs_X67DM1321_C2_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |
| Outputs_X67DM1321_C2_b | | Unsigned8 |
| Bit 5 | | ResetCounter02 |
| Outputs_X67DM1321_C2_c | | Unsigned8 |
| Bit 0 | | QuitInputLatch01 |
| Bit 1 | | QuitInputLatch02 |
| Bit 2 | | QuitInputLatch03 |
| Bit 3 | | QuitInputLatch04 |

| Output | | | |
|--------|-------|-------|------------------|
| Name | | Input | Output |
| | Bit 4 | | QuitInputLatch05 |
| | Bit 5 | | QuitInputLatch06 |
| | Bit 6 | | QuitInputLatch07 |
| | Bit 7 | | QuitInputLatch08 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |
| 0x0014 | 20 | 1 |

7.1. X67DM9331.L12

HwId: 0x1B15

Functionmodel: 0

Description: 8 In- /Outputs 24 VDC

| Input | | |
|-------------------------|-----------------------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DM9331.L12_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X67DM9331.L12_b | Unsigned8 | |
| Bit 0 | StatusSensor01 | |
| Bit 1 | StatusSensor02 | |
| Bit 2 | StatusSensor03 | |
| Bit 3 | StatusSensor04 | |
| Bit 4 | StatusSensor05 | |
| Bit 5 | StatusSensor06 | |
| Bit 6 | StatusSensor07 | |
| Bit 7 | StatusSensor08 | |
| Inputs_X67DM9331.L12_c | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Output | | |
| Name | Input | Output |
| Outputs_X67DM9331.L12_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |

7.1. X67DV1311.L08

HwId: 0x1AED

Functionmodel: 0

Description: 16 In- /Outputs 24 VDC

| Input | | |
|-------------------------|-----------------------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DV1311.L08_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X67DV1311.L08_b | Unsigned8 | |
| Bit 0 | DigitalInput09 | |
| Bit 1 | DigitalInput10 | |
| Bit 2 | DigitalInput11 | |
| Bit 3 | DigitalInput12 | |
| Bit 4 | DigitalInput13 | |
| Bit 5 | DigitalInput14 | |
| Bit 6 | DigitalInput15 | |
| Bit 7 | DigitalInput16 | |
| Inputs_X67DV1311.L08_c | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Inputs_X67DV1311.L08_d | Unsigned8 | |
| Bit 0 | StatusDigitalOutput09 | |
| Bit 1 | StatusDigitalOutput10 | |
| Bit 2 | StatusDigitalOutput11 | |
| Bit 3 | StatusDigitalOutput12 | |
| Bit 4 | StatusDigitalOutput13 | |
| Bit 5 | StatusDigitalOutput14 | |
| Bit 6 | StatusDigitalOutput15 | |
| Bit 7 | StatusDigitalOutput16 | |
| Output | | |
| Name | Input | Output |
| Outputs_X67DV1311.L08_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |

| Output | | | |
|-------------------------|-------|-------|-----------------|
| Name | | Input | Output |
| Outputs_X67DV1311.L08_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

7.1. X67DV1311.L12

HwId: 0x1AEE

Functionmodel: 0

Description: 16 In- /Outputs 24 VDC

| Input | | |
|-------------------------|-----------------------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67DV1311.L12_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Inputs_X67DV1311.L12_b | Unsigned8 | |
| Bit 0 | DigitalInput09 | |
| Bit 1 | DigitalInput10 | |
| Bit 2 | DigitalInput11 | |
| Bit 3 | DigitalInput12 | |
| Bit 4 | DigitalInput13 | |
| Bit 5 | DigitalInput14 | |
| Bit 6 | DigitalInput15 | |
| Bit 7 | DigitalInput16 | |
| Inputs_X67DV1311.L12_c | Unsigned8 | |
| Bit 0 | StatusDigitalOutput01 | |
| Bit 1 | StatusDigitalOutput02 | |
| Bit 2 | StatusDigitalOutput03 | |
| Bit 3 | StatusDigitalOutput04 | |
| Bit 4 | StatusDigitalOutput05 | |
| Bit 5 | StatusDigitalOutput06 | |
| Bit 6 | StatusDigitalOutput07 | |
| Bit 7 | StatusDigitalOutput08 | |
| Inputs_X67DV1311.L12_d | Unsigned8 | |
| Bit 0 | StatusDigitalOutput09 | |
| Bit 1 | StatusDigitalOutput10 | |
| Bit 2 | StatusDigitalOutput11 | |
| Bit 3 | StatusDigitalOutput12 | |
| Bit 4 | StatusDigitalOutput13 | |
| Bit 5 | StatusDigitalOutput14 | |
| Bit 6 | StatusDigitalOutput15 | |
| Bit 7 | StatusDigitalOutput16 | |
| Output | | |
| Name | Input | Output |
| Outputs_X67DV1311.L12_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |

| Output | | | |
|-------------------------|-------|-------|-----------------|
| Name | | Input | Output |
| Outputs_X67DV1311.L12_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

7.1. X67IF1121-1

HwId: 0xA90F

Functionmodel: 254

Description: Interface Modul RS232, RS422/485; 2 In- /Outputs 24 VDC

| Input | | |
|-----------------------|-----------|-----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67IF1121-1_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| Inputs_X67IF1121-1_b | Unsigned8 | |
| | Bit 2 | StatusDigitalOutput03 |
| | Bit 3 | StatusDigitalOutput04 |
| IF1_InputSequence | Unsigned8 | |
| IF1_RxByte01 | Unsigned8 | |
| IF1_RxByte02 | Unsigned8 | |
| IF1_RxByte03 | Unsigned8 | |
| IF1_RxByte04 | Unsigned8 | |
| IF1_RxByte05 | Unsigned8 | |
| IF1_RxByte06 | Unsigned8 | |
| IF1_RxByte07 | Unsigned8 | |
| IF1_RxByte08 | Unsigned8 | |
| IF1_RxByte09 | Unsigned8 | |
| IF1_RxByte10 | Unsigned8 | |
| IF1_RxByte11 | Unsigned8 | |
| IF1_RxByte12 | Unsigned8 | |
| IF2_InputSequence | Unsigned8 | |
| IF2_RxByte01 | Unsigned8 | |
| IF2_RxByte02 | Unsigned8 | |
| IF2_RxByte03 | Unsigned8 | |
| IF2_RxByte04 | Unsigned8 | |
| IF2_RxByte05 | Unsigned8 | |
| IF2_RxByte06 | Unsigned8 | |
| IF2_RxByte07 | Unsigned8 | |
| IF2_RxByte08 | Unsigned8 | |
| IF2_RxByte09 | Unsigned8 | |
| IF2_RxByte10 | Unsigned8 | |
| IF2_RxByte11 | Unsigned8 | |
| IF2_RxByte12 | Unsigned8 | |
| Inputs_X67IF1121-1_c | Unsigned8 | |
| | Bit 0 | IF1StartBitError |
| | Bit 1 | IF1StopBitError |
| | Bit 2 | IF1ParityError |
| | Bit 3 | IF1RXoverrun |
| | Bit 4 | IF2StartBitError |
| | Bit 5 | IF2StopBitError |
| | Bit 6 | IF2ParityError |
| | Bit 7 | IF2RXoverrun |
| Output | | |
| Name | Input | Output |
| Outputs_X67IF1121-1_a | | Unsigned8 |
| | Bit 2 | DigitalOutput03 |

| Output | | | |
|-----------------------|-------|-------|----------------------|
| Name | | Input | Output |
| | Bit 3 | | DigitalOutput04 |
| IF1_OutputSequence | | | Unsigned8 |
| IF1_TxByte01 | | | Unsigned8 |
| IF1_TxByte02 | | | Unsigned8 |
| IF1_TxByte03 | | | Unsigned8 |
| IF1_TxByte04 | | | Unsigned8 |
| IF1_TxByte05 | | | Unsigned8 |
| IF1_TxByte06 | | | Unsigned8 |
| IF1_TxByte07 | | | Unsigned8 |
| IF1_TxByte08 | | | Unsigned8 |
| IF1_TxByte09 | | | Unsigned8 |
| IF1_TxByte10 | | | Unsigned8 |
| IF1_TxByte11 | | | Unsigned8 |
| IF1_TxByte12 | | | Unsigned8 |
| IF2_OutputSequence | | | Unsigned8 |
| IF2_TxByte01 | | | Unsigned8 |
| IF2_TxByte02 | | | Unsigned8 |
| IF2_TxByte03 | | | Unsigned8 |
| IF2_TxByte04 | | | Unsigned8 |
| IF2_TxByte05 | | | Unsigned8 |
| IF2_TxByte06 | | | Unsigned8 |
| IF2_TxByte07 | | | Unsigned8 |
| IF2_TxByte08 | | | Unsigned8 |
| IF2_TxByte09 | | | Unsigned8 |
| IF2_TxByte10 | | | Unsigned8 |
| IF2_TxByte11 | | | Unsigned8 |
| IF2_TxByte12 | | | Unsigned8 |
| Outputs_X67IF1121-1_b | | | Unsigned8 |
| | Bit 0 | | IF1QuitStartBitError |
| | Bit 1 | | IF1QuitStopBitError |
| | Bit 2 | | IF1QuitParityError |
| | Bit 3 | | IF1QuitRXoverrun |
| | Bit 4 | | IF2QuitStartBitError |
| | Bit 5 | | IF2QuitStopBitError |
| | Bit 6 | | IF2QuitParityError |
| | Bit 7 | | IF2QuitRXoverrun |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0501 | 1281 | 1 |
| 0x0104 | 260 | 4 |
| 0x0304 | 772 | 4 |
| 0x014C | 332 | 4 |
| 0x034C | 844 | 4 |
| 0x010C | 268 | 4 |
| 0x030C | 780 | 4 |
| 0x00C4 | 196 | 4 |
| 0x00D4 | 212 | 4 |

7.1. X67UM1352

HwId: 0x1CDF

Functionmodel: 0

Description: 4 dig. Inputs, 2 dig. Outputs, 1 analog Input

| Input | | |
|---------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67UM1352_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| AnalogInput01 | Integer32 | |
| Inputs_X67UM1352_b | Unsigned8 | |
| | Bit 0 | OpenLine01 |
| Inputs_X67UM1352_c | Unsigned8 | |
| | Bit 0 | OutputError01 |
| | Bit 1 | OutputError02 |
| Output | | |
| Name | Input | Output |
| Outputs_X67UM1352_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x001A | 26 | 1 |

7.1. X67UM4389

HwId: 0xA174

Functionmodel: 0

Description: 12 dig. Inputs, 4 dig. Outputs, 4 analog Outputs

| Input | | |
|------------------------|-----------------------|-----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X67UM4389_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Inputs_X67UM4389_b | Unsigned8 | |
| | Bit 0 | DigitalInput09 |
| | Bit 1 | DigitalInput10 |
| | Bit 2 | DigitalInput11 |
| | Bit 3 | DigitalInput12 |
| Output | | |
| Name | Input | Output |
| Outputs_X67UM4389_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| AnalogOutput01 | | Integer16 |
| AnalogOutput02 | | Integer16 |
| AnalogOutput03 | | Integer16 |
| AnalogOutput04 | | Integer16 |
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8. Other Module

8.1. 0AC190.1-NOR

HwId: 0xA0E9

Functionmodel: 0

Description: 5xDO, 5xDI; X2X Link Controller for VS18/26

| Input | | |
|------------------------|-----------------------|-----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| DigitalStatus12 | Unsigned8 | |
| DigitalStatus34 | Unsigned8 | |
| DigitalStatus56 | Unsigned8 | |
| DigitalStatus78 | Unsigned8 | |
| DigitalStatus910 | Unsigned8 | |
| DigitalError12 | Unsigned8 | |
| DigitalError34 | Unsigned8 | |
| DigitalError56 | Unsigned8 | |
| DigitalError78 | Unsigned8 | |
| DigitalError910 | Unsigned8 | |
| Inputs_0AC190.1-NOR_a | Unsigned8 | |
| | Bit 0 | VccVentOVL |
| | Bit 1 | VccVentUVL |
| | Bit 2 | VccIoUVL |
| | Bit 3 | HwFaultDetect |
| | Bit 4 | SwOvlDetect |
| | Bit 5 | SwAbsValveDetect |
| | Bit 7 | IoBusError |
| Output | | |
| Name | Input | Output |
| DigitalOutput12 | | Unsigned8 |
| DigitalOutput34 | | Unsigned8 |
| DigitalOutput56 | | Unsigned8 |
| DigitalOutput78 | | Unsigned8 |
| DigitalOutput910 | | Unsigned8 |
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. 4XP0000.00-K20

HwId: 0x2703

Functionmodel: 0

Description: Distributed I/O

| Input | | | |
|--------------------------|--------|----------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_4XP0000.00-K20_a | | Unsigned8 | |
| | Bit 2 | DigitalInput01 | |
| | Bit 0 | DigitalInput02 | |
| | Bit 3 | DigitalInput03 | |
| | Bit 1 | DigitalInput04 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_4XP0000.00-K20_a | | | Unsigned16 |
| | Bit 14 | | DigitalOutput01 |
| | Bit 15 | | DigitalOutput02 |
| | Bit 0 | | DigitalOutput03 |
| | Bit 8 | | DigitalOutput04 |
| | Bit 9 | | DigitalOutput05 |
| | Bit 10 | | DigitalOutput06 |
| | Bit 1 | | DigitalOutput07 |
| | Bit 2 | | DigitalOutput08 |
| | Bit 3 | | DigitalOutput09 |
| | Bit 11 | | DigitalOutput10 |
| | Bit 12 | | DigitalOutput11 |
| | Bit 13 | | DigitalOutput12 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. 4XP0000.00-K21

HwId: 0x2704

Functionmodel: 0

Description: Distributed I/O

| Input | | |
|--------------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_4XP0000.00-K21_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| Output | | |
| Name | Input | Output |
| Outputs_4XP0000.00-K21_a | | Unsigned16 |
| | Bit 8 | DigitalOutput01 |
| | Bit 9 | DigitalOutput02 |
| | Bit 10 | DigitalOutput03 |
| | Bit 11 | DigitalOutput04 |
| | Bit 12 | DigitalOutput05 |
| | Bit 13 | DigitalOutput06 |
| | Bit 14 | DigitalOutput07 |
| | Bit 15 | DigitalOutput08 |
| | Bit 0 | DigitalOutput09 |
| | Bit 1 | DigitalOutput10 |
| | Bit 2 | DigitalOutput11 |
| | Bit 3 | DigitalOutput12 |
| Outputs_4XP0000.00-K21_b | | Unsigned8 |
| | Bit 0 | DigitalOutput13 |
| | Bit 1 | DigitalOutput14 |
| | Bit 2 | DigitalOutput15 |
| | Bit 3 | DigitalOutput16 |
| | Bit 4 | DigitalOutput17 |
| | Bit 5 | DigitalOutput18 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

8.1. 4XP0000.00-K46

HwId: 0xB4F8

Functionmodel: 0

Description: Distributed I/O

| Input | | |
|--------------------------|----------------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_4XP0000.00-K46_a | Unsigned16 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Bit 8 | DigitalInput09 | |
| Bit 9 | DigitalInput10 | |
| Bit 10 | DigitalInput11 | |
| Bit 11 | DigitalInput12 | |
| Bit 12 | DigitalInput13 | |
| Bit 13 | DigitalInput14 | |
| Bit 14 | DigitalInput15 | |
| Bit 15 | DigitalInput16 | |
| Inputs_4XP0000.00-K46_b | Unsigned16 | |
| Bit 0 | DigitalInput17 | |
| Bit 1 | DigitalInput18 | |
| Bit 2 | DigitalInput19 | |
| Bit 3 | DigitalInput20 | |
| Bit 4 | DigitalInput21 | |
| Bit 5 | DigitalInput22 | |
| Bit 6 | DigitalInput23 | |
| Bit 7 | DigitalInput24 | |
| Bit 8 | DigitalInput25 | |
| Bit 9 | DigitalInput26 | |
| Bit 10 | DigitalInput27 | |
| Output | | |
| Name | Input | Output |
| Outputs_4XP0000.00-K46_a | | Unsigned16 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |
| Bit 8 | | DigitalOutput09 |
| Bit 9 | | DigitalOutput10 |
| Bit 10 | | DigitalOutput11 |
| Bit 11 | | DigitalOutput12 |
| Bit 12 | | DigitalOutput13 |
| Bit 13 | | DigitalOutput14 |
| Bit 14 | | DigitalOutput15 |

| Output | | | |
|--------------------------|--------|-------|-----------------|
| Name | | Input | Output |
| Outputs_4XP0000.00-K46_b | | | Unsigned16 |
| | Bit 0 | | DigitalOutput16 |
| | Bit 1 | | DigitalOutput17 |
| | Bit 2 | | DigitalOutput18 |
| | Bit 3 | | DigitalOutput19 |
| | Bit 4 | | DigitalOutput20 |
| | Bit 5 | | DigitalOutput21 |
| | Bit 6 | | DigitalOutput22 |
| | Bit 7 | | DigitalOutput23 |
| | Bit 8 | | DigitalOutput24 |
| | Bit 9 | | DigitalOutput25 |
| | Bit 10 | | DigitalOutput26 |
| | Bit 11 | | DigitalOutput27 |
| | Bit 12 | | DigitalOutput28 |
| | Bit 13 | | DigitalOutput29 |
| | Bit 14 | | DigitalOutput30 |
| Outputs_4XP0000.00-K46_c | | | Unsigned16 |
| | Bit 0 | | DigitalOutput31 |
| | Bit 1 | | DigitalOutput32 |
| | Bit 2 | | DigitalOutput33 |
| | Bit 3 | | DigitalOutput34 |
| | Bit 4 | | DigitalOutput35 |
| | Bit 5 | | DigitalOutput36 |
| | Bit 6 | | DigitalOutput37 |
| | Bit 7 | | DigitalOutput38 |
| | Bit 8 | | DigitalOutput39 |
| | Bit 9 | | DigitalOutput40 |
| | Bit 10 | | DigitalOutput41 |
| | Bit 11 | | DigitalOutput42 |
| | Bit 12 | | DigitalOutput43 |
| | Bit 13 | | DigitalOutput44 |
| | Bit 14 | | DigitalOutput45 |
| Outputs_4XP0000.00-K46_d | | | Unsigned16 |
| | Bit 0 | | DigitalOutput46 |
| | Bit 1 | | DigitalOutput47 |
| | Bit 2 | | DigitalOutput48 |
| | Bit 3 | | DigitalOutput49 |
| | Bit 4 | | DigitalOutput50 |
| | Bit 5 | | DigitalOutput51 |
| | Bit 6 | | DigitalOutput52 |
| | Bit 7 | | DigitalOutput53 |
| | Bit 8 | | DigitalOutput54 |
| | Bit 9 | | DigitalOutput55 |
| | Bit 10 | | DigitalOutput56 |
| | Bit 11 | | DigitalOutput57 |
| | Bit 12 | | DigitalOutput58 |
| | Bit 13 | | DigitalOutput59 |
| | Bit 14 | | DigitalOutput60 |
| Outputs_4XP0000.00-K46_e | | | Unsigned16 |
| | Bit 0 | | DigitalOutput61 |
| | Bit 1 | | DigitalOutput62 |
| | Bit 2 | | DigitalOutput63 |
| | Bit 3 | | DigitalOutput64 |
| | Bit 4 | | DigitalOutput65 |

| | | | |
|--------|-------|-------|-----------------|
| Output | | | |
| Name | | Input | Output |
| | Bit 5 | | DigitalOutput66 |
| | Bit 6 | | DigitalOutput67 |

| | | |
|-----------------------|-----------------------|-----------------------|
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. 4XP0000.00-K64

HwId: 0xC63E

Functionmodel: 0

Description: Distributed I/O

| Input | | |
|--------------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_4XP0000.00-K64_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| Output | | |
| Name | Input | Output |
| Outputs_4XP0000.00-K64_a | | Unsigned16 |
| | Bit 8 | DigitalOutput01 |
| | Bit 9 | DigitalOutput02 |
| | Bit 10 | DigitalOutput03 |
| | Bit 11 | DigitalOutput04 |
| | Bit 12 | DigitalOutput05 |
| | Bit 13 | DigitalOutput06 |
| | Bit 14 | DigitalOutput07 |
| | Bit 15 | DigitalOutput08 |
| | Bit 0 | DigitalOutput09 |
| | Bit 1 | DigitalOutput10 |
| | Bit 2 | DigitalOutput11 |
| | Bit 3 | DigitalOutput12 |
| | Bit 4 | DigitalOutput13 |
| | Bit 5 | DigitalOutput14 |
| | Bit 6 | DigitalOutput15 |
| | Bit 7 | DigitalOutput16 |
| Outputs_4XP0000.00-K64_b | | Unsigned8 |
| | Bit 0 | DigitalOutput17 |
| | Bit 1 | DigitalOutput18 |
| | Bit 2 | DigitalOutput19 |
| | Bit 3 | DigitalOutput20 |
| | Bit 4 | DigitalOutput21 |
| | Bit 5 | DigitalOutput22 |
| | Bit 6 | DigitalOutput23 |
| | Bit 7 | DigitalOutput24 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

8.1. 4XP0000.00-K94

HwId: 0xDE52

Functionmodel: 0

Description: Distributed I/O

| Input | | |
|--------------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_4XP0000.00-K94_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| Output | | |
| Name | Input | Output |
| Outputs_4XP0000.00-K94_a | | Unsigned16 |
| | Bit 8 | DigitalOutput01 |
| | Bit 9 | DigitalOutput02 |
| | Bit 10 | DigitalOutput03 |
| | Bit 11 | DigitalOutput04 |
| | Bit 12 | DigitalOutput05 |
| | Bit 13 | DigitalOutput06 |
| | Bit 14 | DigitalOutput07 |
| | Bit 15 | DigitalOutput08 |
| | Bit 0 | DigitalOutput09 |
| | Bit 1 | DigitalOutput10 |
| | Bit 2 | DigitalOutput11 |
| | Bit 3 | DigitalOutput12 |
| | Bit 4 | DigitalOutput13 |
| | Bit 5 | DigitalOutput14 |
| | Bit 6 | DigitalOutput15 |
| | Bit 7 | DigitalOutput16 |
| Outputs_4XP0000.00-K94_b | | Unsigned8 |
| | Bit 0 | DigitalOutput17 |
| | Bit 1 | DigitalOutput18 |
| | Bit 2 | DigitalOutput19 |
| | Bit 3 | DigitalOutput20 |
| | Bit 4 | DigitalOutput21 |
| | Bit 5 | DigitalOutput22 |
| | Bit 6 | DigitalOutput23 |
| | Bit 7 | DigitalOutput24 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

8.1. 4XP0000.00-KA4

HwId: 0xDE53

Functionmodel: 0

Description: Distributed I/O

| Input | | |
|--------------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_4XP0000.00-KA4_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| Output | | |
| Name | Input | Output |
| Outputs_4XP0000.00-KA4_a | | Unsigned16 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |
| | Bit 8 | DigitalOutput09 |
| | Bit 9 | DigitalOutput10 |
| | Bit 10 | DigitalOutput11 |
| | Bit 11 | DigitalOutput12 |
| | Bit 12 | DigitalOutput13 |
| | Bit 13 | DigitalOutput14 |
| | Bit 14 | DigitalOutput15 |
| | Bit 15 | DigitalOutput16 |
| Outputs_4XP0000.00-KA4_b | | Unsigned8 |
| | Bit 0 | DigitalOutput17 |
| | Bit 1 | DigitalOutput18 |
| | Bit 2 | DigitalOutput19 |
| | Bit 3 | DigitalOutput20 |
| | Bit 4 | DigitalOutput21 |
| | Bit 5 | DigitalOutput22 |
| | Bit 6 | DigitalOutput23 |
| | Bit 7 | DigitalOutput24 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

| |
|------------------------|
| No acyclic Data |
|------------------------|

8.1. 4XP0070.00-00B

HwId: 0xE61E

Functionmodel: 0

Description: Distributed I/O

| Input | | | |
|--------------------------|--------|----------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_4XP0070.00-00B_a | | Unsigned8 | |
| | Bit 0 | DigitalInput01 | |
| | Bit 1 | DigitalInput02 | |
| | Bit 2 | DigitalInput03 | |
| | Bit 3 | DigitalInput04 | |
| | Bit 4 | DigitalInput05 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_4XP0070.00-00B_a | | | Unsigned16 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| | Bit 8 | | DigitalOutput09 |
| | Bit 9 | | DigitalOutput10 |
| | Bit 10 | | DigitalOutput11 |
| | Bit 11 | | DigitalOutput12 |
| | Bit 12 | | DigitalOutput13 |
| | Bit 13 | | DigitalOutput14 |
| | Bit 14 | | DigitalOutput15 |
| | Bit 15 | | DigitalOutput16 |
| Outputs_4XP0070.00-00B_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput17 |
| | Bit 1 | | DigitalOutput18 |
| | Bit 2 | | DigitalOutput19 |
| | Bit 3 | | DigitalOutput20 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

8.1. 4XP0070.00-00W

HwId: 0xE61F

Functionmodel: 0

Description: Distributed I/O

| Input | | | |
|--------------------------|--------|----------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_4XP0070.00-00W_a | | Unsigned8 | |
| | Bit 0 | DigitalInput01 | |
| | Bit 1 | DigitalInput02 | |
| | Bit 2 | DigitalInput03 | |
| | Bit 3 | DigitalInput04 | |
| | Bit 4 | DigitalInput05 | |
| Output | | | |
| Name | | Input | Output |
| Outputs_4XP0070.00-00W_a | | | Unsigned16 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| | Bit 8 | | DigitalOutput09 |
| | Bit 9 | | DigitalOutput10 |
| | Bit 10 | | DigitalOutput11 |
| | Bit 11 | | DigitalOutput12 |
| | Bit 12 | | DigitalOutput13 |
| | Bit 13 | | DigitalOutput14 |
| | Bit 14 | | DigitalOutput15 |
| | Bit 15 | | DigitalOutput16 |
| Outputs_4XP0070.00-00W_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput17 |
| | Bit 1 | | DigitalOutput18 |
| | Bit 2 | | DigitalOutput19 |
| | Bit 3 | | DigitalOutput20 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

8.1. 4XP0101.00-00W

HwId: 0xE635

Functionmodel: 0

Description: Distributed I/O

| Input | | |
|--------------------------|----------------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_4XP0101.00-00W_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Output | | |
| Name | Input | Output |
| Outputs_4XP0101.00-00W_a | | Unsigned16 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |
| Bit 8 | | DigitalOutput09 |
| Bit 9 | | DigitalOutput10 |
| Bit 10 | | DigitalOutput11 |
| Bit 11 | | DigitalOutput12 |
| Bit 12 | | DigitalOutput13 |
| Bit 13 | | DigitalOutput14 |
| Bit 14 | | DigitalOutput15 |
| Bit 15 | | DigitalOutput16 |
| Outputs_4XP0101.00-00W_b | | Unsigned16 |
| Bit 0 | | DigitalOutput17 |
| Bit 1 | | DigitalOutput18 |
| Bit 2 | | DigitalOutput19 |
| Bit 3 | | DigitalOutput20 |
| Bit 4 | | DigitalOutput21 |
| Bit 5 | | DigitalOutput22 |
| Bit 6 | | DigitalOutput23 |
| Bit 7 | | DigitalOutput24 |
| Bit 8 | | DigitalOutput25 |
| Bit 9 | | DigitalOutput26 |
| Bit 10 | | DigitalOutput27 |
| Bit 11 | | DigitalOutput28 |
| Bit 12 | | DigitalOutput29 |
| Bit 13 | | DigitalOutput30 |
| Bit 14 | | DigitalOutput31 |
| Bit 15 | | DigitalOutput32 |

| | | |
|------------------------|-----------------------|-----------------------|
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. 5AC800.EXT3-K05

HwId: 0xAAB5

Functionmodel: 0

Description: Extension C-Keys 5PB ES right

| Input | | |
|---------------------------|-----------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_5AC800.EXT3-K05_a | Unsigned8 | |
| | Bit 7 | Key01 |
| | Bit 6 | Key02 |
| | Bit 5 | Key03 |
| | Bit 4 | Key04 |
| | Bit 3 | Key05 |
| | Bit 2 | Key06 |
| | Bit 1 | Key07 |
| | Bit 0 | Key08 |
| Inputs_5AC800.EXT3-K05_b | Unsigned8 | |
| | Bit 7 | Key09 |
| | Bit 6 | Key10 |
| | Bit 5 | Key11 |
| | Bit 4 | Key12 |
| | Bit 3 | Key13 |
| | Bit 2 | Key14 |
| | Bit 1 | Key15 |
| | Bit 0 | Key16 |
| Inputs_5AC800.EXT3-K05_c | Unsigned8 | |
| | Bit 7 | Key17 |
| Output | | |
| Name | Input | Output |
| Outputs_5AC800.EXT3-K05_a | | Unsigned8 |
| | Bit 7 | Led01 |
| | Bit 6 | Led02 |
| | Bit 5 | Led03 |
| | Bit 4 | Led04 |
| | Bit 3 | Led05 |
| | Bit 2 | Led06 |
| | Bit 1 | Led07 |
| | Bit 0 | Led08 |
| Outputs_5AC800.EXT3-K05_b | | Unsigned8 |
| | Bit 7 | Led09 |
| | Bit 6 | Led10 |
| | Bit 5 | Led11 |
| | Bit 4 | Led12 |
| | Bit 3 | Led13 |
| | Bit 2 | Led14 |
| | Bit 1 | Led15 |
| | Bit 0 | Led16 |
| Outputs_5AC800.EXT3-K05_c | | Unsigned8 |
| | Bit 7 | Led17 |
| | Bit 6 | Led18 |
| | Bit 5 | Led19 |
| | Bit 4 | Led20 |
| | Bit 3 | Led21 |
| | Bit 2 | Led22 |

| Output | | | |
|---------------------------|-------|-------|-----------|
| Name | | Input | Output |
| | Bit 1 | | Led23 |
| | Bit 0 | | Led24 |
| Outputs_5AC800.EXT3-K05_d | | | Unsigned8 |
| | Bit 7 | | Led25 |
| | Bit 6 | | Led26 |
| | Bit 5 | | Led27 |
| | Bit 4 | | Led28 |
| | Bit 3 | | Led29 |
| | Bit 2 | | Led30 |
| | Bit 1 | | Led31 |
| | Bit 0 | | Led32 |
| Outputs_5AC800.EXT3-K05_e | | | Unsigned8 |
| | Bit 7 | | Led33 |
| | Bit 6 | | Led34 |
| | Bit 5 | | Led35 |
| | Bit 4 | | Led36 |
| | Bit 3 | | Led37 |
| | Bit 2 | | Led38 |
| | Bit 1 | | Led39 |
| | Bit 0 | | Led40 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

8.1. 7XV108.50-11

HwId: 0x1CE3

Functionmodel: 0

Description: 8 Outputs 24 VDC / 0.1 A

| Input | | | |
|------------------------|-------|-------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_7XV108.50-11_a | | Unsigned8 | |
| | Bit 1 | Overload | |
| | Bit 0 | PowerSupply | |
| Output | | | |
| Name | | Input | Output |
| Outputs_7XV108.50-11_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

8.1. 7XV108.50-12

HwId: 0x1CE4

Functionmodel: 0

Description: 8 Outputs 24 VDC / 0.1 A

| Input | | | |
|------------------------|-------|-------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_7XV108.50-12_a | | Unsigned8 | |
| | Bit 1 | Overload | |
| | Bit 0 | PowerSupply | |
| Output | | | |
| Name | | Input | Output |
| Outputs_7XV108.50-12_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. 7XV108.50-51

HwId: 0x1CEB

Functionmodel: 0

Description: 8 Outputs 24 VDC / 0.1 A

| Input | | | |
|------------------------|-------|-------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_7XV108.50-51_a | | Unsigned8 | |
| | Bit 1 | Overload | |
| | Bit 0 | PowerSupply | |
| Output | | | |
| Name | | Input | Output |
| Outputs_7XV108.50-51_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. 7XV108.50-62

HwId: 0x25B8

Functionmodel: 0

Description: 8 Outputs 24 VDC / 0.1 A

| Input | | | |
|------------------------|-------|-------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_7XV108.50-62_a | | Unsigned8 | |
| | Bit 1 | Overload | |
| | Bit 0 | PowerSupply | |
| Output | | | |
| Name | | Input | Output |
| Outputs_7XV108.50-62_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. 7XV116.50-01

HwId: 0x1AC9

Functionmodel: 0

Description: 16 Outputs 0.1A/24VDC

| Input | | |
|------------------------|-------------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_7XV116.50-01_a | Unsigned8 | |
| Bit 1 | Overload | |
| Bit 0 | PowerSupply | |
| Output | | |
| Name | Input | Output |
| Outputs_7XV116.50-01_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |
| Outputs_7XV116.50-01_b | | Unsigned8 |
| Bit 0 | | DigitalOutput09 |
| Bit 1 | | DigitalOutput10 |
| Bit 2 | | DigitalOutput11 |
| Bit 3 | | DigitalOutput12 |
| Bit 4 | | DigitalOutput13 |
| Bit 5 | | DigitalOutput14 |
| Bit 6 | | DigitalOutput15 |
| Bit 7 | | DigitalOutput16 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

8.1. 7XV116.50-11

HwId: 0x1CE5

Functionmodel: 0

Description: 16 Outputs 0.1A/24VDC

| Input | | |
|------------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_7XV116.50-11_a | Unsigned8 | |
| | Bit 1 | Overload |
| | Bit 0 | PowerSupply |
| Output | | |
| Name | Input | Output |
| Outputs_7XV116.50-11_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |
| Outputs_7XV116.50-11_b | | Unsigned8 |
| | Bit 0 | DigitalOutput09 |
| | Bit 1 | DigitalOutput10 |
| | Bit 2 | DigitalOutput11 |
| | Bit 3 | DigitalOutput12 |
| | Bit 4 | DigitalOutput13 |
| | Bit 5 | DigitalOutput14 |
| | Bit 6 | DigitalOutput15 |
| | Bit 7 | DigitalOutput16 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

8.1. 7XV116.50-12

HwId: 0x1CE6

Functionmodel: 0

Description: 16 Outputs 0.1A/24VDC

| Input | | |
|------------------------|-------------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_7XV116.50-12_a | Unsigned8 | |
| Bit 1 | Overload | |
| Bit 0 | PowerSupply | |
| Output | | |
| Name | Input | Output |
| Outputs_7XV116.50-12_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |
| Outputs_7XV116.50-12_b | | Unsigned8 |
| Bit 0 | | DigitalOutput09 |
| Bit 1 | | DigitalOutput10 |
| Bit 2 | | DigitalOutput11 |
| Bit 3 | | DigitalOutput12 |
| Bit 4 | | DigitalOutput13 |
| Bit 5 | | DigitalOutput14 |
| Bit 6 | | DigitalOutput15 |
| Bit 7 | | DigitalOutput16 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

8.1. 7XV116.50-21

HwId: 0x1CE9

Functionmodel: 0

Description: 16 Outputs 0.1A/24VDC

| Input | | | |
|------------------------|-------|-------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_7XV116.50-21_a | | Unsigned8 | |
| | Bit 1 | Overload | |
| | Bit 0 | PowerSupply | |
| Output | | | |
| Name | | Input | Output |
| Outputs_7XV116.50-21_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_7XV116.50-21_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

8.1. 7XV116.50-23

HwId: 0x1DCA

Functionmodel: 0

Description: 16 Outputs 0.1A/24VDC

| Input | | |
|------------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_7XV116.50-23_a | Unsigned8 | |
| | Bit 1 | Overload |
| | Bit 0 | PowerSupply |
| Output | | |
| Name | Input | Output |
| Outputs_7XV116.50-23_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |
| Outputs_7XV116.50-23_b | | Unsigned8 |
| | Bit 0 | DigitalOutput09 |
| | Bit 1 | DigitalOutput10 |
| | Bit 2 | DigitalOutput11 |
| | Bit 3 | DigitalOutput12 |
| | Bit 4 | DigitalOutput13 |
| | Bit 5 | DigitalOutput14 |
| | Bit 6 | DigitalOutput15 |
| | Bit 7 | DigitalOutput16 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

8.1. 7XV116.50-51

HwId: 0x1CEC

Functionmodel: 0

Description: 16 Outputs 24 VDC / 0.1 A

| Input | | |
|------------------------|-------------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_7XV116.50-51_a | Unsigned8 | |
| Bit 1 | Overload | |
| Bit 0 | PowerSupply | |
| Output | | |
| Name | Input | Output |
| Outputs_7XV116.50-51_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |
| Outputs_7XV116.50-51_b | | Unsigned8 |
| Bit 0 | | DigitalOutput09 |
| Bit 1 | | DigitalOutput10 |
| Bit 2 | | DigitalOutput11 |
| Bit 3 | | DigitalOutput12 |
| Bit 4 | | DigitalOutput13 |
| Bit 5 | | DigitalOutput14 |
| Bit 6 | | DigitalOutput15 |
| Bit 7 | | DigitalOutput16 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

8.1. 7XV116.50-62

HwId: 0x25B7

Functionmodel: 0

Description: 16 Outputs 24 VDC / 0.1 A

| Input | | |
|------------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_7XV116.50-62_a | Unsigned8 | |
| | Bit 1 | Overload |
| | Bit 0 | PowerSupply |
| Output | | |
| Name | Input | Output |
| Outputs_7XV116.50-62_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |
| Outputs_7XV116.50-62_b | | Unsigned8 |
| | Bit 0 | DigitalOutput09 |
| | Bit 1 | DigitalOutput10 |
| | Bit 2 | DigitalOutput11 |
| | Bit 3 | DigitalOutput12 |
| | Bit 4 | DigitalOutput13 |
| | Bit 5 | DigitalOutput14 |
| | Bit 6 | DigitalOutput15 |
| | Bit 7 | DigitalOutput16 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|------------------------|-----------------------|-----------------------|
| No acyclic Data | | |

8.1. 7XV124.50-11

HwId: 0x1CE7

Functionmodel: 0

Description: 24 Outputs 24 VDC / 0.1 A

| Input | | |
|------------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_7XV124.50-11_a | Unsigned8 | |
| | Bit 1 | Overload |
| | Bit 0 | PowerSupply |
| Output | | |
| Name | Input | Output |
| Outputs_7XV124.50-11_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |
| Outputs_7XV124.50-11_b | | Unsigned8 |
| | Bit 0 | DigitalOutput09 |
| | Bit 1 | DigitalOutput10 |
| | Bit 2 | DigitalOutput11 |
| | Bit 3 | DigitalOutput12 |
| | Bit 4 | DigitalOutput13 |
| | Bit 5 | DigitalOutput14 |
| | Bit 6 | DigitalOutput15 |
| | Bit 7 | DigitalOutput16 |
| Outputs_7XV124.50-11_c | | Unsigned8 |
| | Bit 0 | DigitalOutput17 |
| | Bit 1 | DigitalOutput18 |
| | Bit 2 | DigitalOutput19 |
| | Bit 3 | DigitalOutput20 |
| | Bit 4 | DigitalOutput21 |
| | Bit 5 | DigitalOutput22 |
| | Bit 6 | DigitalOutput23 |
| | Bit 7 | DigitalOutput24 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

8.1. 7XV124.50-12

HwId: 0x1CE8

Functionmodel: 0

Description: 24 Outputs 24 VDC / 0.1 A

| Input | | |
|------------------------|-------------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_7XV124.50-12_a | Unsigned8 | |
| Bit 1 | Overload | |
| Bit 0 | PowerSupply | |
| Output | | |
| Name | Input | Output |
| Outputs_7XV124.50-12_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |
| Outputs_7XV124.50-12_b | | Unsigned8 |
| Bit 0 | | DigitalOutput09 |
| Bit 1 | | DigitalOutput10 |
| Bit 2 | | DigitalOutput11 |
| Bit 3 | | DigitalOutput12 |
| Bit 4 | | DigitalOutput13 |
| Bit 5 | | DigitalOutput14 |
| Bit 6 | | DigitalOutput15 |
| Bit 7 | | DigitalOutput16 |
| Outputs_7XV124.50-12_c | | Unsigned8 |
| Bit 0 | | DigitalOutput17 |
| Bit 1 | | DigitalOutput18 |
| Bit 2 | | DigitalOutput19 |
| Bit 3 | | DigitalOutput20 |
| Bit 4 | | DigitalOutput21 |
| Bit 5 | | DigitalOutput22 |
| Bit 6 | | DigitalOutput23 |
| Bit 7 | | DigitalOutput24 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

8.1. 7XV124.50-51

HwId: 0x1CED

Functionmodel: 0

Description: 24 Outputs 24 VDC / 0.1 A

| Input | | |
|------------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_7XV124.50-51_a | Unsigned8 | |
| | Bit 1 | Overload |
| | Bit 0 | PowerSupply |
| Output | | |
| Name | Input | Output |
| Outputs_7XV124.50-51_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |
| Outputs_7XV124.50-51_b | | Unsigned8 |
| | Bit 0 | DigitalOutput09 |
| | Bit 1 | DigitalOutput10 |
| | Bit 2 | DigitalOutput11 |
| | Bit 3 | DigitalOutput12 |
| | Bit 4 | DigitalOutput13 |
| | Bit 5 | DigitalOutput14 |
| | Bit 6 | DigitalOutput15 |
| | Bit 7 | DigitalOutput16 |
| Outputs_7XV124.50-51_c | | Unsigned8 |
| | Bit 0 | DigitalOutput17 |
| | Bit 1 | DigitalOutput18 |
| | Bit 2 | DigitalOutput19 |
| | Bit 3 | DigitalOutput20 |
| | Bit 4 | DigitalOutput21 |
| | Bit 5 | DigitalOutput22 |
| | Bit 6 | DigitalOutput23 |
| | Bit 7 | DigitalOutput24 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

8.1. 7XV124.50-61

HwId: 0xA366

Functionmodel: 0

Description: 24 Outputs 24 VDC / 0.1 A

| Input | | |
|------------------------|-----------|-----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_7XV124.50-61_a | Unsigned8 | |
| | Bit 1 | Overload |
| | Bit 0 | PowerSupply |
| Output | | |
| Name | Input | Output |
| Outputs_7XV124.50-61_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |
| Outputs_7XV124.50-61_b | | Unsigned8 |
| | Bit 0 | DigitalOutput09 |
| | Bit 1 | DigitalOutput10 |
| | Bit 2 | DigitalOutput11 |
| | Bit 3 | DigitalOutput12 |
| | Bit 4 | DigitalOutput13 |
| | Bit 5 | DigitalOutput14 |
| | Bit 6 | DigitalOutput15 |
| | Bit 7 | DigitalOutput16 |
| Outputs_7XV124.50-61_c | | Unsigned8 |
| | Bit 0 | DigitalOutput17 |
| | Bit 1 | DigitalOutput18 |
| | Bit 2 | DigitalOutput19 |
| | Bit 3 | DigitalOutput20 |
| | Bit 4 | DigitalOutput21 |
| | Bit 5 | DigitalOutput22 |
| | Bit 6 | DigitalOutput23 |
| | Bit 7 | DigitalOutput24 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

8.1. 7XV124.50-62

HwId: 0x25B6

Functionmodel: 0

Description: 24 Outputs 24 VDC / 0.1 A

| Input | | | |
|------------------------|-------|-------------|-----------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_7XV124.50-62_a | | Unsigned8 | |
| | Bit 1 | Overload | |
| | Bit 0 | PowerSupply | |
| Output | | | |
| Name | | Input | Output |
| Outputs_7XV124.50-62_a | | | Unsigned8 |
| | Bit 0 | | DigitalOutput01 |
| | Bit 1 | | DigitalOutput02 |
| | Bit 2 | | DigitalOutput03 |
| | Bit 3 | | DigitalOutput04 |
| | Bit 4 | | DigitalOutput05 |
| | Bit 5 | | DigitalOutput06 |
| | Bit 6 | | DigitalOutput07 |
| | Bit 7 | | DigitalOutput08 |
| Outputs_7XV124.50-62_b | | | Unsigned8 |
| | Bit 0 | | DigitalOutput09 |
| | Bit 1 | | DigitalOutput10 |
| | Bit 2 | | DigitalOutput11 |
| | Bit 3 | | DigitalOutput12 |
| | Bit 4 | | DigitalOutput13 |
| | Bit 5 | | DigitalOutput14 |
| | Bit 6 | | DigitalOutput15 |
| | Bit 7 | | DigitalOutput16 |
| Outputs_7XV124.50-62_c | | | Unsigned8 |
| | Bit 0 | | DigitalOutput17 |
| | Bit 1 | | DigitalOutput18 |
| | Bit 2 | | DigitalOutput19 |
| | Bit 3 | | DigitalOutput20 |
| | Bit 4 | | DigitalOutput21 |
| | Bit 5 | | DigitalOutput22 |
| | Bit 6 | | DigitalOutput23 |
| | Bit 7 | | DigitalOutput24 |

Acyclic registers

| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
|-----------------------|-----------------------|-----------------------|
|-----------------------|-----------------------|-----------------------|

No acyclic Data

8.1. 80PS080X3.10-01

HwId: 0xA7A9

Functionmodel: 0

Description: DC Power Supply

| Input | | |
|-----------------|------------|-------------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| StatusPacked01 | Unsigned16 | |
| | Bit 2 | CurrentLimit01 |
| | Bit 0 | ErrorOutput01 |
| | Bit 1 | OverloadError01 |
| | Bit 4 | StatusPhaseDetection01 |
| | Bit 5 | StatusOvertemperature01 |
| | Bit 3 | StatusOvervoltage01 |
| | Bit 6 | ChopperActive01 |
| | Bit 8 | StatusChopper01 |
| | Bit 7 | StatusBleeder01 |
| | Bit 9 | StatusOutput02 |
| Voltage01 | Integer8 | |
| Current01 | Integer16 | |
| Output | | |
| Name | Input | Output |
| ControlPacked01 | | Unsigned8 |
| | Bit 0 | ClearError01 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0402 | 1026 | 2 |
| 0x0405 | 1029 | 1 |
| 0x040E | 1038 | 2 |
| 0x0412 | 1042 | 2 |
| 0x0416 | 1046 | 2 |
| 0x041A | 1050 | 2 |
| 0x0424 | 1060 | 4 |
| 0x042C | 1068 | 4 |
| 0x040A | 1034 | 2 |
| 0x0407 | 1031 | 1 |

8.1. 80SD100XD.C044-01

HwId: 0xA781

Functionmodel: 3

Description: ACOPOSmicro stepper, 2x 10A, X2X, 2x ABR

| Input | | |
|-----------------------------|------------|--------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AbsPos01ActVal | Integer32 | |
| MpGenStatus01 | Unsigned16 | |
| InputStatus | Unsigned8 | |
| AbsPos02ActVal | Integer32 | |
| MpGenStatus02 | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| AbsPos01 | | Integer32 |
| MpGenControl01 | | Unsigned16 |
| MpGenMode01 | | Integer8 |
| Outputs_80SD100XD.C044-01_a | | Unsigned8 |
| Bit 0 | | MpGenBrake01 |
| AbsPos02 | | Integer32 |
| MpGenControl02 | | Unsigned16 |
| MpGenMode02 | | Integer8 |
| Outputs_80SD100XD.C044-01_b | | Unsigned8 |
| Bit 0 | | MpGenBrake02 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0030 | 48 | 1 |
| 0x0031 | 49 | 1 |
| 0x0032 | 50 | 1 |
| 0x0070 | 112 | 1 |
| 0x0071 | 113 | 1 |
| 0x0072 | 114 | 1 |
| 0x0048 | 72 | 2 |
| 0x0088 | 136 | 2 |
| 0x0034 | 52 | 2 |
| 0x0074 | 116 | 2 |
| 0x0036 | 54 | 2 |
| 0x0038 | 56 | 2 |
| 0x0076 | 118 | 2 |
| 0x0078 | 120 | 2 |
| 0x003A | 58 | 2 |
| 0x007A | 122 | 2 |
| 0x003C | 60 | 4 |
| 0x0040 | 64 | 4 |
| 0x007C | 124 | 4 |
| 0x0080 | 128 | 4 |
| 0x0044 | 68 | 2 |
| 0x0084 | 132 | 2 |
| 0x004B | 75 | 1 |
| 0x008B | 139 | 1 |
| 0x0046 | 70 | 1 |
| 0x0086 | 134 | 1 |
| 0x00D0 | 208 | 1 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x00D1 | 209 | 1 |
| 0x00D4 | 212 | 4 |
| 0x00DC | 220 | 4 |
| 0x00D2 | 210 | 1 |
| 0x00D8 | 216 | 4 |
| 0x00E0 | 224 | 4 |
| 0x00D3 | 211 | 1 |
| 0x004C | 76 | 1 |
| 0x008C | 140 | 1 |

8.1. 80SD100XD.C0XX-01

HwId: 0xA4A4

Functionmodel: 3

Description: ACOPOSmicro stepper, 2x 10A, X2X

| Input | | |
|-----------------------------|------------|--------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| AbsPos01ActVal | Integer32 | |
| MpGenStatus01 | Unsigned16 | |
| InputStatus | Unsigned8 | |
| AbsPos02ActVal | Integer32 | |
| MpGenStatus02 | Unsigned16 | |
| Output | | |
| Name | Input | Output |
| AbsPos01 | | Integer32 |
| MpGenControl01 | | Unsigned16 |
| MpGenMode01 | | Integer8 |
| Outputs_80SD100XD.C0XX-01_a | | Unsigned8 |
| Bit 0 | | MpGenBrake01 |
| AbsPos02 | | Integer32 |
| MpGenControl02 | | Unsigned16 |
| MpGenMode02 | | Integer8 |
| Outputs_80SD100XD.C0XX-01_b | | Unsigned8 |
| Bit 0 | | MpGenBrake02 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0030 | 48 | 1 |
| 0x0031 | 49 | 1 |
| 0x0032 | 50 | 1 |
| 0x0070 | 112 | 1 |
| 0x0071 | 113 | 1 |
| 0x0072 | 114 | 1 |
| 0x0048 | 72 | 2 |
| 0x0088 | 136 | 2 |
| 0x0034 | 52 | 2 |
| 0x0074 | 116 | 2 |
| 0x0036 | 54 | 2 |
| 0x0038 | 56 | 2 |
| 0x0076 | 118 | 2 |
| 0x0078 | 120 | 2 |
| 0x003A | 58 | 2 |
| 0x007A | 122 | 2 |
| 0x003C | 60 | 4 |
| 0x0040 | 64 | 4 |
| 0x007C | 124 | 4 |
| 0x0080 | 128 | 4 |
| 0x0044 | 68 | 2 |
| 0x0084 | 132 | 2 |
| 0x004B | 75 | 1 |
| 0x008B | 139 | 1 |
| 0x0046 | 70 | 1 |
| 0x0086 | 134 | 1 |
| 0x00D0 | 208 | 1 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x00D1 | 209 | 1 |
| 0x00D4 | 212 | 4 |
| 0x00DC | 220 | 4 |
| 0x00D2 | 210 | 1 |
| 0x00D8 | 216 | 4 |
| 0x00E0 | 224 | 4 |
| 0x00D3 | 211 | 1 |

8.1. X20BR9300

HwId: 0x1BC1

Functionmodel: 0

Description: Bus receiver, IO supply 24 VDC and bus

| Input | | | |
|-----------------------|-------|---------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20BR9300_a | | Unsigned8 | |
| | Bit 0 | StatusInput01 | |
| | Bit 2 | StatusInput02 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20BT9100

HwId: 0x1BC2

Functionmodel: 0

Description: Bus transmitter, IO supply 24 VDC

| Input | | | |
|-----------------------|-------|---------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20BT9100_a | | Unsigned8 | |
| | Bit 0 | StatusInput01 | |
| | Bit 2 | StatusInput02 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20BT9400

HwId: 0xA238

Functionmodel: 0

Description: Bus transmitter, IO supply 24 VDC, X67 supply

| Input | | |
|-----------------------|-----------|---------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20BT9400_a | Unsigned8 | |
| | Bit 0 | StatusInput01 |
| | Bit 2 | StatusInput02 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20cBR9300

HwId: 0xDD48

Functionmodel: 0

Description: Coated Bus receiver, IO supply 24 VDC and bus

| Input | | | |
|-----------------------|-------|---------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20cBR9300_a | | Unsigned8 | |
| | Bit 0 | StatusInput01 | |
| | Bit 2 | StatusInput02 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20cBT9100

HwId: 0xE219

Functionmodel: 0

Description: Coated Bus transmitter, IO supply 24 VDC

| Input | | | |
|-----------------------|-------|---------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20cBT9100_a | | Unsigned8 | |
| | Bit 0 | StatusInput01 | |
| | Bit 2 | StatusInput02 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20cPD2113

HwId: 0xE23B

Functionmodel: 0

Description: Coated Potential distribution, 6xGND 6x24V, internal/external

| Input | | | |
|-----------------------|-------|-------------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20cPD2113_a | | Unsigned8 | |
| | Bit 0 | StatusFuse | |
| | Bit 1 | StatusPowerSupply | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20cPS2100

HwId: 0xE23C

Functionmodel: 0

Description: Coated 24 VDC power supply module for internal IO supply

| Input | | | |
|-----------------------|-------|-----------------------|-----------------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20cPS2100_a | | Unsigned8 | |
| | Bit 0 | StatusInput01 | |
| | Bit 2 | StatusInput02 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |
| | | | |
| Acyclic registers | | | |
| Registeraddress (hex) | | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | | |

8.1. X20cPS2110

HwId: 0xE23D

Functionmodel: 0

Description: Coated 24 VDC power supply module for internal IO supply, fuse

| Input | | |
|-----------------------|-----------------------|-----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20cPS2110_a | Unsigned8 | |
| | Bit 0 | StatusInput01 |
| | Bit 2 | StatusInput02 |
| | Bit 1 | StatusInput03 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20cPS3300

HwId: 0xDF13

Functionmodel: 0

Description: Coated 24 VDC power supply module for internal IO supply and bus

| Input | | |
|-----------------------|-----------|---------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20cPS3300_a | Unsigned8 | |
| | Bit 0 | StatusInput01 |
| | Bit 2 | StatusInput02 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20cPS3300_C1

HwId: 0xDF13
Functionmodel: 0
Description: Coated 24 VDC power supply module for internal IO supply and bus

| Input | | |
|----------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20cPS3310

HwId: 0xDD46
Functionmodel: 0
Description: Coated 24 VDC power supply module for internal IO supply and bus, fuse

| Input | | | |
|-----------------------|-------|-----------------------|-----------------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20cPS3310_a | | Unsigned8 | |
| | Bit 0 | StatusInput01 | |
| | Bit 2 | StatusInput02 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |
| | | | |
| Acyclic registers | | | |
| Registeraddress (hex) | | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | | |

8.1. X20cPS9400

HwId: 0xD579

Functionmodel: 0

Description: Coated 24 VDC power supply module for BC, internal IO supply and bus

| Input | | |
|-----------------------|---------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20cPS9400_a | Unsigned8 | |
| Bit 0 | StatusInput01 | |
| Bit 2 | StatusInput02 | |
| SupplyCurrent | Unsigned8 | |
| SupplyVoltage | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20cPS9400_C1

HwId: 0xD579
Functionmodel: 0
Description: Coated 24 VDC power supply module for BC, internal IO supply and bus

| Input | | |
|----------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20CS1011

HwId: 0xA38D

Functionmodel: 0

Description: Interface Module SmartWire

| Input | | |
|-----------------------|-----------------------|-----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| SlaveStatus | Unsigned16 | |
| MasterStatus | Unsigned16 | |
| MasterOperatingState | Unsigned8 | |
| Input01 | Unsigned8 | |
| Input02 | Unsigned8 | |
| Input03 | Unsigned8 | |
| Input04 | Unsigned8 | |
| Input05 | Unsigned8 | |
| Input06 | Unsigned8 | |
| Input07 | Unsigned8 | |
| Input08 | Unsigned8 | |
| Input09 | Unsigned8 | |
| Input10 | Unsigned8 | |
| Input11 | Unsigned8 | |
| Input12 | Unsigned8 | |
| Input13 | Unsigned8 | |
| Input14 | Unsigned8 | |
| Input15 | Unsigned8 | |
| Input16 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| FastOutput01_02 | | Unsigned8 |
| FastOutput03_04 | | Unsigned8 |
| FastOutput05_06 | | Unsigned8 |
| FastOutput07_08 | | Unsigned8 |
| FastOutput09_10 | | Unsigned8 |
| FastOutput11_12 | | Unsigned8 |
| FastOutput13_14 | | Unsigned8 |
| FastOutput15_16 | | Unsigned8 |
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0103 | 259 | 1 |
| 0x0101 | 257 | 1 |

8.1. X20CS1012

HwId: 0xCABF

Functionmodel: 254

Description: Interface Module MBus

| Input | | |
|-----------------|------------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| MBusState | Unsigned8 | |
| ValidDataByte | Unsigned8 | |
| InvalidDataByte | Unsigned8 | |
| MBusOperation | Unsigned8 | |
| Data1 | Unsigned32 | |
| Data2 | Unsigned32 | |
| Data3 | Unsigned32 | |
| InputSequence | Unsigned8 | |
| RxByte01 | Unsigned8 | |
| RxByte02 | Unsigned8 | |
| RxByte03 | Unsigned8 | |
| RxByte04 | Unsigned8 | |
| RxByte05 | Unsigned8 | |
| RxByte06 | Unsigned8 | |
| RxByte07 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| MBusCommand | | Unsigned8 |
| OutputSequence | | Unsigned8 |
| TxByte01 | | Unsigned8 |
| TxByte02 | | Unsigned8 |
| TxByte03 | | Unsigned8 |
| TxByte04 | | Unsigned8 |
| TxByte05 | | Unsigned8 |
| TxByte06 | | Unsigned8 |
| TxByte07 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0317 | 791 | 1 |
| 0x0309 | 777 | 1 |
| 0x030D | 781 | 1 |
| 0x0312 | 786 | 2 |
| 0x0327 | 807 | 1 |
| 0x0319 | 793 | 1 |
| 0x031D | 797 | 1 |
| 0x0322 | 802 | 2 |
| 0x0337 | 823 | 1 |
| 0x0329 | 809 | 1 |
| 0x032D | 813 | 1 |
| 0x0332 | 818 | 2 |

8.1. X20CS1013

HwId: 0xDE85

Functionmodel: 0

Description: Interface Module Dali

| Input | | |
|---------------------|------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Dali_State | Unsigned16 | |
| Dali_AnswerCounter | Unsigned8 | |
| Dali_RequestCounter | Unsigned8 | |
| Dali_Answer | Unsigned8 | |

| Output | | |
|--------------|-------|------------|
| Name | Input | Output |
| Dali_Enable | | Unsigned8 |
| Dali_Control | | Unsigned16 |
| Dali_Address | | Unsigned8 |
| Dali_Command | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0204 | 516 | 4 |

8.1. X20CS1020

HwId: 0x1FCF

Functionmodel: 254

Description: Interface Modul RS232

| Input | | |
|----------------|-----------|---------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| InputSequence | Unsigned8 | |
| RxByte01 | Unsigned8 | |
| RxByte02 | Unsigned8 | |
| RxByte03 | Unsigned8 | |
| RxByte04 | Unsigned8 | |
| RxByte05 | Unsigned8 | |
| RxByte06 | Unsigned8 | |
| RxByte07 | Unsigned8 | |
| RxByte08 | Unsigned8 | |
| RxByte09 | Unsigned8 | |
| RxByte10 | Unsigned8 | |
| RxByte11 | Unsigned8 | |
| RxByte12 | Unsigned8 | |
| RxByte13 | Unsigned8 | |
| RxByte14 | Unsigned8 | |
| RxByte15 | Unsigned8 | |
| RxByte16 | Unsigned8 | |
| RxByte17 | Unsigned8 | |
| RxByte18 | Unsigned8 | |
| RxByte19 | Unsigned8 | |
| RxByte20 | Unsigned8 | |
| RxByte21 | Unsigned8 | |
| RxByte22 | Unsigned8 | |
| RxByte23 | Unsigned8 | |
| RxByte24 | Unsigned8 | |
| RxByte25 | Unsigned8 | |
| RxByte26 | Unsigned8 | |
| RxByte27 | Unsigned8 | |
| InterfaceError | Unsigned8 | |
| | Bit 0 | StartBitError |
| | Bit 1 | StopBitError |
| | Bit 2 | ParityError |
| | Bit 3 | RXoverrun |
| Output | | |
| Name | Input | Output |
| OutputSequence | | Unsigned8 |
| TxByte01 | | Unsigned8 |
| TxByte02 | | Unsigned8 |
| TxByte03 | | Unsigned8 |
| TxByte04 | | Unsigned8 |
| TxByte05 | | Unsigned8 |
| TxByte06 | | Unsigned8 |
| TxByte07 | | Unsigned8 |
| TxByte08 | | Unsigned8 |
| TxByte09 | | Unsigned8 |
| TxByte10 | | Unsigned8 |
| TxByte11 | | Unsigned8 |

| Output | | |
|-------------------|-------|-------------------|
| Name | Input | Output |
| TxByte12 | | Unsigned8 |
| TxByte13 | | Unsigned8 |
| TxByte14 | | Unsigned8 |
| TxByte15 | | Unsigned8 |
| TxByte16 | | Unsigned8 |
| TxByte17 | | Unsigned8 |
| TxByte18 | | Unsigned8 |
| TxByte19 | | Unsigned8 |
| TxByte20 | | Unsigned8 |
| TxByte21 | | Unsigned8 |
| TxByte22 | | Unsigned8 |
| TxByte23 | | Unsigned8 |
| TxByte24 | | Unsigned8 |
| TxByte25 | | Unsigned8 |
| TxByte26 | | Unsigned8 |
| TxByte27 | | Unsigned8 |
| AckInterfaceError | | Unsigned8 |
| | Bit 0 | QuitStartBitError |
| | Bit 1 | QuitStopBitError |
| | Bit 2 | QuitParityError |
| | Bit 3 | QuitRXoverrun |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0001 | 1 | 1 |
| 0x00E1 | 225 | 1 |
| 0x00E3 | 227 | 1 |
| 0x0003 | 3 | 1 |
| 0x0005 | 5 | 1 |
| 0x0007 | 7 | 1 |
| 0x000C | 12 | 4 |
| 0x004A | 74 | 2 |
| 0x006A | 106 | 2 |
| 0x1881 | 6273 | 1 |

8.1. X20CS1030

HwId: 0x1FD0

Functionmodel: 254

Description: Interface Modul RS422 / RS485

| Input | | |
|----------------|-----------|---------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| InputSequence | Unsigned8 | |
| RxByte01 | Unsigned8 | |
| RxByte02 | Unsigned8 | |
| RxByte03 | Unsigned8 | |
| RxByte04 | Unsigned8 | |
| RxByte05 | Unsigned8 | |
| RxByte06 | Unsigned8 | |
| RxByte07 | Unsigned8 | |
| RxByte08 | Unsigned8 | |
| RxByte09 | Unsigned8 | |
| RxByte10 | Unsigned8 | |
| RxByte11 | Unsigned8 | |
| RxByte12 | Unsigned8 | |
| RxByte13 | Unsigned8 | |
| RxByte14 | Unsigned8 | |
| RxByte15 | Unsigned8 | |
| RxByte16 | Unsigned8 | |
| RxByte17 | Unsigned8 | |
| RxByte18 | Unsigned8 | |
| RxByte19 | Unsigned8 | |
| RxByte20 | Unsigned8 | |
| RxByte21 | Unsigned8 | |
| RxByte22 | Unsigned8 | |
| RxByte23 | Unsigned8 | |
| RxByte24 | Unsigned8 | |
| RxByte25 | Unsigned8 | |
| RxByte26 | Unsigned8 | |
| RxByte27 | Unsigned8 | |
| InterfaceError | Unsigned8 | |
| | Bit 0 | StartBitError |
| | Bit 1 | StopBitError |
| | Bit 2 | ParityError |
| | Bit 3 | RXoverrun |
| Output | | |
| Name | Input | Output |
| OutputSequence | | Unsigned8 |
| TxByte01 | | Unsigned8 |
| TxByte02 | | Unsigned8 |
| TxByte03 | | Unsigned8 |
| TxByte04 | | Unsigned8 |
| TxByte05 | | Unsigned8 |
| TxByte06 | | Unsigned8 |
| TxByte07 | | Unsigned8 |
| TxByte08 | | Unsigned8 |
| TxByte09 | | Unsigned8 |
| TxByte10 | | Unsigned8 |
| TxByte11 | | Unsigned8 |

| Output | | |
|-------------------|-------|-------------------|
| Name | Input | Output |
| TxByte12 | | Unsigned8 |
| TxByte13 | | Unsigned8 |
| TxByte14 | | Unsigned8 |
| TxByte15 | | Unsigned8 |
| TxByte16 | | Unsigned8 |
| TxByte17 | | Unsigned8 |
| TxByte18 | | Unsigned8 |
| TxByte19 | | Unsigned8 |
| TxByte20 | | Unsigned8 |
| TxByte21 | | Unsigned8 |
| TxByte22 | | Unsigned8 |
| TxByte23 | | Unsigned8 |
| TxByte24 | | Unsigned8 |
| TxByte25 | | Unsigned8 |
| TxByte26 | | Unsigned8 |
| TxByte27 | | Unsigned8 |
| AckInterfaceError | | Unsigned8 |
| | Bit 0 | QuitStartBitError |
| | Bit 1 | QuitStopBitError |
| | Bit 2 | QuitParityError |
| | Bit 3 | QuitRXoverrun |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0001 | 1 | 1 |
| 0x00E1 | 225 | 1 |
| 0x00E3 | 227 | 1 |
| 0x0003 | 3 | 1 |
| 0x0005 | 5 | 1 |
| 0x0007 | 7 | 1 |
| 0x000C | 12 | 4 |
| 0x004A | 74 | 2 |
| 0x006A | 106 | 2 |
| 0x1881 | 6273 | 1 |

8.1. X20CS1070

HwId: 0x1FD1

Functionmodel: 254

Description: Interface Modul CAN

| Input | | |
|----------------|-----------|--------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| InputSequence | Unsigned8 | |
| RxByte01 | Unsigned8 | |
| RxByte02 | Unsigned8 | |
| RxByte03 | Unsigned8 | |
| RxByte04 | Unsigned8 | |
| RxByte05 | Unsigned8 | |
| RxByte06 | Unsigned8 | |
| RxByte07 | Unsigned8 | |
| RxByte08 | Unsigned8 | |
| RxByte09 | Unsigned8 | |
| RxByte10 | Unsigned8 | |
| RxByte11 | Unsigned8 | |
| RxByte12 | Unsigned8 | |
| RxByte13 | Unsigned8 | |
| RxByte14 | Unsigned8 | |
| RxByte15 | Unsigned8 | |
| RxByte16 | Unsigned8 | |
| RxByte17 | Unsigned8 | |
| RxByte18 | Unsigned8 | |
| RxByte19 | Unsigned8 | |
| RxByte20 | Unsigned8 | |
| RxByte21 | Unsigned8 | |
| RxByte22 | Unsigned8 | |
| RxByte23 | Unsigned8 | |
| RxByte24 | Unsigned8 | |
| RxByte25 | Unsigned8 | |
| RxByte26 | Unsigned8 | |
| RxByte27 | Unsigned8 | |
| InterfaceError | Unsigned8 | |
| | Bit 0 | CANwarning |
| | Bit 1 | CANpassive |
| | Bit 2 | CANbusoff |
| | Bit 3 | CANRXoverrun |
| Output | | |
| Name | Input | Output |
| OutputSequence | | Unsigned8 |
| TxByte01 | | Unsigned8 |
| TxByte02 | | Unsigned8 |
| TxByte03 | | Unsigned8 |
| TxByte04 | | Unsigned8 |
| TxByte05 | | Unsigned8 |
| TxByte06 | | Unsigned8 |
| TxByte07 | | Unsigned8 |
| TxByte08 | | Unsigned8 |
| TxByte09 | | Unsigned8 |
| TxByte10 | | Unsigned8 |
| TxByte11 | | Unsigned8 |

| Output | | |
|-------------------|-------|------------------|
| Name | Input | Output |
| TxByte12 | | Unsigned8 |
| TxByte13 | | Unsigned8 |
| TxByte14 | | Unsigned8 |
| TxByte15 | | Unsigned8 |
| TxByte16 | | Unsigned8 |
| TxByte17 | | Unsigned8 |
| TxByte18 | | Unsigned8 |
| TxByte19 | | Unsigned8 |
| TxByte20 | | Unsigned8 |
| TxByte21 | | Unsigned8 |
| TxByte22 | | Unsigned8 |
| TxByte23 | | Unsigned8 |
| TxByte24 | | Unsigned8 |
| TxByte25 | | Unsigned8 |
| TxByte26 | | Unsigned8 |
| TxByte27 | | Unsigned8 |
| AckInterfaceError | | Unsigned8 |
| | Bit 0 | QuitCANwarning |
| | Bit 1 | QuitCANpassive |
| | Bit 2 | QuitCANbusoff |
| | Bit 3 | QuitCANRXoverrun |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x00C1 | 193 | 1 |
| 0x00C3 | 195 | 1 |
| 0x0101 | 257 | 1 |
| 0x0103 | 259 | 1 |
| 0x0105 | 261 | 1 |
| 0x010A | 266 | 2 |
| 0x1881 | 6273 | 1 |

8.1. X20DS1928

HwId: 0xA912

Functionmodel: 0

Description: Interface Module EnDat 2.1/2.2

| Input | | |
|----------------|------------|-----------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| PositionHW | Unsigned32 | |
| PositionLW | Unsigned32 | |
| PosCycle | Integer8 | |
| ErrorInfo | Unsigned8 | |
| EnDatError | Unsigned16 | |
| EnDatWarning | Unsigned16 | |
| InputSequence | Unsigned8 | |
| RxByte01 | Unsigned8 | |
| RxByte02 | Unsigned8 | |
| RxByte03 | Unsigned8 | |
| RxByte04 | Unsigned8 | |
| RxByte05 | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| AckErrorInfo | | Unsigned8 |
| EnDatAck | | Unsigned8 |
| OutputSequence | | Unsigned8 |
| TxByte01 | | Unsigned8 |
| TxByte02 | | Unsigned8 |
| TxByte03 | | Unsigned8 |
| TxByte04 | | Unsigned8 |
| TxByte05 | | Unsigned8 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0201 | 513 | 1 |
| 0x028E | 654 | 2 |
| 0x0185 | 389 | 1 |
| 0x0401 | 1025 | 1 |
| 0x0403 | 1027 | 1 |
| 0x040A | 1034 | 2 |
| 0x040E | 1038 | 2 |
| 0x0414 | 1044 | 4 |
| 0x1001 | 4097 | 1 |
| 0x1201 | 4609 | 1 |
| 0x1203 | 4611 | 1 |

8.1. X20DS438A

HwId: 0xCAC0

Functionmodel: 0

Description: Interface Module IO-Link

| Input | | |
|---------------------|------------|----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| ChannelStatus01 | Unsigned8 | |
| ChannelStatus02 | Unsigned8 | |
| ChannelStatus03 | Unsigned8 | |
| ChannelStatus04 | Unsigned8 | |
| InputData01a | Unsigned8 | |
| InputData01b | Unsigned8 | |
| InputData01c | Unsigned8 | |
| InputData01d | Unsigned8 | |
| InputData02a | Unsigned8 | |
| InputData02b | Unsigned8 | |
| InputData02c | Unsigned8 | |
| InputData02d | Unsigned8 | |
| InputData03a | Unsigned8 | |
| InputData03b | Unsigned8 | |
| InputData03c | Unsigned8 | |
| InputData03d | Unsigned8 | |
| InputData04a | Unsigned8 | |
| InputData04b | Unsigned8 | |
| InputData04c | Unsigned8 | |
| InputData04d | Unsigned8 | |
| EventPortSeq | Unsigned8 | |
| EventQualifier | Unsigned8 | |
| EventCode | Unsigned16 | |
| ParameterCtrlIn | Unsigned8 | |
| ParameterDataIn_0 | Unsigned32 | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DS438A_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DisablePowerSupply01 |
| | Bit 5 | DisablePowerSupply02 |
| | Bit 6 | DisablePowerSupply03 |
| | Bit 7 | DisablePowerSupply04 |
| DsControl01 | | Unsigned8 |
| DsControl02 | | Unsigned8 |
| DsControl03 | | Unsigned8 |
| DsControl04 | | Unsigned8 |
| OutputData01a | | Unsigned8 |
| OutputData01b | | Unsigned8 |
| OutputData01c | | Unsigned8 |
| OutputData01d | | Unsigned8 |
| OutputData02a | | Unsigned8 |
| OutputData02b | | Unsigned8 |
| OutputData02c | | Unsigned8 |

| Output | | |
|----------------------|-------|------------|
| Name | Input | Output |
| OutputData02d | | Unsigned8 |
| OutputData03a | | Unsigned8 |
| OutputData03b | | Unsigned8 |
| OutputData03c | | Unsigned8 |
| OutputData03d | | Unsigned8 |
| OutputData04a | | Unsigned8 |
| OutputData04b | | Unsigned8 |
| OutputData04c | | Unsigned8 |
| OutputData04d | | Unsigned8 |
| EventQuit | | Unsigned8 |
| ParameterIndexOut | | Unsigned16 |
| ParameterSubIndexOut | | Unsigned8 |
| ParameterCtrlOut | | Unsigned8 |
| ParameterDataOut_0 | | Unsigned32 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0201 | 513 | 1 |
| 0x1004 | 4100 | 4 |
| 0x1024 | 4132 | 4 |
| 0x101E | 4126 | 2 |
| 0x1404 | 5124 | 4 |
| 0x1424 | 5156 | 4 |
| 0x141E | 5150 | 2 |
| 0x1804 | 6148 | 4 |
| 0x1824 | 6180 | 4 |
| 0x181E | 6174 | 2 |
| 0x1C04 | 7172 | 4 |
| 0x1C24 | 7204 | 4 |
| 0x1C1E | 7198 | 2 |

8.1. X20PD0011

HwId: 0x267D

Functionmodel: 0

Description: Potential distribution, 12x GND, internal

| Input | | |
|-----------------------|------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20PD0011_a | Unsigned8 | |
| Bit 0 | StatusFuse | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20PD0012

HwId: 0x267E

Functionmodel: 0

Description: Potential distribution, 12x 24V, internal

| Input | | |
|-----------------------|------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20PD0012_a | Unsigned8 | |
| Bit 0 | StatusFuse | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20PD0016

HwId: 0x2680
Functionmodel: 0
Description: Potential distribution, 6xGND 6x24V, external

| Input | | | |
|-----------------------|-------|-----------------------|-----------------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20PD0016_a | | Unsigned8 | |
| | Bit 0 | StatusFuse | |
| | Bit 1 | StatusPowerSupply | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |
| | | | |
| Acyclic registers | | | |
| Registeraddress (hex) | | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | | |

8.1. X20PD2113

HwId: 0x267F

Functionmodel: 0

Description: Potential distribution, 6xGND 6x24V, internal/external

| Input | | | |
|-----------------------|-------|-------------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20PD2113_a | | Unsigned8 | |
| | Bit 0 | StatusFuse | |
| | Bit 1 | StatusPowerSupply | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20PS2100

HwId: 0x1BBF

Functionmodel: 0

Description: 24 VDC power supply module for internal IO supply

| Input | | | |
|-----------------------|-------|---------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20PS2100_a | | Unsigned8 | |
| | Bit 0 | StatusInput01 | |
| | Bit 2 | StatusInput02 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20PS2110

HwId: 0x2016

Functionmodel: 0

Description: 24 VDC power supply module for internal IO supply, fuse

| Input | | |
|------------------------|-----------------------|-----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20PS2110_a | Unsigned8 | |
| | Bit 0 | StatusInput01 |
| | Bit 2 | StatusInput02 |
| | Bit 1 | StatusInput03 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20PS3300

HwId: 0x1BC0

Functionmodel: 0

Description: 24 VDC power supply module for internal IO supply and bus

| Input | | | |
|--------------------|-------|---------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20PS3300_a | | Unsigned8 | |
| | Bit 0 | StatusInput01 | |
| | Bit 2 | StatusInput02 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20PS3300_C1

HwId: 0x1BC0
Functionmodel: 0
Description: 24 VDC power supply module for internal IO supply and bus

| Input | | |
|----------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20PS3310

HwId: 0x2017

Functionmodel: 0

Description: 24 VDC power supply module for internal IO supply and bus, fuse

| Input | | | |
|--------------------|-------|---------------|--------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20PS3310_a | | Unsigned8 | |
| | Bit 0 | StatusInput01 | |
| | Bit 2 | StatusInput02 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20PS4951

HwId: 0x1F43

Functionmodel: 0

Description: 4 x ?10 V Potentiometer Supply

| Input | | |
|-----------------------|-----------|----------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20PS4951_a | Unsigned8 | |
| | Bit 4 | OpenLine01 |
| | Bit 5 | OpenLine02 |
| | Bit 6 | OpenLine03 |
| | Bit 7 | OpenLine04 |
| | Bit 0 | ShortCircuit01 |
| | Bit 1 | ShortCircuit02 |
| | Bit 2 | ShortCircuit03 |
| | Bit 3 | ShortCircuit04 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20PS9400

HwId: 0x1F8C

Functionmodel: 0

Description: 24 VDC power supply module for BC, internal IO supply and bus

| Input | | |
|-----------------------|---------------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Inputs_X20PS9400_a | Unsigned8 | |
| Bit 0 | StatusInput01 | |
| Bit 2 | StatusInput02 | |
| SupplyCurrent | Unsigned8 | |
| SupplyVoltage | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20PS9400_C1

HwId: 0x1F8C

Functionmodel: 0

Description: 24 VDC power supply module for BC, internal IO supply and bus

| Input | | |
|----------------|-----------|--------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X20PS9402

HwId: 0xA389
Functionmodel: 0
Description: 24 VDC power supply module for BC, internal IO supply and bus, no isolation

| Input | | | |
|-----------------------|-------|-----------------------|-----------------------|
| Name | | Input | Output |
| ModuleOK | | Unsigned8 | |
| Inputs_X20PS9402_a | | Unsigned8 | |
| | Bit 0 | StatusInput01 | |
| | Bit 2 | StatusInput02 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |
| | | | |
| Acyclic registers | | | |
| Registeraddress (hex) | | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | | |

8.1. X20ZF0000

HwId: 0xFFFF
Functionmodel: 0
Description: Dummy Module

| | | |
|----------------|-------|--------|
| Input | | |
| Name | Input | Output |
| No Input Data | | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| | | |
|-----------------------|-----------------------|-----------------------|
| Acyclic registers | | |
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

8.1. X67DS438A

HwId: 0xCAAE

Functionmodel: 0

Description: Interface Module IO-Link

| Input | | |
|---------------------|------------|----------------------|
| Name | Input | Output |
| ModuleOK | Unsigned8 | |
| ChannelStatus01 | Unsigned8 | |
| ChannelStatus02 | Unsigned8 | |
| ChannelStatus03 | Unsigned8 | |
| ChannelStatus04 | Unsigned8 | |
| InputData01a | Unsigned8 | |
| InputData01b | Unsigned8 | |
| InputData01c | Unsigned8 | |
| InputData01d | Unsigned8 | |
| InputData02a | Unsigned8 | |
| InputData02b | Unsigned8 | |
| InputData02c | Unsigned8 | |
| InputData02d | Unsigned8 | |
| InputData03a | Unsigned8 | |
| InputData03b | Unsigned8 | |
| InputData03c | Unsigned8 | |
| InputData03d | Unsigned8 | |
| InputData04a | Unsigned8 | |
| InputData04b | Unsigned8 | |
| InputData04c | Unsigned8 | |
| InputData04d | Unsigned8 | |
| EventPortSeq | Unsigned8 | |
| EventQualifier | Unsigned8 | |
| EventCode | Unsigned16 | |
| ParameterCtrlIn | Unsigned8 | |
| ParameterDataIn_0 | Unsigned32 | |
| Output | | |
| Name | Input | Output |
| Outputs_X67DS438A_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DisablePowerSupply01 |
| | Bit 5 | DisablePowerSupply02 |
| | Bit 6 | DisablePowerSupply03 |
| | Bit 7 | DisablePowerSupply04 |
| DsControl01 | | Unsigned8 |
| DsControl02 | | Unsigned8 |
| DsControl03 | | Unsigned8 |
| DsControl04 | | Unsigned8 |
| OutputData01a | | Unsigned8 |
| OutputData01b | | Unsigned8 |
| OutputData01c | | Unsigned8 |
| OutputData01d | | Unsigned8 |
| OutputData02a | | Unsigned8 |
| OutputData02b | | Unsigned8 |
| OutputData02c | | Unsigned8 |

| Output | | |
|----------------------|-------|------------|
| Name | Input | Output |
| OutputData02d | | Unsigned8 |
| OutputData03a | | Unsigned8 |
| OutputData03b | | Unsigned8 |
| OutputData03c | | Unsigned8 |
| OutputData03d | | Unsigned8 |
| OutputData04a | | Unsigned8 |
| OutputData04b | | Unsigned8 |
| OutputData04c | | Unsigned8 |
| OutputData04d | | Unsigned8 |
| EventQuit | | Unsigned8 |
| ParameterIndexOut | | Unsigned16 |
| ParameterSubIndexOut | | Unsigned8 |
| ParameterCtrlOut | | Unsigned8 |
| ParameterDataOut_0 | | Unsigned32 |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0201 | 513 | 1 |
| 0x1004 | 4100 | 4 |
| 0x1024 | 4132 | 4 |
| 0x101E | 4126 | 2 |
| 0x1404 | 5124 | 4 |
| 0x1424 | 5156 | 4 |
| 0x141E | 5150 | 2 |
| 0x1804 | 6148 | 4 |
| 0x1824 | 6180 | 4 |
| 0x181E | 6174 | 2 |
| 0x1C04 | 7172 | 4 |
| 0x1C24 | 7204 | 4 |
| 0x1C1E | 7198 | 2 |

9. Special Module (FW dependent)

9.1. X20cDI9371_C101

HwId: 0xD574

Functionmodel: 0

Description: Coated 12 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | | |
|--------------------------|-------|----------------|--------|
| Name | | Input | Output |
| Inputs_X20cDI9371_C101_a | | Unsigned8 | |
| | Bit 0 | DigitalInput01 | |
| | Bit 1 | DigitalInput02 | |
| | Bit 2 | DigitalInput03 | |
| | Bit 3 | DigitalInput04 | |
| | Bit 4 | DigitalInput05 | |
| | Bit 5 | DigitalInput06 | |
| | Bit 6 | DigitalInput07 | |
| | Bit 7 | DigitalInput08 | |
| Inputs_X20cDI9371_C101_b | | Unsigned8 | |
| | Bit 0 | DigitalInput09 | |
| | Bit 1 | DigitalInput10 | |
| | Bit 2 | DigitalInput11 | |
| | Bit 3 | DigitalInput12 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

9.1. X20cDO9322_C101

HwId: 0xD578

Functionmodel: 0

Description: Coated 12 Outputs 24 VDC / 0.5 A

| Input | | |
|---------------------------|-------|-----------------|
| Name | Input | Output |
| No Input Data | | |
| Output | | |
| Name | Input | Output |
| Outputs_X20cDO9322_C101_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |
| Outputs_X20cDO9322_C101_b | | Unsigned8 |
| | Bit 0 | DigitalOutput09 |
| | Bit 1 | DigitalOutput10 |
| | Bit 2 | DigitalOutput11 |
| | Bit 3 | DigitalOutput12 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

9.1. X20DI8371_C101

HwId: 0xA4AB

Functionmodel: 0

Description: 8 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | | |
|-------------------------|-------|----------------|--------|
| Name | | Input | Output |
| Inputs_X20DI8371_C101_a | | Unsigned8 | |
| | Bit 0 | DigitalInput01 | |
| | Bit 1 | DigitalInput02 | |
| | Bit 2 | DigitalInput03 | |
| | Bit 3 | DigitalInput04 | |
| | Bit 4 | DigitalInput05 | |
| | Bit 5 | DigitalInput06 | |
| | Bit 6 | DigitalInput07 | |
| | Bit 7 | DigitalInput08 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

9.1. X20DI9371_C101

HwId: 0x1B95

Functionmodel: 0

Description: 12 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | |
|-------------------------|-----------|----------------|
| Name | Input | Output |
| Inputs_X20DI9371_C101_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Inputs_X20DI9371_C101_b | Unsigned8 | |
| | Bit 0 | DigitalInput09 |
| | Bit 1 | DigitalInput10 |
| | Bit 2 | DigitalInput11 |
| | Bit 3 | DigitalInput12 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

9.1. X20DIF371_C101

HwId: 0xC0E8

Functionmodel: 0

Description: 16 Digital Inputs 24 VDC, Sink, IEC 61131-2, Type 1

| Input | | |
|-------------------------|-----------|----------------|
| Name | Input | Output |
| Inputs_X20DIF371_C101_a | Unsigned8 | |
| | Bit 0 | DigitalInput01 |
| | Bit 1 | DigitalInput02 |
| | Bit 2 | DigitalInput03 |
| | Bit 3 | DigitalInput04 |
| | Bit 4 | DigitalInput05 |
| | Bit 5 | DigitalInput06 |
| | Bit 6 | DigitalInput07 |
| | Bit 7 | DigitalInput08 |
| Inputs_X20DIF371_C101_b | Unsigned8 | |
| | Bit 0 | DigitalInput09 |
| | Bit 1 | DigitalInput10 |
| | Bit 2 | DigitalInput11 |
| | Bit 3 | DigitalInput12 |
| | Bit 4 | DigitalInput13 |
| | Bit 5 | DigitalInput14 |
| | Bit 6 | DigitalInput15 |
| | Bit 7 | DigitalInput16 |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0012 | 18 | 1 |

9.1. X20DO8322_C101

HwId: 0xA4AC

Functionmodel: 0

Description: 8 Outputs 24 VDC / 0.5 A

| Input | | |
|--------------------------|-------|-----------------|
| Name | Input | Output |
| No Input Data | | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DO8322_C101_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

9.1. X20DO9322_C101

HwId: 0x1B9A

Functionmodel: 0

Description: 12 Outputs 24 VDC / 0.5 A

| Input | | |
|--------------------------|-------|-----------------|
| Name | Input | Output |
| No Input Data | | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DO9322_C101_a | | Unsigned8 |
| | Bit 0 | DigitalOutput01 |
| | Bit 1 | DigitalOutput02 |
| | Bit 2 | DigitalOutput03 |
| | Bit 3 | DigitalOutput04 |
| | Bit 4 | DigitalOutput05 |
| | Bit 5 | DigitalOutput06 |
| | Bit 6 | DigitalOutput07 |
| | Bit 7 | DigitalOutput08 |
| Outputs_X20DO9322_C101_b | | Unsigned8 |
| | Bit 0 | DigitalOutput09 |
| | Bit 1 | DigitalOutput10 |
| | Bit 2 | DigitalOutput11 |
| | Bit 3 | DigitalOutput12 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

9.1. X20DOF322_C101

HwId: 0xC0EA

Functionmodel: 0

Description: 16 Outputs 24 VDC / 0.5 A

| Input | | |
|--------------------------|-------|-----------------|
| Name | Input | Output |
| No Input Data | | |
| Output | | |
| Name | Input | Output |
| Outputs_X20DOF322_C101_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |
| Outputs_X20DOF322_C101_b | | Unsigned8 |
| Bit 0 | | DigitalOutput09 |
| Bit 1 | | DigitalOutput10 |
| Bit 2 | | DigitalOutput11 |
| Bit 3 | | DigitalOutput12 |
| Bit 4 | | DigitalOutput13 |
| Bit 5 | | DigitalOutput14 |
| Bit 6 | | DigitalOutput15 |
| Bit 7 | | DigitalOutput16 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

9.1. X67AI1323_C101

HwId: 0x16F2

Functionmodel: 0

Description: 4 Inputs 0 to 20 mA

| Input | | |
|-----------------------|-----------|--------|
| Name | Input | Output |
| AnalogInput01 | Integer16 | |
| AnalogInput02 | Integer16 | |
| AnalogInput03 | Integer16 | |
| AnalogInput04 | Integer16 | |
| Output | | |
| Name | Input | Output |
| No Output Data | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| 0x0010 | 16 | 1 |
| 0x0012 | 18 | 1 |
| 0x0014 | 20 | 2 |
| 0x0016 | 22 | 2 |

9.1. X67DI1371_C101

HwId: 0x1434
Functionmodel: 0
Description: 8 Inputs 24 VDC, 1 ms

| Input | | | |
|-------------------------|-------|----------------|--------|
| Name | | Input | Output |
| Inputs_X67DI1371_C101_a | | Unsigned8 | |
| | Bit 0 | DigitalInput01 | |
| | Bit 1 | DigitalInput02 | |
| | Bit 2 | DigitalInput03 | |
| | Bit 3 | DigitalInput04 | |
| | Bit 4 | DigitalInput05 | |
| | Bit 5 | DigitalInput06 | |
| | Bit 6 | DigitalInput07 | |
| | Bit 7 | DigitalInput08 | |
| Output | | | |
| Name | | Input | Output |
| No Output Data | | | |

| Acyclic registers | | |
|-----------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

9.1. X67DM1321_C101

HwId: 0x1311

Functionmodel: 0

Description: 8 In- /Outputs 24 VDC

| Input | | |
|--------------------------|----------------|-----------------|
| Name | Input | Output |
| Inputs_X67DM1321_C101_a | Unsigned8 | |
| Bit 0 | DigitalInput01 | |
| Bit 1 | DigitalInput02 | |
| Bit 2 | DigitalInput03 | |
| Bit 3 | DigitalInput04 | |
| Bit 4 | DigitalInput05 | |
| Bit 5 | DigitalInput06 | |
| Bit 6 | DigitalInput07 | |
| Bit 7 | DigitalInput08 | |
| Output | | |
| Name | Input | Output |
| Outputs_X67DM1321_C101_a | | Unsigned8 |
| Bit 0 | | DigitalOutput01 |
| Bit 1 | | DigitalOutput02 |
| Bit 2 | | DigitalOutput03 |
| Bit 3 | | DigitalOutput04 |
| Bit 4 | | DigitalOutput05 |
| Bit 5 | | DigitalOutput06 |
| Bit 6 | | DigitalOutput07 |
| Bit 7 | | DigitalOutput08 |
| I/O Mask | | Unsigned8 |
| Input filter | | Unsigned8 |

| Acyclic registers | | |
|------------------------|-----------------------|-----------------------|
| Registeraddress (hex) | Registeraddress (dec) | Registerwidth (Bytes) |
| No acyclic Data | | |

