

X20DI2653

1 General Information

The module is equipped with 2 inputs for 3-wire connections. It is designed for an input voltage of 100 to 240 VAC.

- 2 digital inputs
- 100 to 240 VAC inputs
- 50 Hz or 60 Hz
- 3-wire connections
- 240 V coded

Danger!

Risk of electric shock!

The terminal block is only permitted to conduct voltage when it is connected. It is not permitted to be disconnected or connected while voltage is applied or have voltage applied to it while it is removed under any circumstances.

This module is not permitted to be the last module connected on the X2X Link network. At least one subsequent X20ZF dummy module must provide protection against contact.

2 Order data


Model number	Short description	Figure
	Digital inputs	
X20DI2653	X20 digital input module, 2 inputs, 100 to 240 VAC, 240 V keyed, 3-wire connections	
	Required accessories	
	Bus modules	
X20BM12	X20 bus module, 240 VDC keyed, internal I/O supply continuous	
	Terminal blocks	
X20TB32	X20 terminal block, 12-pin, 240 VAC keyed	

Table 1: X20DI2653 - Order data

3 Technical data

Model number	X20DI2653
Short description	
I/O module	2 digital inputs 100 to 240 VAC for 3-wire connections
General information	
B&R ID code	0x2544
Status indicators	I/O function per channel, operating state, module status
Diagnostics	
Module run/error	Yes, using LED status indicator and software
External I/O power supply	Yes, using software status (typ. threshold 85 VAC)
Power consumption	
Bus	0.14 W
Internal I/O	-
External I/O	0.55 W
Additional power dissipation caused by actuators (resistive) [W]	-
Certifications	
CE	Yes
KC	Yes
EAC	Yes
UL	cULus E115267 Industrial control equipment
HazLoc	cCSAus 244665 Process control equipment for hazardous locations Class I, Division 2, Groups ABCD, T5
ATEX	Zone 2, II 3G Ex nA nC IIA T5 Gc IP20, Ta (see X20 user's manual) FTZU 09 ATEX 0083X
Digital inputs	
Nominal voltage	100 to 240 VAC
Input filter	
Software	Default 1 ms, configurable between 0 and 25 ms in 0.2 ms intervals
Hardware	
1 → 0	≤30 ms
0 → 1	≤40 ms
Connection type	3-wire connections
Rated frequency	47 to 63 Hz
Switching threshold	
Low	<40 VAC
High	>79 VAC
Isolation voltage between channel and bus	1 minute 2500 VAC
Input voltage	
Maximum	264 VAC
Input current	
100 VAC / 60 Hz	4 mA (Rev. ≥ E0), 5 mA (Rev. < E0)
240 VAC / 50 Hz	8.5 mA (Rev. ≥ E0), 11 mA (Rev. < E0)
Sensor power supply	
Voltage	Corresponds to the module power supply
Summation current	2 A _{eff}
Short-circuit proof	No
Electrical properties	
Electrical isolation	Channel isolated from bus Channel not isolated from channel
Operating conditions	
Mounting orientation	
Horizontal	Yes
Vertical	Yes
Installation elevation above sea level	
0 to 2000 m	No limitation
>2000 m	Not permitted
Degree of protection per EN 60529	IP20
Ambient conditions	
Temperature	
Operation	
Horizontal mounting orientation	-25 to 60°C
Vertical mounting orientation	-25 to 50°C
Derating	-
Storage	-40 to 85°C
Transport	-40 to 85°C


Table 2: X20DI2653 - Technical data

Model number	X20DI2653
Relative humidity	
Operation	5 to 95%, non-condensing
Storage	5 to 95%, non-condensing
Transport	5 to 95%, non-condensing
Mechanical properties	
Note	Order 1x terminal block X20TB32 separately. Order 1x bus module X20BM12 separately.
Spacing	12.5 ^{+0.2} mm

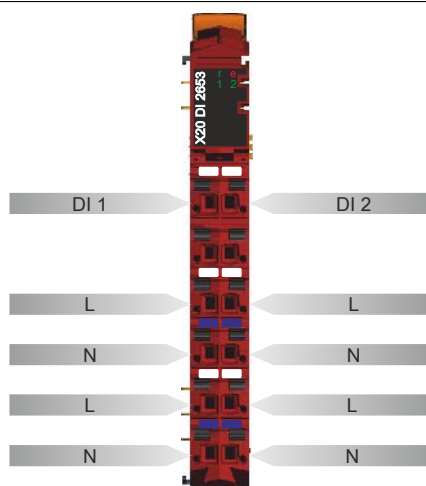
Table 2: X20DI2653 - Technical data

4 Status LEDs

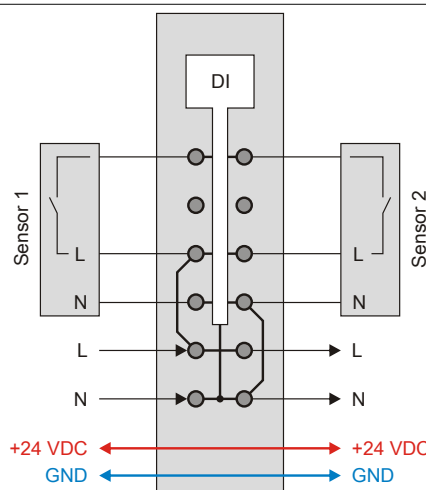
For a description of the various operating modes, see section "Additional information - Diagnostic LEDs" of the X20 system user's manual.

Image	LED	Color	Status	Description
	r	Green	Off	No power to module
			Single flash	RESET mode
			Blinking	PREOPERATIONAL mode
			On	RUN mode
	e	Red	Off	Module supply not connected or everything OK
			Double flash	External supply is too low or not connected
	e + r	Red on / Green single flash		Invalid firmware
	1 - 2	Green		Input status of the corresponding digital input

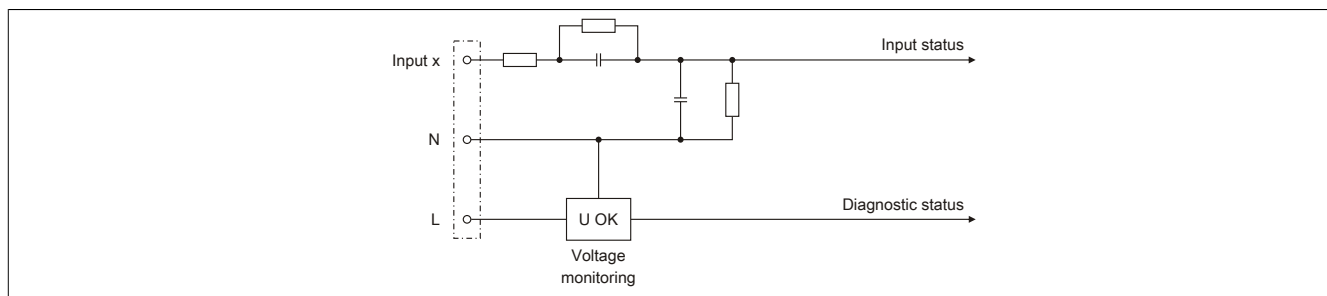
5 Pinout



6 Connection example

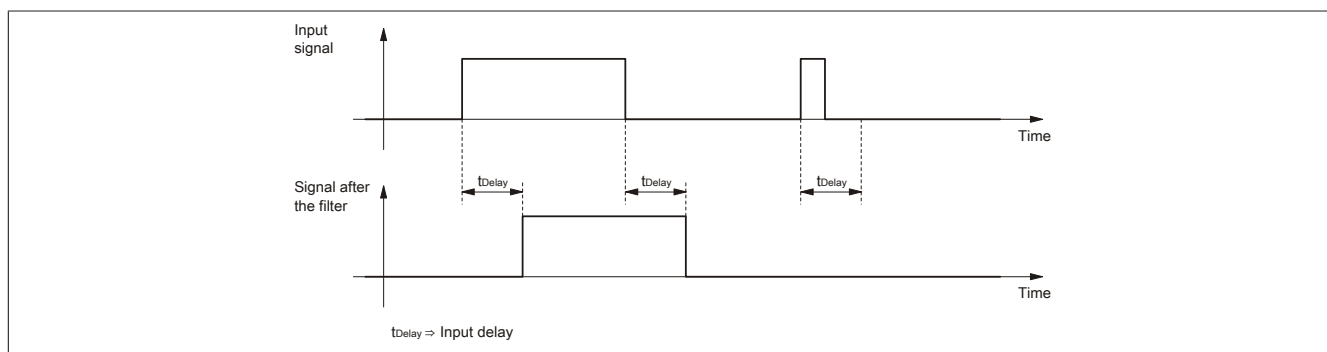


7 Input circuit diagram



8 Input filter

An input filter is available for each input. The input delay can be set using register "[ConfigOutput01](#)" on page 6. Disturbance pulses which are shorter than the input delay are suppressed by the input filter.



9 Register description

9.1 General data points

In addition to the registers described in the register description, the module has additional general data points. These are not module-specific but contain general information such as serial number and hardware variant.

General data points are described in section "Additional information - General data points" of the X20 system user's manual.

9.2 Function model 0 - Standard

Register	Fixed offset	Name	Data type	Read		Write	
				Cyclic	Acyclic	Cyclic	Acyclic
Configuration							
18	-	ConfigOutput01 (input filter)	USINT				•
Communication							
0	1	DigitalInput	USINT	•			
		DigitalInput01	Bit 0				
		DigitalInput02	Bit 1				
		PowerSupply	Bit 7				

Fixed modules require their data points to be in a specific order in the X2X frame. Cyclic access occurs according to a predefined offset, not based on the register address.

Acyclic access continues to be based on the register numbers.

9.3 Function model 254 - Bus Controller

Register	Offset ¹⁾	Name	Data type	Read		Write	
				Cyclic	Acyclic	Cyclic	Acyclic
Configuration							
18	-	ConfigOutput01 (input filter)	USINT				•
Communication							
0	0	Input status of digital inputs 1 to 2	USINT	•			
		DigitalInput01	Bit 0				
		DigitalInput02	Bit 1				
		PowerSupply	Bit 7				

1) The offset specifies where the register is within the CAN object.

9.3.1 Using the module on the bus controller

Function model 254 "Bus controller" is used by default only by non-configurable bus controllers. All other bus controllers can use additional registers and functions depending on the fieldbus used.

For detailed information, see section "Additional information - Using I/O modules on the bus controller" of the X20 user's manual (version 3.50 or later).

9.3.2 CAN I/O bus controller

The module occupies 1 digital logical slot on CAN I/O.

9.4 Digital inputs

Unfiltered

The input state is collected with a fixed offset to the network cycle and transferred in the same cycle.

Filtered

The filtered status is collected with a fixed offset to the network cycle and transferred in the same cycle. Filtering takes place asynchronously to the network in multiples of 200 µs with a network-related jitter of up to 50 µs.

9.4.1 Digital input filter

Name:

ConfigOutput01

This register can be used to specify the filter value for all digital inputs.

The filter value can be configured in steps of 100 μ s. It makes sense to enter values in steps of 2, however, since the input signals are sampled every 200 μ s.

Data type	Value	Filter
USINT	0	No software filter (bus controller default setting)
	2	0.2 ms

	250	25 ms - Higher values are limited to this value

9.4.2 Input status of digital inputs 1 to 2

Name:

DigitalInput or

DigitalInput01 to DigitalInput02

PowerSupply

The input status of digital inputs 1 to 2 is mapped in this register.

Only function model 0 - Standard:

The "packed inputs" setting in the Automation Studio I/O configuration is used to determine whether all of this register's bits should be set up individually as data points in the Automation Studio I/O mapping ("DigitalInput01", "DigitalInput02" and "PowerSupply") or whether this register should be displayed as an individual USINT data point ("DigitalInput").

Data type	Value	Information
USINT	0 to 255	Packed inputs = On
	See the bit structure.	Packed inputs = Off or function model \neq 0 - Standard

Bit structure:

Bit	Name	Value	Information
0	DigitalInput01	0 or 1	Input status - Digital input 1
1	DigitalInput02	0 or 1	Input state - Digital input 2
2 - 6	Reserved	0	
7	PowerSupply	0	Supply voltage too low
		1	Supply voltage >80 VAC

9.5 Minimum cycle time

The minimum cycle time specifies the time up to which the bus cycle can be reduced without communication errors occurring. It is important to note that very fast cycles reduce the idle time available for handling monitoring, diagnostics and acyclic commands.

Minimum cycle time	
Without filtering	100 μ s
With filtering	150 μ s

9.6 Minimum I/O update time

The minimum I/O update time defines how far the bus cycle can be reduced while still allowing an I/O update to take place in each cycle.

Minimum I/O update time	
Without filtering	100 μ s
With filtering	200 μ s