8AC140.60-3

1 General information

The AC110 plug-in module can be used in an ACOPOS slot (requires two slots).

The ACOPOS servo drive connection which the AC140 is plugged into, has an emulation of an AC110 - CAN interface plug-in module on slot 1. All other CAN stations are connected via the CAN interface IF2.

The module offers interchangeable application memory in the form of a CompactFlash card as well as a separate backup battery for the module. 1)

It is equipped with up to four application interfaces:

- One RS232 interface (IF1) for programming and configuring using B&R Automation Studio™
- · One CAN interface (IF2) for connecting to a CAN network
- one PROFIBUS DP slave interface (IF3) for connecting to a PROFIBUS network

In addition, a maximum of three digital inputs / outputs are provided as well as one analog input (±10 V differential input).

The digital inputs and outputs can be configured individually as input or output. Additional functions such as a counter function with direction switching (stepper motor) or period and gate measurement are integrated. The inputs and outputs are scanned directly by the CPU module; the ACOPOS servo drive does not have direct

The analog input has a resolution of 12 bits and an analog input filter with 10kHz (3rd order low pass).

2 Order data

access to these inputs and outputs.

Model number	Short description	Figure
	Plug-in modules	
8AC140.60-3	ACOPOS plug-in module, CPU, x86 100 MHz Intel compatible, 32 MB DRAM, 32 kB SRAM, removable application memory: CompactFlash, 1 CAN interface, 1 Ethernet interface 100 Base-T, 1 PROFIBUS DP slave interface, 1 RS232 interface, 3 digital I/O points configurable as 24 VDC input or 500 mA output, 1 analog input ±10 V, order program memory and 0TB708 terminal block separately	AC 140
	Required accessories	
	CompactFlash cards	
0CFCRD.0128E.01	CompactFlash 128 MB WD extended temp.	
0CFCRD.0512E.01	CompactFlash 512 MB WD extended temp.	
5CFCRD.0064-03	CompactFlash 64 MB Western Digital (SLC)	
5CFCRD.0128-03	CompactFlash 128 MB Western Digital (SLC)	
5CFCRD.0256-03	CompactFlash 256 MB Western Digital (SLC)	9 1 1 1 1 1 1 1 1
5CFCRD.0512-03	CompactFlash 512 MB Western Digital (SLC)	Har Oll
5CFCRD.1024-03	CompactFlash 1 GB Western Digital (SLC)	
5CFCRD.2048-03	CompactFlash 2 GB Western Digital (SLC)	
5CFCRD.4096-03	CompactFlash 4 GB Western Digital (SLC)	
5CFCRD.8192-03	CompactFlash 8 GB Western Digital (SLC)	
	Terminal blocks	
0TB704.91	Accessory terminal block, 4-pin, cage clamps 2.5 mm ²	
0TB708:91-02	Accessory terminal block, 8 pins, 20 pieces cage clamp 1,5 mm ²	
0TB708.91	Accessory terminal block, 8-pin, cage clamps 1.5 mm ²	
	Optional accessories	
	Batteries	
0AC201.91	Lithium batteries 4 pcs., 3 V / 950 mAh button cell We hereby state that the lithium cells contained in this shipment qualify as "partly regulated". Handle with care. If the package is damaged, inspect the cells, repack intact cells and protect cells against short circuits. For emergency information, call RENATA SA at + 41 61 319 28 27	
	Cables	
0G0001.00-090	PC - PLC/PW cable, RS232, online cable	

Table 1: 8AC140.60-3 - Order data

¹⁾ Application memory must be ordered separately.

8AC140.60-3

Model number	Short description	Figure
	Infrastructure components	
0AC912.9	Bus adapter, CAN, 1 CAN interface	
0AC913.92	Bus adapter, CAN, 2 CAN interfaces, including 30 cm attachment cable (DSUB)	
7AC911.9	Bus connector, CAN	

Table 1: 8AC140.60-3 - Order data

3 Technical data

Product ID	8AC140.60-3
General information	
Module type	ACOPOS double-width plug-in module
B&R ID code	0x26D9
Slot 1)	Slots 1 + 2
Power consumption	Max. 4.5 W
ACOPOS capability	Yes
Visual Components support	Yes
Certification	.00
CE	Yes
cULus	Yes
Controller	
Operating system	AC140 (version V2.67 and higher)
Processor clock	100 MHz
DRAM	32 MB
SRAM	32 kB
Inputs/Outputs	
Module-side connection	8-pin connector
Configuration of digital inputs/outputs	Individually configurable as inputs or outputs
Interfaces	.
IF1 interface	
Туре	RS232
Design	9-pin male DSUB connector
Status indicators	X1 LED
Electrical isolation	No
Max. baud rate	115.2 kbaud
Max. distance	15 m / 19200 Baud
IF2 interface	10 111 10200 2000
Туре	CAN bus
Design	9-pin male DSUB connector
Status indicators	RX / TX LEDs
Bus terminating resistor	Externally wired
Electrical isolation	Yes
Max. distance	1000 m
Network-capable	Yes
Max. transfer rate	
Bus length ≤60 m	500 kbit/s
Bus length ≤200 m	250 kbit/s
Bus length ≤1000 m	50 kbit/s
IF3 interface	
Туре	RS485
Design	9-pin female DSUB connector
Status indicators	PB LED
Bus terminating resistor	External T-connector
Controller	ASIC SPC3
Electrical isolation	Yes
RAM	1.5 kB
Max. distance	1000 m
Network-capable	Yes
Transfer protocol	PROFIBUS DP
Max. transfer rate	
Bus length ≤100 m	12 Mbit∕s
Bus length ≤200 m	1.5 Mbit/s
Bus length ≤400 m	500 kbit/s
Bus length ≤1000 m	187.5 kbit/s
240 longar = 1000 m	101.0 1010

Table 2: 8AC140.60-3 - Technical data

Product ID	8AC140.60-3
IF5 interface	UAO 140.00°3
Type	Ethernet
	Male RJ45 connector
Design Status indicators	ACT LED
Baud rate	10/100 Mbit/s
Electrical isolation	Yes
Max. distance	100 m
Network-capable	Yes
Incremental encoder	
Counter size	16-bit
Input frequency	Max. 20 kHz
Evaluation	4x
Signal form	Square wave pulse
Encoder monitoring	No
Counter frequency	Max. 80 kHz
Reference frequency	Max. 20 kHz
Distance between edges	Min. 5 µs
Inputs	o po
Input 1	Channel A
Input 1	Channel B
·	
Input 3	Reference pulse R
Digital inputs 2)	
Quantity	Max. 3
Modulation compared to ground potential	Max. ±30 V
Wiring	Sink
Input current at nominal voltage	Approx. 4.2 mA
Input delay	<5 µs
Switching threshold	
Low	<5 V
High	>15 V
Input voltage	
Nominal	24 VDC
Maximum	30 VDC
Electrical isolation	30 VBO
Channel - ACOPOS	Yes
	**
Channel - Channel	No
Event counter	
Event counter Signal form	Square wave pulse
Event counter Signal form Input frequency	Square wave pulse Max. 100 kHz
Event counter Signal form Input frequency Pulse length	Square wave pulse Max. 100 kHz Min. 5 µs
Event counter Signal form Input frequency	Square wave pulse Max. 100 kHz
Event counter Signal form Input frequency Pulse length	Square wave pulse Max. 100 kHz Min. 5 µs
Event counter Signal form Input frequency Pulse length Counter size	Square wave pulse Max. 100 kHz Min. 5 µs
Event counter Signal form Input frequency Pulse length Counter size Inputs	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Signal form	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Square wave pulse Max. 100 kHz
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Max. 100 kHz Min. 5 µs Max. 100 kHz
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Period measurement Signal form Input frequency Pulse length Counter frequency Internal	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Max. 100 kHz Min. 5 µs
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Period measurement Signal form Input frequency Pulse length Counter frequency Internal External	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Max. 100 kHz Min. 5 µs Max. 100 kHz
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Period measurement Signal form Input frequency Pulse length Counter frequency Internal	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Max. 100 kHz Min. 5 µs
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Period measurement Signal form Input frequency Pulse length Counter frequency Internal External	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Max. 100 kHz Min. 5 µs
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Internal External Pulse length Cate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Internal External Analog inputs	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Max. 100 kHz Min. 5 µs
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Counter frequency Internal External Pulse length Coate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Internal External Analog inputs Digital converter resolution	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Max. 100 kHz Square wave pulse Max. 100 kHz Min. 5 µs Max. 100 kHz 12-bit
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Period measurement Signal form Counter frequency Period measurement Signal form Input frequency Pulse length Counter frequency Pulse length Counter frequency Internal External Analog inputs Digital converter resolution Conversion time	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse 31.25 kHz or 4 MHz Min. 5 µs Max. 100 kHz 12-bit <50 µs
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Period measurement Signal form Counter frequency Period measurement Signal form Input frequency Pulse length Counter frequency Pulse length Counter frequency Internal External Analog inputs Digital converter resolution Conversion time	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Square wave pulse Max. 100 kHz 100 kHz 112-bit 12-bit 150 µs INT 16 \$8001 - \$7FFF
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Dulse length Signal form Input frequency Pulse length Counter frequency Dulse length Counter frequency Internal External Analog inputs Digital converter resolution Conversion time Output format	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Max. 100 kHz 12-bit <50 µs INT 16 \$8001 - \$7FFF LSB = \$0010 = 4.88 mV
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Dulse length Cate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Internal External Analog inputs Digital converter resolution Conversion time Output format Design Electrical isolation	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Max. 100 kHz Square wave pulse Max. 100 kHz 12-bit <50 µs INT 16 \$8001 - \$7FFF LSB = \$0010 = 4.88 mV Differential input
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Dulse length Signal form Input frequency Pulse length Counter frequency Dulse length Counter frequency Internal External Analog inputs Digital converter resolution Conversion time Output format Design Electrical isolation Input - ACOPOS 3)	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Max. 100 kHz 12-bit <50 µs INT 16 \$8001 - \$7FFF LSB = \$0010 = 4.88 mV
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Dulse length Signal form Input frequency Pulse length Counter frequency Dulse length Counter frequency Internal External Analog inputs Digital converter resolution Conversion time Output format Design Electrical isolation Input - ACOPOS 3) Input signal	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Max. 100 kHz Square wave pulse Max. 100 kHz 31.25 kHz or 4 MHz Min. 5 µs 31.25 kHz or 4 MHz Min. 5 µs 31.25 kHz or 4 MHz Min. 5 µs 31.25 kHz or 4 MHz Max. 100 kHz 12-bit 50 µs INT 16 \$8001 - \$7FFF LSB = \$0010 = 4.88 mV Differential input No, max. modulation: ±13 V
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Dulse length Counter frequency Period measurement Signal form Input frequency Pulse length Counter frequency Internal External Analog inputs Digital converter resolution Conversion time Output format Design Electrical isolation Input - ACOPOS 3) Input signal Nominal	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Max. 100 kHz Square wave pulse Max. 100 kHz 12-bit 450 µs INT 16 \$8001 - \$7FFF LSB = \$0010 = 4.88 mV Differential input No, max. modulation: ±13 V
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Distal form Input frequency Pulse length Counter frequency Pulse length Counter frequency Internal External Analog inputs Digital converter resolution Conversion time Output format Design Electrical isolation Input - ACOPOS 3) Input signal Nominal Maximum	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Max. 100 kHz Min. 5 µs 31.25 kHz or 4 MHz Min. 5 µs 31.25 kHz or 4 MHz Min. 5 µs 31.25 kHz or 4 MHz Max. 100 kHz 12-bit 50 µs INT 16 \$8001 - \$7FFF LSB = \$0010 = 4.88 mV Differential input No, max. modulation: ±13 V -10 to +10 V -13 to +13 V
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Pise length Signal form Input frequency Pulse length Counter frequency Internal External Analog inputs Digital converter resolution Conversion time Output format Design Electrical isolation Input - ACOPOS 3) Input signal Nominal Maximum Operating modes	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Max. 100 kHz Square wave pulse Max. 100 kHz 12-bit 450 µs INT 16 \$8001 - \$7FFF LSB = \$0010 = 4.88 mV Differential input No, max. modulation: ±13 V Cyclic measurement non-synchronous to 50 µs ACOPOS clock
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Pilse length Signal form Input frequency Pulse length Counter frequency Internal External Pulse length Counter frequency Internal External Analog inputs Digital converter resolution Conversion time Output format Design Electrical isolation Input - ACOPOS 3) Input signal Nominal Maximum Operating modes Conversion procedure	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Max. 100 kHz Is ps 31.25 kHz or 4 MHz Max. 100 kHz 12-bit <50 µs INT 16 \$8001 - \$7FFF LSB = \$0010 = 4.88 mV Differential input No, max. modulation: ±13 V -10 to +10 V -13 to +13 V Cyclic measurement non-synchronous to 50 µs ACOPOS clock Successive approximation
Event counter Signal form Input frequency Pulse length Counter size Inputs Input 1 Gate measurement Signal form Counter frequency Internal External Pulse length Gate frequency Period measurement Signal form Input frequency Pulse length Counter frequency Pise length Signal form Input frequency Pulse length Counter frequency Internal External Analog inputs Digital converter resolution Conversion time Output format Design Electrical isolation Input - ACOPOS 3) Input signal Nominal Maximum Operating modes	Square wave pulse Max. 100 kHz Min. 5 µs 32-bit Counter 1 Square wave pulse 31.25 kHz or 4 MHz Max. 100 kHz Min. 5 µs Max. 100 kHz Square wave pulse Square wave pulse Max. 100 kHz Square wave pulse Max. 100 kHz 12-bit 450 µs INT 16 \$8001 - \$7FFF LSB = \$0010 = 4.88 mV Differential input No, max. modulation: ±13 V Cyclic measurement non-synchronous to 50 µs ACOPOS clock

Table 2: 8AC140.60-3 - Technical data

8AC140.60-3

Product ID	8AC140.60-3	
Common-mode rejection		
DC	Min. 73 dB	
50 Hz	Min. 73 dB	
Nonlinearity	±2 LSB	
Differential input impedance	20 ΜΩ	
Digital outputs		
Quantity	Max. 3	
Readable outputs	Yes	
Continuous short circuit current at 24 V	Typ. 4 A	
Continuous current	Max. 500 mA	
Switching frequency (resistive load)	Max. 100 Hz	
Switching delay	Max. 500 μs (typ. 250 μs)	
Туре	High-side transistor outputs	
Electrical isolation		
Output - ACOPOS	Yes	
Output - Output	No	
Switching voltage		
Minimum	18 VDC	
Nominal	24 VDC	
Maximum	30 VDC	
Protection		
Short circuit protection	Yes	
Overload protection	Yes	
Environmental conditions		
Temperature		
Operation		
Nominal	5 to 40°C	
Maximum	55°C	
Storage	-25 to 55°C	
Transport	-25 to 70°C	
Relative humidity		
Operation	5 to 85%	
Storage	5 to 95%	
Transport	Max. 95% at 40°C	

Table 2: 8AC140.60-3 - Technical data

- 1) The AC140 is a double-width module that occupies slots 1 and 2.
- 2) Shielded cables must be used for inputs 1 3.
- 3) External electrical isolation of the connected sensors is recommended since the analog input is not electrically isolated.

4 Indicators

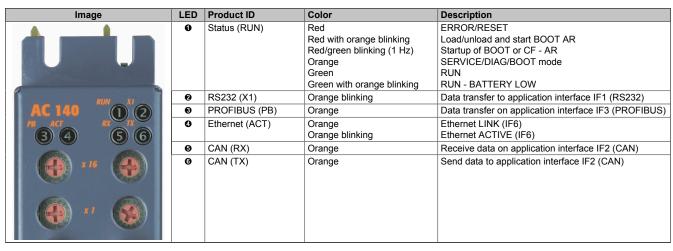


Table 3: Indicators - 8AC140.60-3, 8AC140.61-3

5 Firmware

The firmware is part of the operating system for the ACOPOS servo drives. Firmware is updated by updating the ACOPOS operating system.

6 Setting the CAN node number (IF2)

The CAN node number can be set using two HEX code switches:

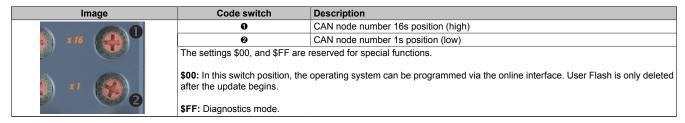


Table 4: Setting the CAN node number

A changed CAN node number will take effect the next time the ACOPOS servo drive is switched on.

There must be a terminating resistor (120 Ω , 0.25 W) between CAN_H and CAN_L at the beginning and end of the CAN bus.

Information:

The CAN bus IF2 is always made up of at least two stations that are integrated in the AC140. These are the AC140 CPU and an AC110 emulation, which the ACOPOS uses for communication. Therefore, the AC140 CPU prevents a potential error in which no other stations are found on the CAN bus. This is why the AC140 CPU does not register a hardware error if there is no physical connection to external CAN devices.

7 Setting the PROFIBUS node number (IF3)

The PROFIBUS node number can be set using two HEX code switches:

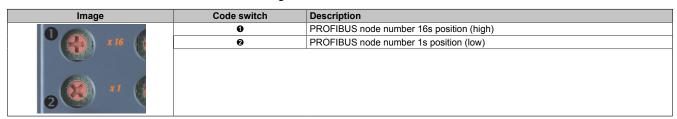


Table 5: Setting the PROFIBUS node number

A changed PROFIBUS node number will take effect the next time the ACOPOS servo drive is switched on.

8 Setting the Ethernet network address (IF6)

The Ethernet network address can be set using software (B&R Automation Studio).

9 Reset button

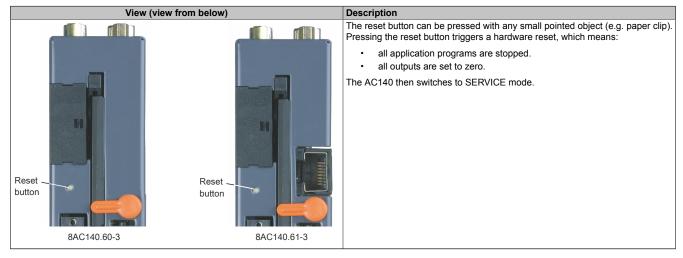


Table 6: Reset button

10 Slot for application memory (CompactFlash)

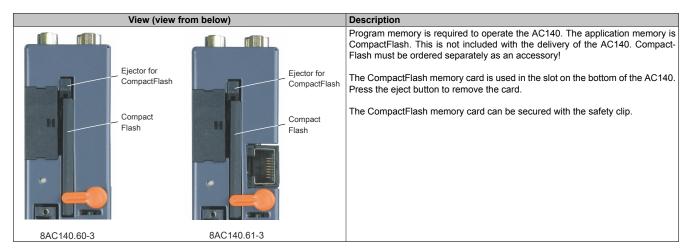


Table 7: Application memory

11 Backup battery AC140

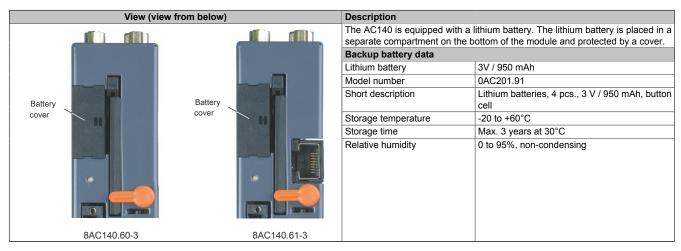


Table 8: Backup battery

Data / real-time buffering

The following areas are buffered:

- · Remanent variables
- User RAM
- · System RAM
- · Real-time clock

Battery monitoring

The battery voltage is checked cyclically. The cyclic load test of the battery does not considerably shorten the battery life, instead it gives an early warning of weakened buffer capacity.

The status information "Battery OK" is available from the "BatteryInfo" system library function.

Battery change interval

Caution!

The battery should be changed every 4 years. The change intervals refer to the average service life and operating conditions and are recommended by B&R. It is not the maximum buffer duration.

Information:

Data stored in the AC140 RAM will be lost if the battery is changed with the PLC switched off! The battery can be changed with power applied, but this is not allowed in all countries!

Warning!

The battery must be replaced by a Type CR2477N Renata battery only. The use of another battery may present a risk of fire or explosion.

The battery may explode if handled improperly. Do not recharge, disassemble or dispose of in fire.

Procedure for changing the battery

- 1. Touch the mounting rail or ground connection (not the power supply!) in order to discharge any electrostatic charge from your body.
- 2. Remove the cover from the lithium battery holder using a screwdriver.



Figure 1: Remove the cover for the lithium battery

Remove the battery from the holder by pulling the removal strip (don't use uninsulated tools because of risk of short circuiting). The battery should not be held by its edges. Insulated tweezers may also be used for removing the battery.

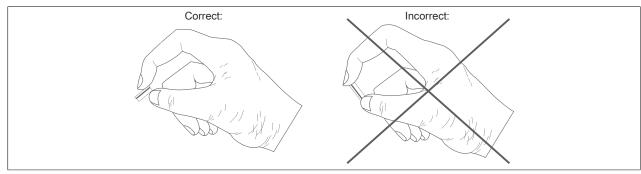


Figure 2: Hold the battery correctly

4. Insert the new battery with correct polarity. The removal strip should be pulled to the right of the battery holder and the "+" side of the battery should be facing left. In order to be able to remove the battery again in future, the removal strip **must be on the right side** of the battery.

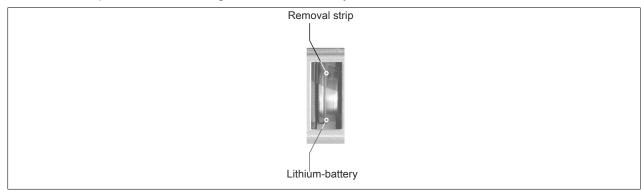


Figure 3: Removal strip should be pulled to the right

- 5. Now wrap the end of the removal strip over the top of the battery and insert it underneath the battery so that it does not protrude from the battery holder.
- 6. Replace cover. Insert the lower edge of the cover in the battery holder opening. Press the upper end of the cover home firmly.

Information:

Lithium batteries are considered hazardous waste. Used batteries should be disposed of appropriately.

12 Input/Output registers

Digital in r/- (16-bit):

Bit no.	Value	Description
0		Logical status of digital I/O 1
1		Logical status of digital I/O 2
2		Logical status of digital I/O 3
3 - 15		Reserved

Digital out r/w (16-bit):

All reserved bits must be written with 0.

Bit no.	Value	Description
0	0	Digital output 1 inactive
	1	Digital output 1 active
1	0	Digital output 2 inactive
	1	Digital output 2 active
2	0	Digital output 3 inactive
	1	Digital output 3 active
3 - 15		Reserved

Analog in (16-bit) r/-:

±10V (12 bit resolution)

Counter (32-bit) r/(w):

In addition to the typical counter modes, this counter has a "Stepper motor counter mode" (see Configuration register bits 4-6).

In stepper motor counter mode, the count direction is set using digital I/O 2 (0...increment, 1...decrement), and the counter clock is on digital I/O 1. Only one clock edge is used for counting (can be configured with bit 3 of the counter configuration register).

Counter configuration (16 bit) r/w:

All reserved bits must be written with 0.

Bit no.	Value	Description
0		Reserved
1	0	AB(R) counter mode: R input disabled
	1	AB(R) counter mode: R input enabled
2		Reserved
3	0	Measurement starts at increasing edge
	1	Measurement starts at falling edge
4 - 6	000	No counter operation
	001	AB(R) counter mode
	010	Event counter mode
	011	Period measurement mode
	100	stepper motor counter mode
	101	Gate measurement mode
	110	Not allowed
	111	Not allowed
7 - 8	00	Counter frequency 4MHz
	01	External counter frequency
	10	Counter frequency 31.25kHz
	11	Not allowed
9 0 Counter overflow recognition disabled / Reset counter overf		Counter overflow recognition disabled / Reset counter overflow bit
	1	Overflow recognition of the continuous counter is enabled (value limited to \$FFFF)
10 - 14		Reserved
15	0	Time / counter reset
	1	Time / counter enabled (ATTENTION: Only set bit after counter configuration is complete)

Status (16 Bit) r/-:

•	,	
Bit no.	Value	Description
0 - 8		Reserved
9	0	Period or gate measurement within the counter range 0 - \$FFFF (only valid if bit 9 is set in the counter configuration word)
	1	Counter overflow during period or gate measurement. Acknowledge by resetting bit 9 of the counter configuration word
10 - 14		Reserved
15	0	Output supply voltage monitoring 24 VDC - OK
	1	Output supply voltage monitoring 24 VDC error

13 Wiring



Figure 4: Overview of AC140 connections (view from front)

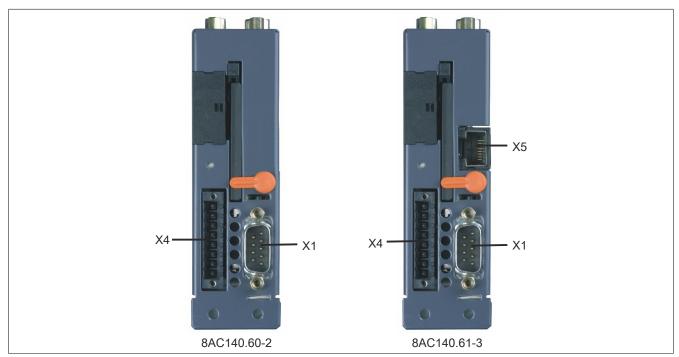


Figure 5: Overview of AC140 connections (view from below)

13.1 X1 - Pinout (application interface IF1 - RS232)

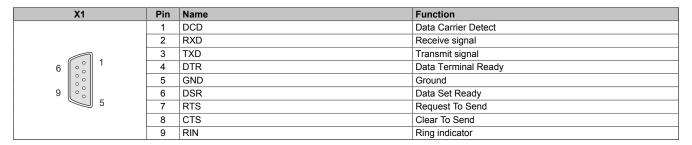


Table 9: X1 connector (RS232) - Pinout

13.2 X2 - Pinout (application interface IF2 - CAN)

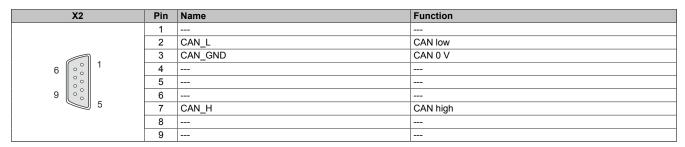


Table 10: X2 connector (CAN) - Pinout

13.3 X3 - Pinout (application interface IF3 - PROFIBUS)

Х3	Pin	Name	Function
	1		
	2		
	3	DATA	Data
9 • • 5	4	CNTRL	Transmit enable
	5	PROFIBUS_GND	PROFIBUS GND (electrically isolated)
6 • • 1	6	+5 V / 50 mA	+5 V supply / 50 mA (electrically isolated)
	7		
	8	DATA\	Data\
	9	CNTRL\	Transmit enable\

Table 11: X3 connector (PROFIBUS) - Pinout

13.4 X4 connector (inputs/outputs) - Pinout

X4	Pin	Name	Function in incremental counter mode	Function in period/gate mea- surement mode	Function in stepper motor counter mode
	1	GND		GND	
	2	+24 VDC		Dig. supply I/O +24V 1)	
	3	Digital I/O 1	A	Count	er input
	4	Digital I/O 2	В		Counting direction
5	5	Digital I/O 3	R	External clock	
	6	Shield		Shield	
	7	Analog I +		Analog Input +	
	8	Analog I -		Analog Input -	

Table 12: X4 connector (inputs/outputs) - Pinout

1) The +24 V supply is only necessary for digital I/O 1 .. 3.