# X20DI4372

# **1** General Information

The module is equipped with 4 inputs for 3-wire connections.

- 4 digital inputs
- Source connection
- 3-wire connections
- 24 VDC and GND for sensor supply
- Software input filter can be configured for entire module

## 2 Order data

Order number	Short description	Figure
	Digital inputs	ŝ
X20DI4372	X20 digital input module, 4 inputs, 24 VDC, source, configurable input filter, 3-wire connections	33
	Required accessories	
	Bus modules	
X20BM11	X20 bus module, 24 VDC keyed, internal I/O power supply con- nected through	
X20BM15	X20 bus module, with node number switch, 24 VDC keyed, in- ternal I/O power supply connected through	
	Terminal blocks	2
X20TB12	X20 terminal block, 12-pin, 24 VDC keyed	

Table 1: X20DI4372 - Order data

# 3 Technical data

Order number	X20DI4372
Short description	
I/O module	4 digital inputs 24 VDC for 3-wire connections
General information	
B&R ID code	0x22A8
Status indicators	I/O function per channel, operating state, module status
Diagnostics	
Module run/error	Yes, using LED status indicator and software
Power consumption	
Bus	0.14 W
Internal I/O	0.59 W
Additional power dissipation caused by actuators	-
(resistive) [W]	
Certifications	
CE	Yes
UKCA	Yes
ATEX	Zone 2, II 3G Ex nA nC IIA T5 Gc
	IP20, Ta (see X20 user's manual)
	FTZÚ 09 ATEX 0083X
UL	cULus E115267
	Industrial control equipment
HazLoc	cCSAus 244665
	Process control equipment for hazardous locations
	tor nazardous locations Class I, Division 2, Groups ABCD, T5
DNV	Temperature: <b>B</b> (0 - 55°C)
DINV	Humidity: B (up to 100%)
	Vibration: <b>B</b> (4 g)
	EMC: B (bridge and open deck)
LR	ENV1
KR	Yes
ABS	Yes
EAC	Yes
KC	Yes
Digital inputs	
Nominal voltage	24 VDC
Input characteristics per EN 61131-2	Type 1
Input voltage	24 VDC -15% / +20%
Input current at 24 VDC	Typ. 3.75 mA
Input circuit	Source
Input filter	
Hardware	≤100 µs
Software	Default 1 ms, configurable between 0 and 25 ms in 0.2 ms increments
Connection type	3-wire connections
Input resistance	Typ. 6.4 kΩ
Switching threshold	i jp. 0. <del>4</del> hzz
Low	<5 VDC
	>15 VDC
High Insulation voltage between channel and bus	500 V <sub>eff</sub>
	JUU V <sub>eff</sub>
Sensor power supply	
Power consumption	Max. 12 W <sup>1)</sup>
Voltage	Module power supply minus voltage drop for short-circuit protection
Voltage drop for short-circuit protection at 500 mA	Max. 2 VDC
Summation current	0.5 A
Short-circuit proof	Yes
Electrical properties	
Electrical isolation	Channel isolated from bus
	Channel not isolated from channel
Operating conditions	
Mounting orientation	
Horizontal	Yes
Vertical	Yes
Installation elevation above sea level	
0 to 2000 m	No limitation
	No limitation Reduction of ambient temperature by 0.5°C per 100 m

Table 2: X20DI4372 - Technical data

Order number	X20DI4372
Ambient conditions	
Temperature	
Operation	
Horizontal mounting orientation	-25 to 60°C
Vertical mounting orientation	-25 to 50°C
Derating	
Storage	-40 to 85°C
Transport	-40 to 85°C
Relative humidity	
Operation	5 to 95%, non-condensing
Storage	5 to 95%, non-condensing
Transport	5 to 95%, non-condensing
Mechanical properties	
Note	Order 1x terminal block X20TB12 separately.
	Order 1x bus module X20BM11 separately.
Pitch	12.5 <sup>+0.2</sup> mm

#### Table 2: X20DI4372 - Technical data

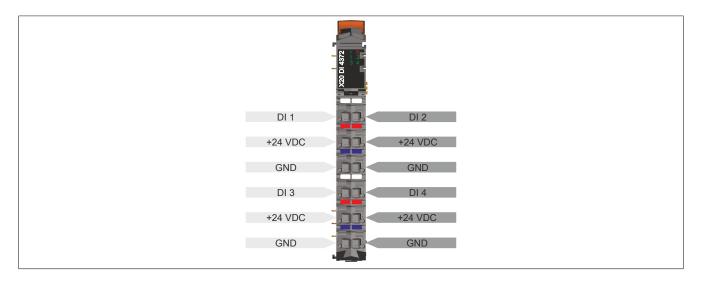
1) The power consumption of the sensors connected to the module is not permitted to exceed 12 W.

### 4 Status LEDs

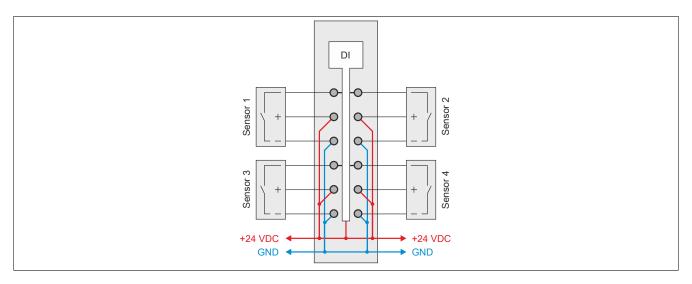
For a description of the various operating modes, see section "Additional information - Diagnostic LEDs" in the X20 system user's manual.

I	mage	LED	Color	Status	Description
		r	Green	Off	No power to module
in the				Single flash	RESET mode
1				Blinking	PREOPERATIONAL mode
2	r e			On	RUN mode
437		е	Red	Off	Module supply not connected or everything OK
DI 4	3 4	e+r	Red on / Green	single flash	Invalid firmware
		1 - 4	Green		Input status of the corresponding digital input
X20					

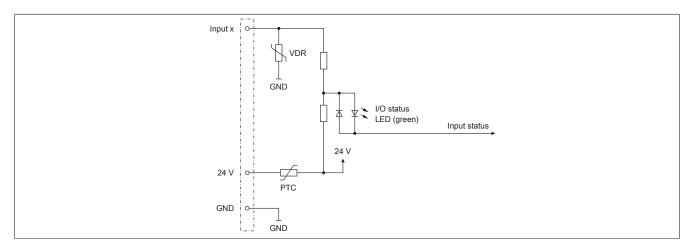
### **5** Pinout



# **6** Connection example

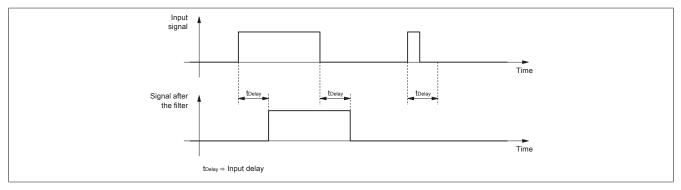


## 7 Input circuit diagram



### 8 Input filter

An input filter is available for each input. The input delay can be set using register "ConfigOutput01" on page 6. Disturbance pulses that are shorter than the input delay are suppressed by the input filter.



### **9 Register description**

### 9.1 General data points

In addition to the registers described in the register description, the module has additional general data points. These are not module-specific but contain general information such as serial number and hardware variant.

General data points are described in section "Additional information - General data points" in the X20 system user's manual.

### 9.2 Function model 0 - Standard

Register	Fixed offset	Name	Data type	Read		Write	
				Cyclic	Acyclic	Cyclic	Acyclic
Configuration	~						
18	-	ConfigOutput01 (input filter)	USINT				•
Communicatio	on						
0	1	DigitalInput	USINT	•			
		DigitalInput01	Bit 0				
		DigitalInput04	Bit 3				

Fixed modules require their data points to be in a specific order in the X2X frame. Cyclic access occurs according to a predefined offset, not based on the register address.

Acyclic access continues to be based on the register numbers.

#### 9.3 Function model 254 - Bus Controller

Register	Offset <sup>1)</sup>	Name	Data type	Read		Write	
				Cyclic	Acyclic	Cyclic	Acyclic
Configuration							
18	-	ConfigOutput01 (input filter)	USINT				•
Communication	n						
0	0	Input status of digital inputs 1 to 4	USINT	•			
		DigitalInput01	Bit 0				
		DigitalInput04	Bit 3				

1) The offset specifies where the register is within the CAN object.

#### 9.3.1 Using the module on the bus controller

Function model 254 "Bus controller" is used by default only by non-configurable bus controllers. All other bus controllers can use other registers and functions depending on the fieldbus used.

For detailed information, see section "Additional information - Using I/O modules on the bus controller" in the X20 user's manual (version 3.50 or later).

#### 9.3.2 CAN I/O bus controller

The module occupies 1 digital logical slot on CAN I/O.

#### 9.4 Digital inputs

#### Unfiltered

The input state is collected with a fixed offset to the network cycle and transferred in the same cycle.

#### Filtered

The filtered state is collected with a fixed offset to the network cycle and transferred in the same cycle. Filtering takes place asynchronously to the network in multiples of 200  $\mu$ s with a network-related jitter of up to 50  $\mu$ s.

### 9.4.1 Digital input filter

Name:

ConfigOutput01

The filter value for all digital inputs can be configured in this register.

The filter value can be configured in steps of 100  $\mu$ s. It makes sense, however, to enter values in steps of 2 since the input signals are sampled in an interval of 200  $\mu$ s.

Data type Valu		Filter
USINT	0	No software filter (bus controller default setting)
	2	0.2 ms
	250	25 ms - Higher values are limited to this value.

#### 9.4.2 Input status of digital inputs 1 to 4

Name: DigitalInput or DigitalInput01 to DigitalInput04

The input status of digital inputs 1 to 4 is mapped in this register.

Only function model 0 - Standard:

The "Packed inputs" setting in the Automation Studio I/O configuration is used to determine whether all of the bits from these registers should be set up individually as data points in the Automation Studio I/O mapping ("DigitalInput01" through "DigitalInput04") or whether this register should be displayed as an individual USINT data point ("DigitalInput").

Data type	Value	Information
USINT	0 to 15	Packed inputs = On
	See the bit structure.	Packed inputs = Off or function model ≠ 0 - Standard

Bit structure:

Bit	Name	Value	Information
0	DigitalInput01	0 or 1	Input status - Digital input 1
3	DigitalInput04	0 or 1	Input state - Digital input 4

#### 9.5 Minimum cycle time

The minimum cycle time specifies how far the bus cycle can be reduced without communication errors occurring. It is important to note that very fast cycles reduce the idle time available for handling monitoring, diagnostics and acyclic commands.

Minimum cycle time		
Without filtering	100 μs	
With filtering	150 μs	

### 9.6 Minimum I/O update time

The minimum I/O update time specifies how far the bus cycle can be reduced so that an I/O update is performed in each cycle.

Minimum I/O update time		
Without filtering	100 µs	
With filtering	200 µs	