# 8AC141.60-2

### 1 General information

The AC141 plug-in module can be used in an ACOPOS slot (requires two slots).

The ACOPOS servo drive in which the AC141 is plugged into is connected via emulation of an AC110 CAN interface plug-in module in slot 1. All other CAN stations are connected via the IF2 CAN interface.

The module offers interchangeable application memory in the form of a CompactFlash card as well as a separate backup battery for the module. 1)

It is equipped with five application interfaces:

- One RS232 interface (IF1) for programming and configuring using B&R Automation Studio™
- Two CAN interfaces (IF2, IF3) for connecting to CAN networks
- One X2X Link interface (IF4)
- One Ethernet interface (IF6) for connecting to an Ethernet network.

In addition, a maximum of three digital inputs / outputs are provided as well as one analog input (±10 V differential input).

The digital inputs and outputs can be configured individually as input or output. Additional functions such as a counter function with direction switching (stepper motor) or period and gate measurement are integrated.

The inputs and outputs are scanned directly by the CPU module; the ACOPOS servo drive does not have direct access to these inputs and outputs.

The analog input has a resolution of 12 bits and an analog input filter with 10 kHz (3rd-order low pass).

#### 2 Order data

Model number	Short description	Figure		
	Plug-in modules			
8AC141.60-2	ACOPOS plug-in module, CPU, x86 100 MHz Intel compatible, 16 MB DRAM, 32 kB SRAM, removable application memory: CompactFlash, 2 CAN interfaces, 1 Ethernet interface 100 Base-T, 1 RS232 interface, 1 X2X Link Master interface, 3 digital I/O points configurable as 24 VDC input or 500 mA output, 1 analog input ±10 V, order program memory and 0TB704 and 0TB708 terminal blocks separately			
	Required accessories			
	CompactFlash cards			
0CFCRD.0128E.01	CompactFlash 128 MB WD extended temp.	<b>○ ○ ○ ○</b>		
0CFCRD.0512E.01	CompactFlash 512 MB WD extended temp.			
5CFCRD.0064-03	CompactFlash 64 MB Western Digital (SLC)			
5CFCRD.0128-03	CompactFlash 128 MB Western Digital (SLC)			
5CFCRD.0256-03	CompactFlash 256 MB Western Digital (SLC)			
5CFCRD.0512-03	CompactFlash 512 MB Western Digital (SLC)	COS.		
5CFCRD.1024-03	CompactFlash 1 GB Western Digital (SLC)			
5CFCRD.2048-03	CompactFlash 2 GB Western Digital (SLC)			
5CFCRD.4096-03	CompactFlash 4 GB Western Digital (SLC)			
5CFCRD.8192-03	CompactFlash 8 GB Western Digital (SLC)			
	Terminal blocks			
0TB704.9	Accessory terminal block, 4-pin, screw clamps 2.5 mm <sup>2</sup>			
0TB704.91	Accessory terminal block, 4-pin, cage clamps 2.5 mm <sup>2</sup>			
0TB708:91-02	Accessory terminal block, 8 pins, 20 pieces cage clamp 1,5 mm <sup>2</sup>			
0TB708.91	Accessory terminal block, 8-pin, cage clamps 1.5 mm <sup>2</sup>			
	Optional accessories			
	Batteries			
0AC201.91	Lithium batteries 4 pcs., 3 V / 950 mAh button cell We hereby state that the lithium cells contained in this shipment qualify as "partly regulated". Handle with care. If the package is damaged, inspect the cells, repack intact cells and protect cells against short circuits. For emergency information, call RENATA SA at + 41 61 319 28 27			
	Cables			

Table 1: 8AC141.60-2 - Order data

<sup>1)</sup> Application memory must be ordered separately.

## 8AC141.60-2

Model number	Short description	Figure
0G0001.00-090	PC - PLC/PW cable, RS232, online cable	
	Infrastructure components	
0AC912.9	Bus adapter, CAN, 1 CAN interface	
0AC913.92	Bus adapter, CAN, 2 CAN interfaces, including 30 cm attachment cable (DSUB)	
7AC911.9	Bus connector, CAN	

Table 1: 8AC141.60-2 - Order data

# 3 Technical data

Product ID	8AC141.60-2
General information	***************************************
Module type	ACOPOS double-width plug-in module
B&R ID code	0x1DDA
Slot 1)	Slots 1 + 2
Power consumption	Max. 4.5 W
ACOPOS capability	Yes
Visual Components support	Yes
Certification	165
CE	Yes
cULus	Yes
KC	Yes
Controller	165
	AC140 (version V/2 90 and higher)
Operating system Processor clock	AC140 (version V2.80 and higher)
	100 MHz
DRAM	16 MB
SRAM	32 kB
Inputs/Outputs	A
Module-side connection	8-pin connector
Configuration of digital inputs/outputs	Individually configurable as inputs or outputs
Interfaces	
IF1 interface	
Туре	RS232
Design	9-pin male DSUB connector
Status indicators	232 LED
Electrical isolation	No
Max. baud rate	115.2 kbaud
Max. distance	15 m / 19200 Baud
IF2 interface	
Туре	CAN bus
Design	9-pin male DSUB connector
Status indicators	CAN1 LED
Bus terminating resistor	Externally wired
Electrical isolation	Yes
Max. distance	1000 m
Network-capable	Yes
Max. transfer rate	
Bus length ≤60 m	500 kbit/s
Bus length ≤200 m	250 kbit/s
Bus length ≤1000 m	50 kbit/s
IF3 interface	
Туре	CAN bus
Design	9-pin male DSUB connector
Status indicators	CAN2 LED
Bus terminating resistor	Externally wired
Electrical isolation	Yes
Max. distance	1000 m
Network-capable	Yes
Max. transfer rate	
Bus length ≤60 m	500 kbit/s
Bus length ≤200 m	250 kbit/s
Bus length ≤1000 m	50 kbit/s
IF4 interface	
Туре	X2X
Design	4-pin connector
Status indicators	X2X LED
Electrical isolation	Yes
Max. distance	100 m
a. diotatioo	100 111

Table 2: 8AC141.60-2 - Technical data

Product ID	8AC141.60-2
IF6 interface	0AU 141.00-Z
Type	Ethernet
Design Status indicators	Male RJ45 connector
Status indicators	ACT LED
Baud rate	10/100 Mbit/s
Electrical isolation	Yes
Max. distance	100 m
Network-capable	Yes
Incremental encoder	
Counter size	16-bit
Input frequency	Max. 20 kHz
Evaluation	4x
Signal form	Square wave pulse
Encoder monitoring	No
Counter frequency	Max. 80 kHz
Reference frequency	Max. 20 kHz
Distance between edges	Min. 5 µs
Inputs	
Input 1	Channel A
Input 2	Channel B
Input 3	Reference pulse R
•	Releience pulse R
Digital inputs 2)	н. о
Quantity	Max. 3
Modulation compared to ground potential	Max. ±30 V
Wiring	Sink
Input current at nominal voltage	Approx. 4.2 mA
Input delay	<5 µs
Switching threshold	
Low	<5 V
High	>15 V
Input voltage	
Nominal	24 VDC
Maximum	30 VDC
	30 VDC
Electrical isolation	
Channel - ACOPOS	Yes
Channel - Channel	No
Event counter	
Signal form	Square wave pulse
Input frequency	Max. 100 kHz
Pulse length	Min. 5 μs
Counter size	32-bit
Inputs	
Input 1	Counter 1
Input 2	Count direction (only in stepper motor mode)
Gate measurement	()
Signal form	Square wave pulse
Counter frequency	ογιαίο πανό μαίδο
'	24.25 kHz or 4 MHz
Internal	31.25 kHz or 4 MHz
External	Max. 100 kHz
Pulse length	Min. 5 µs
Gate frequency	Max. 100 kHz
Period measurement	
Signal form	Square wave pulse
Input frequency	Max. 100 kHz
Pulse length	Min. 5 μs
Counter frequency	
Internal	31.25 kHz or 4 MHz
External	Max. 100 kHz
Analog inputs	THEN THE
Digital converter resolution	12-bit
Conversion time	
	•
Output format	INT 16 \$8001 - \$7FFF LSB = \$0010 = 4.88 mV
Dogian	
Design Electrical in a letting	Differential input
Electrical isolation	
Input - ACOPOS 3)	No, max. modulation: ±13 V
Input signal	
Nominal	-10 to +10 V
Maximum	-13 to +13 V
Operating modes	Cyclic measurement non-synchronous to 50 µs ACOPOS clock
. )	
Conversion procedure	Successive approximation
Conversion procedure	Successive approximation Analog low pass 3rd-order
Conversion procedure Input filter	Successive approximation  Analog low pass 3rd-order  Cutoff frequency: 10 kHz

Table 2: 8AC141.60-2 - Technical data

## 8AC141.60-2

Product ID	8AC141.60-2
Common-mode rejection	
DC	Min. 73 dB
50 Hz	Min. 73 dB
Nonlinearity	±2 LSB
Differential input impedance	20 ΜΩ
Digital outputs	
Quantity	Max. 3
Readable outputs	Yes
Continuous short circuit current at 24 V	Typ. 4 A
Continuous current	Max. 500 mA
Switching frequency (resistive load)	Max. 100 Hz
Switching delay	Max. 500 μs (typ. 250 μs)
Туре	High-side transistor outputs
Electrical isolation	
Output - ACOPOS	Yes
Output - Output	No
Switching voltage	
Minimum	18 VDC
Nominal	24 VDC
Maximum	30 VDC
Protection	
Short circuit protection	Yes
Overload protection	Yes
Environmental conditions	
Temperature	
Operation	
Nominal	5 to 40°C
Maximum	55°C
Storage	-25 to 55°C
Transport	-25 to 70°C
Relative humidity	
Operation	5 to 85%
Storage	5 to 95%
Transport	Max. 95% at 40°C

Table 2: 8AC141.60-2 - Technical data

- 1) 2) 3) The AC141 is a double-width module that occupies slots 1 and 2.
- Shielded cables must be used for inputs 1 3.

  External electrical isolation of the connected sensors is recommended since the analog input is not electrically isolated.

#### 4 Indicators

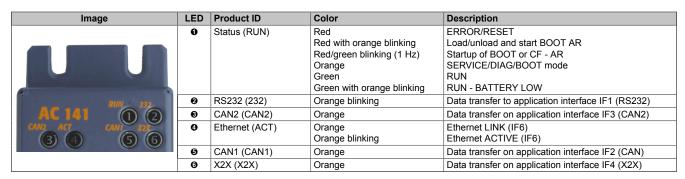


Table 3: AC141 indicators

#### 5 Firmware

The firmware is part of the operating system for the ACOPOS servo drives. Firmware is updated by updating the ACOPOS operating system.

## 6 Setting the CAN node number (IF2)

The CAN node number can be set using two HEX code switches:

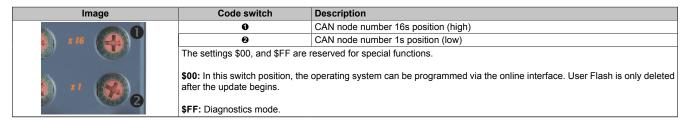


Table 4: Setting the CAN node number

A changed CAN node number will take effect the next time the ACOPOS servo drive is switched on.

There must be a terminating resistor (120  $\Omega$ , 0.25 W) between CAN\_H and CAN\_L at the beginning and end of the CAN bus.

#### Information:

The CAN bus IF2 is always made up of at least two stations that are integrated in the AC141. These are the AC141 CPU and an AC110 emulation, which the ACOPOS servo drive uses for communication. Therefore, the AC141 CPU prevents a potential error in which no other stations are found on the CAN bus. This is why the AC141 CPU does not register a hardware error if there is no physical connection to external CAN devices.

## 7 Setting the CAN node number (IF3)

The CAN node number can be set using two HEX code switches:

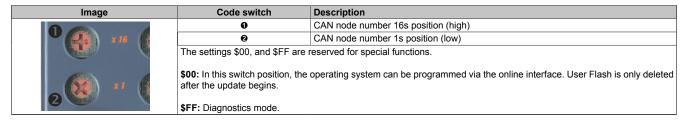


Table 5: Setting the CAN node number (IF3)

A changed CAN node number will take effect the next time the ACOPOS servo drive is switched on.

There must be a terminating resistor (120  $\Omega$ , 0.25 W) between CAN\_H and CAN\_L at the beginning and end of the CAN bus.

# 8 Setting the Ethernet network address (IF6)

The Ethernet network address can be set using software (B&R Automation Studio).

#### 9 Reset button

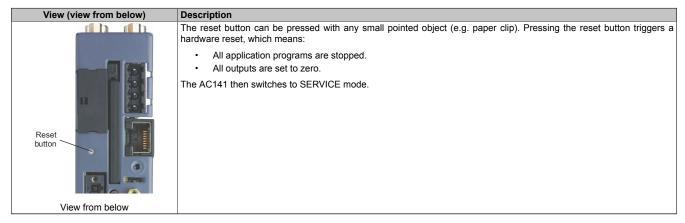


Table 6: Reset button

## 10 Slot for application memory (CompactFlash)

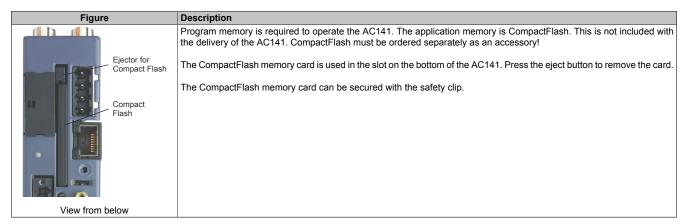


Table 7: Application memory

## 11 Backup battery

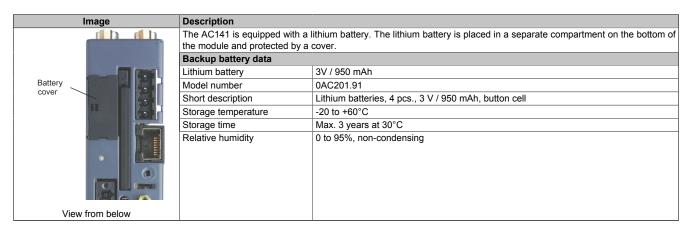


Table 8: Backup battery

## Data / real-time buffering

The following areas are buffered:

- · Remanent variables
- User RAM
- · System RAM
- · Real-time clock

#### **Battery monitoring**

The battery voltage is checked cyclically. The cyclic load test of the battery does not considerably shorten the battery life, instead it gives an early warning of weakened buffer capacity.

The status information "Battery OK" is available from the "BatteryInfo" system library function.

## **Battery change interval**

#### Caution!

The battery should be changed every 4 years. The change intervals refer to the average service life and operating conditions and are recommended by B&R. It is not the maximum buffer duration.

## Information:

Data stored in the AC141 RAM will be lost if the battery is changed with the PLC switched off! The battery can be changed with power applied, but this is not allowed in all countries!

## Warning!

The battery must be replaced by a Type CR2477N Renata battery only. The use of another battery may present a risk of fire or explosion.

The battery may explode if handled improperly. Do not recharge, disassemble or dispose of in fire.

#### Procedure for changing the battery

- 1. Touch the mounting rail or ground connection (not the power supply!) in order to discharge any electrostatic charge from your body.
- 2. Remove the cover from the lithium battery holder using a screwdriver.



Figure 1: Remove the cover for the lithium battery

Remove the battery from the holder by pulling the removal strip (don't use uninsulated tools because of risk of short circuiting). The battery should not be held by its edges. Insulated tweezers may also be used for removing the battery.

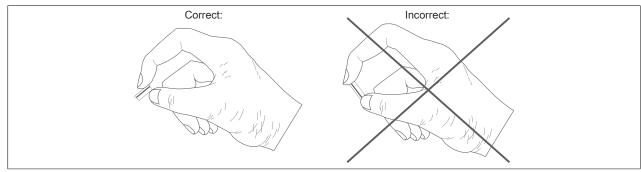


Figure 2: Hold the battery correctly

4. Insert the new battery with correct polarity. The removal strip should be pulled to the right of the battery holder and the "+" side of the battery should be facing left. In order to be able to remove the battery again in future, the removal strip **must be on the right side** of the battery.

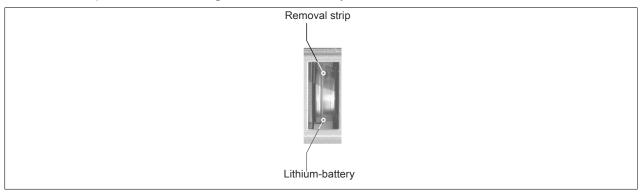


Figure 3: Removal strip should be pulled to the right

- 5. Now wrap the end of the removal strip over the top of the battery and insert it underneath the battery so that it does not protrude from the battery holder.
- 6. Replace cover. Insert the lower edge of the cover in the battery holder opening. Press the upper end of the cover home firmly.

#### Information:

Lithium batteries are considered hazardous waste. Used batteries should be disposed of appropriately.

# 12 Input/Output registers

#### Digital in r/- (16-bit):

Bit no.	Value	Description
0		Logical status of digital I/O 1
1		Logical status of digital I/O 2
2		Logical status of digital I/O 3
3 - 15		Reserved

#### Digital out r/w (16-bit):

All reserved bits must be written with 0.

Bit no.	Value	Description
0	0	Digital output 1 inactive
	1	Digital output 1 active
1	0	Digital output 2 inactive
	1	Digital output 2 active
2	0	Digital output 3 inactive
	1	Digital output 3 active
3 - 15		Reserved

## Analog in (16-bit) r/-:

±10V (12 bit resolution)

#### Counter (32-bit) r/(w):

In addition to the typical counter modes, this counter has a "Stepper motor counter mode" (see Configuration register bits 4-6).

In stepper motor counter mode, the count direction is set using digital I/O 2 (0...increment, 1...decrement), and the counter clock is on digital I/O 1. Only one clock edge is used for counting (can be configured with bit 3 of the counter configuration register).

#### Counter configuration (16 bit) r/w:

All reserved bits must be written with 0.

Bit no.	Value	Description
0		Reserved
1	0	AB(R) counter mode: R input disabled
	1	AB(R) counter mode: R input enabled
2		Reserved
3	0	Measurement starts at increasing edge
	1	Measurement starts at falling edge
4 - 6	000	No counter operation
	001	AB(R) counter mode
	010	Event counter mode
	011	Period measurement mode
	100	stepper motor counter mode
	101	Gate measurement mode
	110	Not allowed
	111	Not allowed
7 - 8	00	Counter frequency 4MHz
	01	External counter frequency
	10	Counter frequency 31.25kHz
	11	Not allowed
9	0	Counter overflow recognition disabled / Reset counter overflow bit
	1	Overflow recognition of the continuous counter is enabled (value limited to \$FFFF)
10 - 14		Reserved
15	0	Time / counter reset
	1	Time / counter enabled (ATTENTION: Only set bit after counter configuration is complete)

#### Status (16 Bit) r/-:

•	,	
Bit no.	Value	Description
0 - 8		Reserved
9	0	Period or gate measurement within the counter range 0 - \$FFFF (only valid if bit 9 is set in the counter configuration word)
	1	Counter overflow during period or gate measurement. Acknowledge by resetting bit 9 of the counter configuration word
10 - 14		Reserved
15	0	Output supply voltage monitoring 24 VDC - OK
	1	Output supply voltage monitoring 24 VDC error

# 13 Wiring

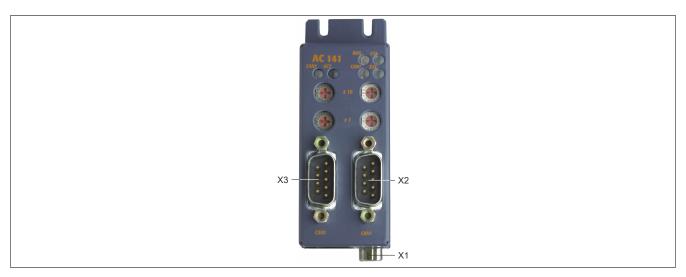


Figure 4: Overview of AC141 connections (view from front)

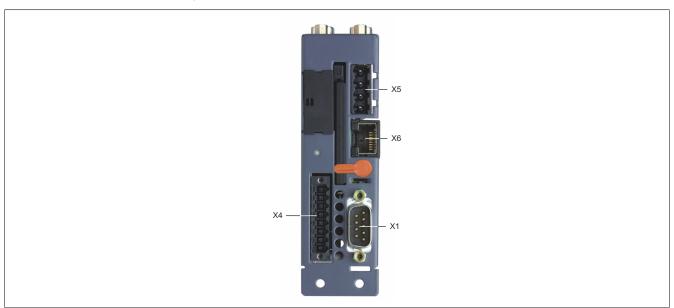


Figure 5: Overview of AC141 connections (view from below)

# 13.1.1 X1 - Pinout (application interface IF1 - RS232)

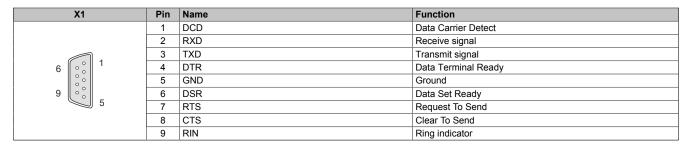


Table 9: X1 connector (RS232) - Pinout

## 13.1.2 X2 - Pinout (application interface IF2 - CAN1)

X2	Pin	Name	Function
	1		
	2	CAN_L	CAN low
	3	CAN_GND	CAN 0 V
6 6 6 1	4		
	5		
9 000	6		
5	7	CAN_H	CAN high
	8		
	9		

Table 10: X2 connector (CAN1) - Pinout

## 13.1.3 X3 - Pinout (application interface IF3 - CAN2)

Х3	Pin	Name	Function
	1		
	2	CAN_L	CAN low
	3	CAN_GND	CAN 0 V
6   0 0 1	4		
	5		
9     ° °   _	6		
5	7	CAN_H	CAN high
	8		
	9		

Table 11: X3 connector (CAN2) - Pinout

#### 13.1.4 X4 connector (inputs/outputs) - Pinout

X4	Pin	Name	Function in incremental counter mode	Function in period/gate mea- surement mode	Function in stepper motor counter mode
	1	GND		GND	
	2	+24 VDC		Dig. supply I/O +24V 1)	
	3	Digital I/O 1	A	Count	er input
	4	Digital I/O 2	В		Counting direction
5	5	Digital I/O 3	R	External clock	
	6	Shield		Shield	
	7	Analog I +		Analog Input +	
	8	Analog I -		Analog Input -	

Table 12: X4 connector (inputs/outputs) - Pinout

1) The +24 V supply is only necessary for digital I/O 1 .. 3.

## 13.1.5 X5 - Pinout (application interface IF4 - X2X)

X5	Pin	Name	Function
	1	X2X	X2X data
	2	X2X⊥	X2X ground
2	3	X2X\	X2X data inverted
3	4	SHLD	Shield
4			

Table 13: X5 connector (X2X) - Pinout

## 13.1.6 X6 - Pinout (application interface IF6 - Ethernet)

X6	Pin	Name	Function
1	1	RXD	Receive signal
	2	RXD\	Receive signal inverted
	3	TXD	Transmit signal
	4	Termination	Termination
	5	Termination	Termination
	6	TXD\	Transmit signal inverted
	7	Termination	Termination
	8	Termination	Termination

Table 14: X6 connector (Ethernet) - Pinout